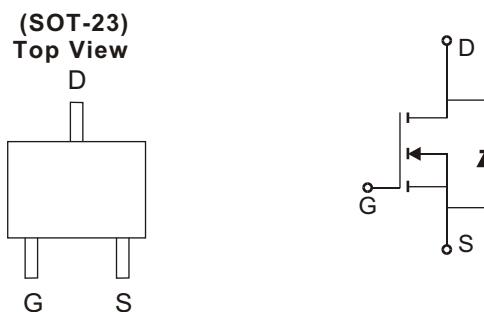


N-Channel Enhancement Mode MOSFET

FEATURES

1. 30V/4A, $R_{DS(ON)}=37m\Omega$ @ $V_{GS}=10V$
2. 30V/3.5A, $R_{DS(ON)}=49m\Omega$ @ $V_{GS}=4.5V$
3. Super high density cell design for extremely low $R_{DS(ON)}$
4. Exceptional on-resistance and maximum DC current capability

PIN CONFIGURATION (SOT-23)



PIN DESCRIPTION

Pin	Symbol	Description
1	G	Gate
2	S	Source
3	D	Drain

Absolute Maximum Ratings (TA=25°C Unless Otherwise Noted)

Parameter	Symbol	5 secs	Steady State	Units
Drain-Source Voltage	V_{DS}		30	V
Gate-Source Voltage	V_{GS}		± 20	V
Continuous Drain Current($T_J=150^{\circ}C$) ^{a,b}	I_D	4.0	3.16	A
		3.5	2.7	
Pulsed Drain Current	I_{DM}		20	A
Continuous Source Current (Diode Conduction) ^{a,b}	I_S	1.04	0.62	A
Power Dissipation ^{a,b}	P_D	1.25	0.75	W
		0.8	0.48	
Operating Junction Temperature	T_J	-55 to 150		°C

Thermal Resistance Ratings

Parameter	Symbol	Limits		Units
		Typ	Max	
Maximum Junction-to-Ambient ^a	R_{thJA}	80	100	°C/W
		130	166	
Maximum Junction-to-Foot(Drain) Steady-State	R_{thJF}	60	75	°C/W

Notes

a. Surface Mounted on FR4 Board, $t \leq 5$ sec.

b. Pulse width limited by maximum junction temperature.

N-Channel Enhancement Mode MOSFET

Electrical Characteristics ($T_J = 25^\circ\text{C}$ Unless Specified)

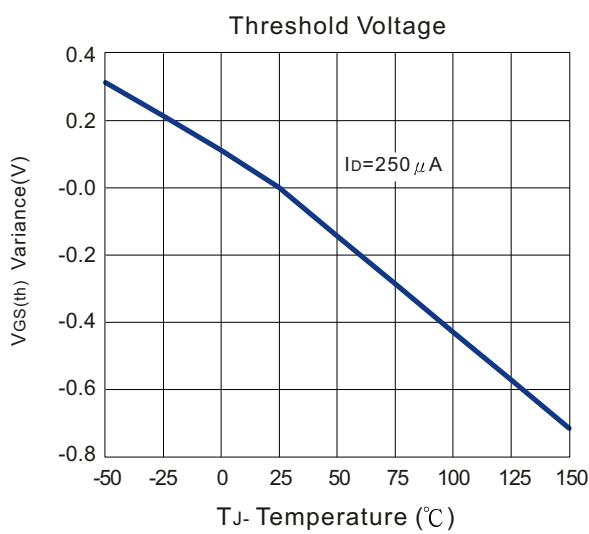
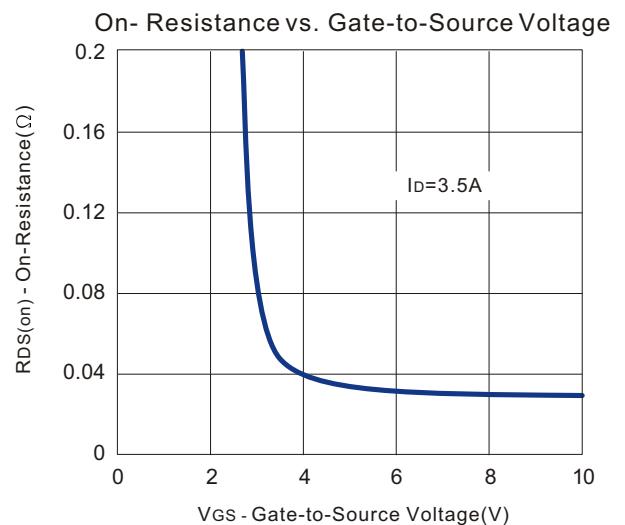
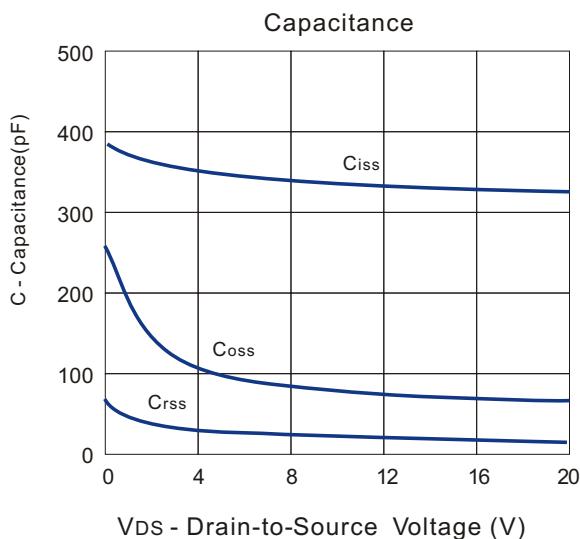
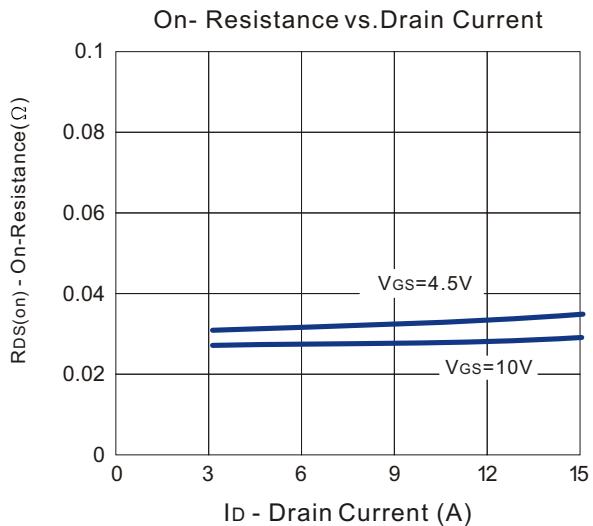
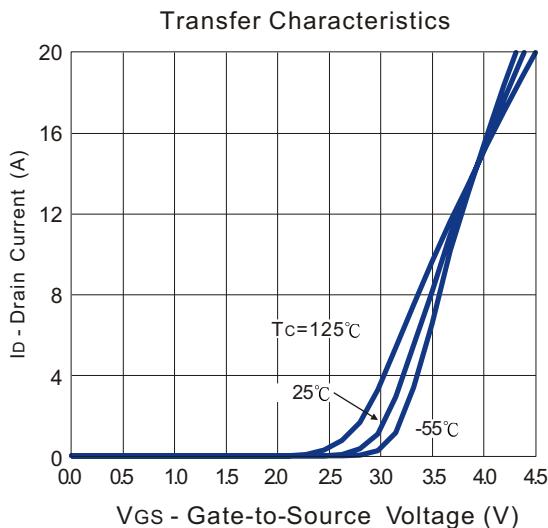
Symbol	Parameter	Conditions	Min	Typ	Max	Units	
STATIC PARAMETERS							
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{V}, I_D = 10\ \mu\text{A}$	30			V	
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\ \mu\text{A}$	1.0	1.21	3.0		
I_{GSS}	Gate-Body Leakage	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$			± 100	nA	
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 30\text{V}, V_{GS} = 0\text{V}$			0.5	μA	
		$V_{DS} = 30\text{V}, V_{GS} = 0\text{V}$ $T_J = 55^\circ\text{C}$			10		
$I_{D(\text{ON})}$	On-State Drain Current ^a	$V_{DS} \geq 4.5\text{V}, V_{GS} = 10\text{V}$	6			A	
$R_{DS(\text{ON})}$	Drain-Source On-Resistance ^a	$V_{GS} = 10\text{V}, I_D = 4.0\text{A}$		28	37	$\text{m}\Omega$	
		$V_{GS} = 4.5\text{V}, I_D = 3.5\text{A}$		36	49		
V_{SD}	Diode Forward Voltage	$I_S = 1.25\text{A}, V_{GS} = 0\text{V}$		0.8	1.2	V	
DYNAMIC PARAMETERS							
Q_g	Total Gate Charge	$V_{DS} = 15\text{V}, V_{GS} = 10\text{V}, I_D = 2.5\text{A}$		10.6	15	nC	
Q_{gs}	Gate Source Charge			3.2			
Q_{gd}	Gate-Drain Charge			1			
R_g	Gate Resistance	$f = 1.0\text{MHz}$		0.72		Ω	
$t_{d(on)}$	Turn-On Time	$V_{DD} = 15\text{V}, R_L = 15\Omega$ $I_D = 1\text{A}, V_{GEN} = 10\text{V}$ $R_G = 6\Omega$		7.4	15	nS	
t_r				13.2	20		
$t_{d(off)}$	Turn-Off Time			21.6	31		
t_f				3.5	9		

Notes

a. Pulse test: $PW \leq 300\ \mu\text{s}$ duty cycle $\leq 2\%$.

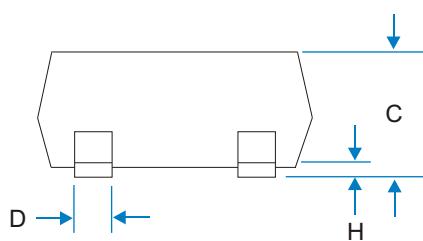
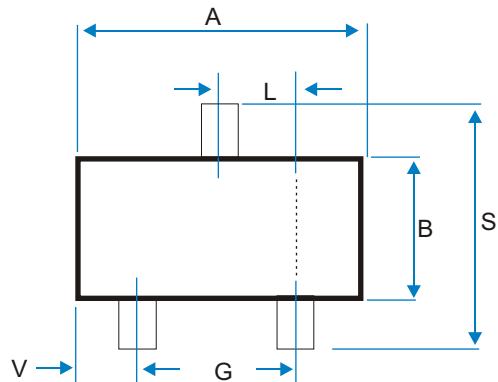
N-Channel Enhancement Mode MOSFET

Typical Characteristics ($T_J = 25^\circ\text{C}$ Noted)



N-Channel Enhancement Mode MOSFET

SOT-23 Package Outline



DIM	MILLIMETERS	
	MIN	MAX
A	2.80	3.1
B	1.20	1.7
C	0.89	1.3
D	0.37	0.50
G	1.78	2.04
H	0.013	0.15
J	0.085	0.2
K	0.45	0.7
L	0.89	1.02
S	2.10	3
V	0.45	0.60
L1	0.2	0.6

