SON5-R-0.50

Weight: 0.003 g (typ.)

4 OUT Y

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SZU04AFE

Inverter

Features

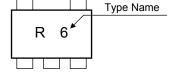
High output drive: ±16 mA (min.) @VCC = 4.5 V

- Low quiescent power: $I_{CC} < 2 \mu A (max)$
 - $@V_{CC} = 5.5 \text{ V}, \text{ Ta} = 25^{\circ}\text{C}$
- Operation voltage range: VCC = 1.8~5.5 V
- Supply voltage data retention: $V_{CC} = 1.5 \sim 5.5 \text{ V}$
- Latch-up performance: ±500 mA or higher
- ESD performance: Human body model > ±2000 V Machine model > ±200 V
- Power down protection is provided on all inputs.

Marking



GND 3



Characteristics	Symbol <	Rating	Unit
Supply voltage range	Vcc	-0.5~6	V
DC input voltage	VIN	-0.5~6	V
DC output voltage	V _{OUT}	-0.5~V _{CC} + 0.5	V
Input diode current	I IK	-20	mA
Output diode current	HOK	±20	mA
DC output current	TUOH	±50	mA
DC V _{CC} /ground current	loc	±50	mA
Power dissipation	PD	150	mW
Storage temperature	∕ ⊤ _{stg}	-65~150	°C
Lead temperature (10 s)	ΤL	260	°C

Absolute Maximum Ratings (Ta = 25°C)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

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Truth Table

Logic Diagram





Operating Ranges

Characteristics	Symbol	Rating
Supply voltage	Vee	1.8~5.5
	V _{CC}	1.5~5.5 (Note 1)
Input voltage	V _{IN}	0~5.5 (V
Output voltage	V _{OUT}	0~V _{CC} V
Operating temperature	T _{opr}	-40~85 C

Note 1: Data retention only.

Electrical Characteristics

DC Characteristics

				21			$(\subset$		-		
		Test			Та		Га = 25°С	a=25°C		Ta = -40~85°C	
Characteristics Symbol Circuit			Vcc (V)	Min	(тур.)	Max	Min	Max	Unit		
High-level input voltage			400	1.8	0.85 × V _{CC}		_	$\begin{array}{c} 0.85 \\ \times \ V_{CC} \end{array}$	_	V	
		(\bigcirc)		2.3~5.5	0.8 ×VCC)]_		$0.8 \\ \times V_{CC}$	_	,	
Low-level input VIL -		(1.8	_		0.15 × V _{CC}	—	$\begin{array}{c} 0.15 \\ \times V_{CC} \end{array}$	v	
	(J	2.3~5.5	> -		$0.2 \\ \times V_{CC}$	_	$^{0.2}_{\timesV_{CC}}$		
	6	\sim $^{\prime}$	(\bigcirc)	G	1.8	1.6	1.8	_	1.6	—	V
			V _{IN} =	IOH = -100 μA	2.3	2.1	2.3	_	2.1	—	
		$\langle \langle -$	∀IL	I _{OH} = -100 μA	3.0	2.7	3.0	_	2.7	—	
High-level	V _{OH}	\geq			4.5	4.0	4.4	_	4.0	—	
output voltage	>	VIN = GND	I _{OH} = -4 mA	2.3	1.9	2.14	_	1.9	—	V	
			l _{OH} = −8 mA	3.0	2.4	2.75	_	2.4	—		
			(_{OH} = −12 mA	3.0	2.3	2.61	_	2.3	—		
	\bigcirc			1 _{OH} = −16 mA	4.5	3.8	4.13	_	3.8	—	
			VIN =	l _{OL} = 100 μA	1.8		0	0.2		0.2	-
Low-level output voltage]				2.3		0	0.2		0.2	
		VIN = VCC	ΙΟΕ - 100 μΑ	3.0	—	0	0.3		0.3	V	
				4.5	—	0	0.5		0.5		
			$I_{OL} = 4 \text{ mA}$	2.3	—	0.1	0.3		0.3		
			I _{OL} = 8 mA	3.0	—	0.17	0.4		0.4		
			I _{OL} = 12 mA	3.0		0.25	0.55		0.55		
			I _{OL} = 16 mA	4.5		0.26	0.55		0.55		
Input leakage current	I _{IN}	_	$V_{IN} = 5.5 V \text{ or GND}$		0~5.5	—	_	±1	—	±10	μA
Quiescent supply current	ICC	_	V _{IN} = V _C	$V_{IN} = V_{CC}$ or GND		_	_	2	_	20	μΑ

AC Characteristics (Unless otherwise specified, input: $t_r = t_f = 3$ ns)

Characteristics Symbol	Test	Test Condition		Ta = 25°C			Ta = -40~85°C		Unit	
	Circuit		V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit	
Propagation delay ^t PLH time ^t PHL				1.8	1.0		8.5	1.0	9.0	
		$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	2.5 ± 0.2	0.8		6.2	0.8	6.5	ns	
			$\textbf{3.3}\pm\textbf{0.3}$	0.5	_ <	4.5	0.5	4.8		
	t _{PHL}	_		5.0 ± 0.5	0.5		3.9	0.5	4.1	115
		C _L = 50 pF,	$\textbf{3.3}\pm\textbf{0.3}$	1.0		6.0))1.0	6.5		
			$R_L = 500 \Omega$	5.0 ± 0.5	0.8	-60	5.0	0.8	5.5	
Input capacitance	CIN		—	0~5.5	\geq	5	\mathcal{A}	_	_	pF
Power dissipation capacitance C _{PD} –		(Note)	3.3	-((9		_		рF	
	CPD		(NOLE)	5.5		25)		_		μr

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation,

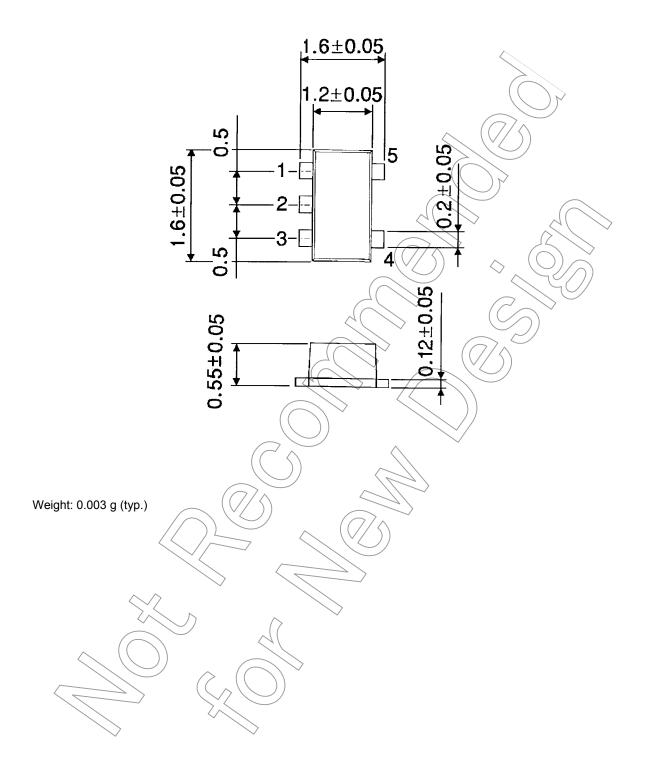
 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

TOSHIBA

Package Dimensions

SON5-P-0.50

Unit : mm



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