TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SZ00AFE

2 Input NAND Gate

Features

- High output drive: ±24 mA (min) at V_{CC} = 3 V •
- Super high speed operation: $t_{PD} = 2.4$ ns (typ.)
- Operation voltage range: V_{CC (opr)} = 1.8~5.5 V ٠
- Supply voltage data retention: V_{CC} = 1.5~5.5 V
- 5.5-V tolerant inputs.
- Matches the performance of TC74LCX series when operated at 3.3-V V_{CC}

at V_{CC} = 5 V, 50 pF SON5-P-0.50 (ESV) Weight: 0.003 g (typ.) Pin Assignment (top view) Product name IN B 1 R IN A 2 4 OUT Y GND 3

Marking

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage range	Vcc	_0.5~6	V
DC input voltage	VIN	-0.5~6	V
DC output voltage	VOUT	-0.5~V _{CC} + 0.5	V
Input diode current	<u> I</u> IK	-20	mA
Output diode current	lok	±20	mA
DC output current	Tyol	±50	mA
DC V _{CC} /ground current	tce	±50	mA
Power dissipation	PD	150	mW
Storage temperature	T _{stg}	-65~150	°C
Lead temperature (10 s)	ΤL	260	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

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Logic Diagram

Truth Table

N A		A B Y L L H					
&	OUT Y						
Ν В ——							
		$\langle (// 5)$					
perating Ranges							
Jeren							
Characteristics	Symbol	Rating					
		1.8~5.5					
Supply voltage	V _{CC}	1.575,5 (Note 1) V					
Input voltage	V _{IN}	0~5.5 V					
Output voltage	V _{OUT}						
Operating temperature		-40~85 / °C					
	T _{opr}						
log styles and fall time	al (al	$0 \sim 20 (V_{CC} = 1.8 \text{ V}, 2.5 \text{ V} \pm 0.2 \text{ V})$					
Input rise and fall time	d _t /d _v						
		0~5 (V _{CC} = 5.5 V ± 0.5 V)					
lote 1: Data retention only							
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Electrical Characteristics

DC Characteristics

Characteristics Symbol Test Condition			Ta = 25°C Ta = -40~85				0~85°C	Linit		
		Te	St Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
High-level input voltage			1.8	$0.75 \times V_{CC}$	_	\mathcal{A}	0.75 × V _{CC}	_	V	
			—	2.3-5.5	$0.7 \times V_{CC}$	—	$\left(\left(\right) \right)$	0.7 Vcc	—	v
Low-level input voltage	_	1.8	-<	-(0.25 × V _{CC}	_	0.25 × V _{CC}	V		
			2.3-5.5	(\geq	0.3 × V _{CC}			$0.3 \times V_{CC}$	
High-level V _{OH}				1.8	1.7	1.8) _	1.7	_	
		I _{OH} = -100 μA	2.3	2.2	2,3	_	2.2		V	
			3.0	2.9	3.0	_	2.9	\geq		
	V _{IN} = V _{IH} or V _{IL}		4.5 ((74.4	4.5	((4,4	$\geq -$		
		I _{OH} =8 mA	2.3	1.9	2.15	K	_1.9) -		
		I _{OH} = -16 mA	3.0	2.4	2.8		2.4			
		I _{OH} = -24 mA	3.0	2.3	2.68	$\langle \rangle$	2.3	_		
			$I_{OH} = -32 \text{ mA}$	4.5	3.8	4.2		3.8	_	
				1.8	_		0.1	_	0.1	
Low-level output voltage		I _{OL} = 100 μΑ	2.3		0	0.1	_	0.1	- V	
			3.0 <	\leq	9	0.1	_	0.1		
	V _{IN} = V _{IH}		4.5	X	/0	0.1	_	0.1		
	VIN = VIH	IoL=8 mA	2.3	_	√ 0.1	0.3	_	0.3		
		$I_{OL} = 16 \text{ mA}$	3.0	–	0.15	0.4	_	0.4		
		$I_{OL} = 24 \text{ mA}$	3.0	\rightarrow	0.22	0.55	_	0.55		
	$I_{OL} = 32 \text{ mA}$		4.5	> -	0.22	0.55	_	0.55		
Input leakage current	Тум	$V_{\rm IN} = 5.5$ V or GND $(0-5.5)$				±1		±10	μA	
Quiescent supply current	Icc	VIN = VCC	or GND	5.5	_	_	2	—	20	μA

AC Characteristics (Input: $t_r = t_f = 3 \text{ ns}$)

Characteristics Symbol	Test Condition		$Ta = 25^{\circ}C$			Ta = -40~85°C		Unit	
		V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit	
Propagation delay ^t PLH time tPHL	$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	1.8	2.0	4.5	9.5	2.0	10.0	ns	
		2.5 ± 0.2	0.8	3.0	6.5	0.8	7.0		
		$\textbf{3.3}\pm\textbf{0.3}$	0.5	2.4	4.5	0.5	4.7		
	t PHL		5.0 ± 0.5	0.5	2.0	3.9	0.5	4.1	115
	$\begin{array}{l} C_{L} = 50 \; pF, \\ R_{L} = 500 \; \Omega \end{array}$	$\textbf{3.3}\pm\textbf{0.3}$	1.5	2.9	5.0	1.5	5.2		
		5.0 ± 0.5	0.8	2.4	4.3	(0,8	4.5		
Input capacitance	CIN	—	0-5.5	_	4	\mathcal{A}	Ľ	_	pF
Power dissipation C _{PD}	(Note 2)	3.3	_	19 (-			~F	
		5.5	_	27	\mathbb{R}	_		pF	

Note2: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation

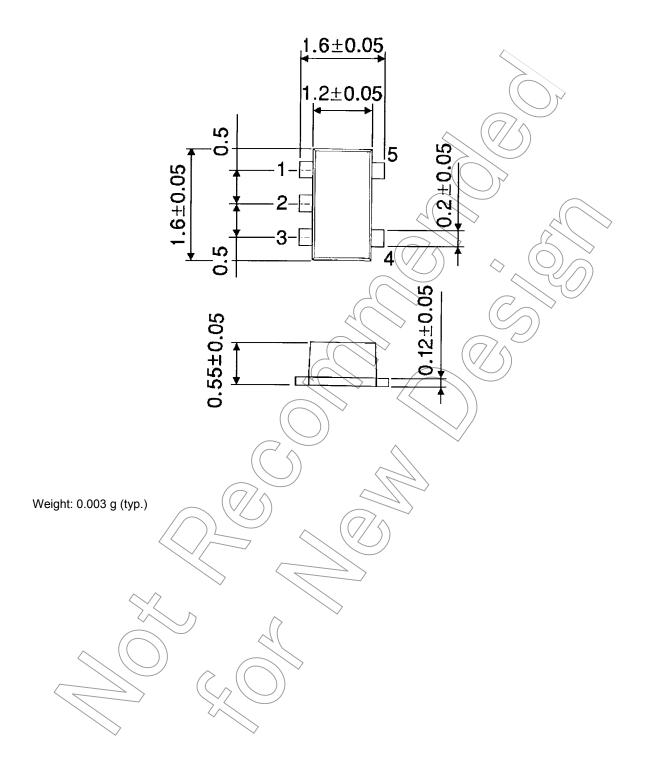
 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

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Package Dimensions

SON5-P-0.50

Unit : mm



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