MOSFETs Silicon P-Channel MOS (U-MOSVI)

TJ15P04M3

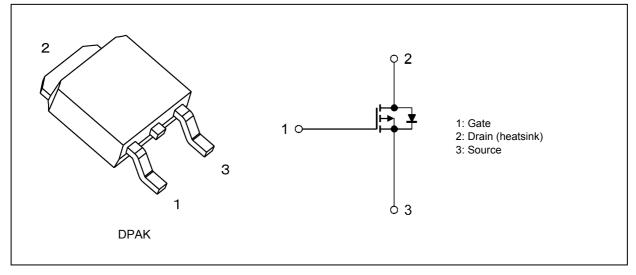
1. Applications

- DC-DC Converters
- Desktop Computers

2. Features

- (1) Low drain-source on-resistance: $R_{DS(ON)} = 28 \text{ m}\Omega$ (typ.) ($V_{GS} = -10 \text{ V}$)
- (2) Low leakage current: $I_{DSS} = -10 \ \mu A \ (max) \ (V_{DS} = -40 \ V)$
- (3) Enhancement mode: V_{th} = -0.8 to -2.0 V (V_{DS} = -10 V, I_{D} = -0.1 mA)

3. Packaging and Internal Circuit



4. Absolute Maximum Ratings (Note) ($T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics			Rating	Unit
Drain-source voltage		V _{DSS}	-40	V
Gate-source voltage		V _{GSS}	±20	
Drain current (DC)	(Note 1)	Ι _D	-15	A
Drain current (pulsed)	(Note 1)	I _{DP}	-45	
Power dissipation $(T_c = 25^{\circ}C)$		PD	29	W
Single-pulse avalanche energy	(Note 2)	E _{AS}	29	mJ
Avalanche current		I _{AR}	-15	A
Channel temperature		T _{ch}	150	°C
Storage temperature		T _{stg}	-55 to 150	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

5. Thermal Characteristics

Characteristics	Symbol	Max	Unit
Channel-to-case thermal resistance	R _{th(ch-c)}	4.3	°C/W
Channel-to-ambient thermal resistance	R _{th(ch-a)}	125	

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: V_{DD} = -32 V, T_{ch} = 25°C (initial), L = 100 μ H, R_G = 25 Ω , I_{AR} = -15 A

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

6. Electrical Characteristics

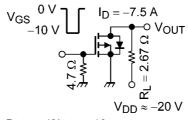
6.1. Static Characteristics (T_a = 25°C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I _{GSS}	V_{GS} = ±20 V, V_{DS} = 0 V	_	_	±0.1	μA
Drain cut-off current	I _{DSS}	V _{DS} = -40 V, V _{GS} = 0 V	-	—	-10	
Drain-source breakdown voltage	V _{(BR)DSS}	I _D = -10 mA, V _{GS} = 0 V	-40	_	_	V
Drain-source breakdown voltage (Note 3)	V _{(BR)DSX}	I _D = -10 mA, V _{GS} = 10 V	-30	—	—	
Gate threshold voltage	V _{th}	V _{DS} = -10 V, I _D = -0.1 mA	-0.8	_	-2.0	
Drain-source on-resistance	R _{DS(ON)}	V _{GS} = -4.5 V, I _D = -7.5 A	_	37	48	mΩ
		V _{GS} = -10 V, I _D = -7.5 A	_	28	36	

Note 3: If a reverse bias is applied between gate and source, this device enters V_{(BR)DSX} mode. Note that the drainsource breakdown voltage is lowered in this mode.

6.2. Dynamic Characteristics ($T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C _{iss}	V _{DS} = -10 V, V _{GS} = 0 V, f = 1 MHz	_	1100	_	pF
Reverse transfer capacitance	C _{rss}		_	130	—	
Output capacitance	C _{oss}		_	170	_	
Switching time (rise time)	tr	See Figure 6.2.1.	_	11	_	ns
Switching time (turn-on time)	t _{on}		_	19	—	
Switching time (fall time)	t _f		_	42	—	
Switching time (turn-off time)	t _{off}		_	170		



Duty \leq 1%, $t_W =$ 10 μs



6.3. Gate Charge Characteristics ($T_a = 25^{\circ}C$ unless otherwise specified)

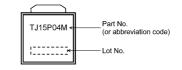
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	$V_{DD}\approx -32 \text{ V}, \text{ V}_{GS} = -10 \text{ V}, \text{ I}_{D} = -15 \text{ A}$	—	26	—	nC
Gate-source charge 1	Q _{gs1}		_	6.7	_	
Gate-drain charge	Q _{gd}			2.5		

6.4. Source-Drain Characteristics ($T_a = 25^{\circ}C$ unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (pulsed) (Note 4)	I _{DRP}	—	_	_	-45	А
Diode forward voltage	V _{DSF}	I _{DR} = -15 A, V _{GS} = 0 V	—	_	1.2	V

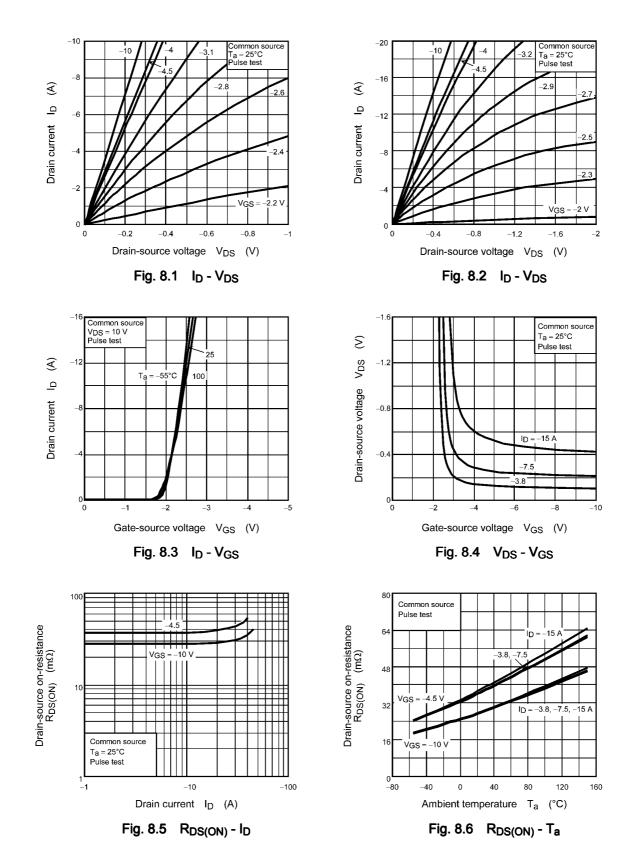
Note 4: Ensure that the channel temperature does not exceed 150°C.

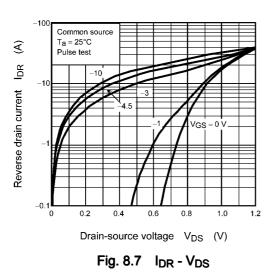
7. Marking

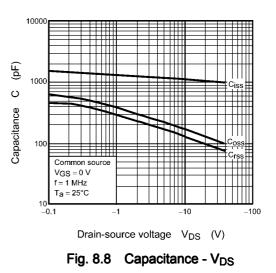




8. Characteristics Curves (Note)







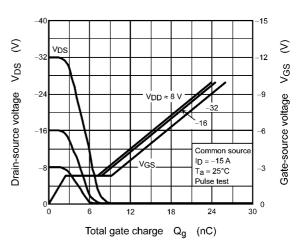
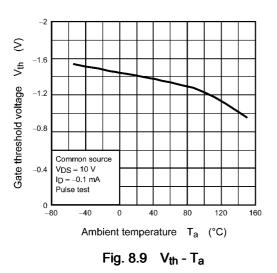
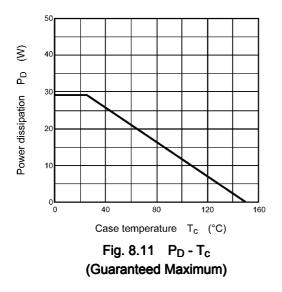


Fig. 8.10 Dynamic Input/Output Characteristics





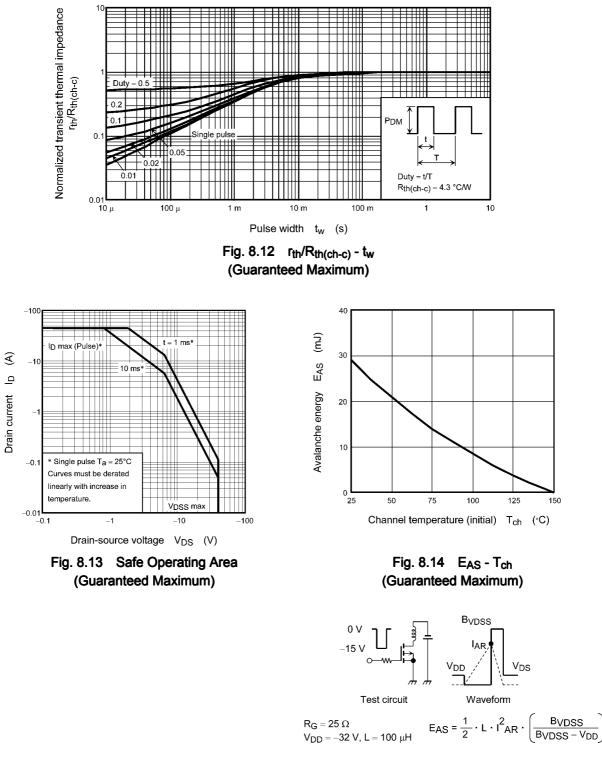


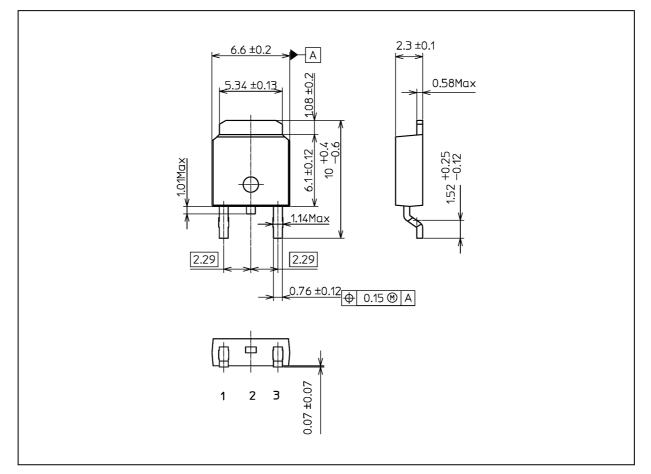
Fig. 8.15 Test Circuit/Waveform

Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

TJ15P04M3

Package Dimensions

Unit: mm



Weight: 0.36 g (typ.)

	Package Name(s)
TOSHIBA: 2-7K1S	
Nickname: DPAK	

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