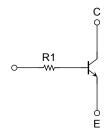
TOSHIBA Transistor Silicon NPN Epitaxial Type (PCT Process) (Transistor with Built-in Bias Resistor)

## **RN1910AFS, RN1911AFS**

Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

- Two devices are incorporated into a fine-pitch, small-mold (6-pin) package.
- Incorporating a bias resistor into a transistor reduces the parts count.
   Reducing the parts count enables the manufacture of ever more compact equipment and lowers assembly costs.
- Complementary to the RN2910AFS/RN2911AFS

### **Equivalent Circuit and Bias Resistor Values**



Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

		( ) \ )	
Characteristic	Symbol	Rating	Unit
Collector-base voltage	V <sub>CBO</sub>	50	V
Collector-emitter voltage	VCEO		V
Emitter-base voltage	V <sub>EBO</sub>	5	N/
Collector current	lc lc	80	Am
Collector power dissipation	Pc (Note 1)	50 <	_mW\
Junction temperature	( <u>)</u>	150	> °C >
Storage temperature range	T <sub>stg</sub>	-55~150	√,%c

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

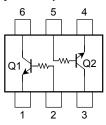
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

Note 1: Total rating

### Unit: mm 1.0±0.05 0.8±0.05 0.1±0.05 0.1±0,05 15±0.05 $1\pm 0.05$ (E1) 1. EMITTER (B1) 2. BASÉ1 3. COLLECTOR2 (C2)(E2) 4. EMITTER2 (B2)5) BASE2 ⁄ø. COLLECTOR1 (C1) fS6 / JEDEC JEITA TOSHIBA 2-1F1D

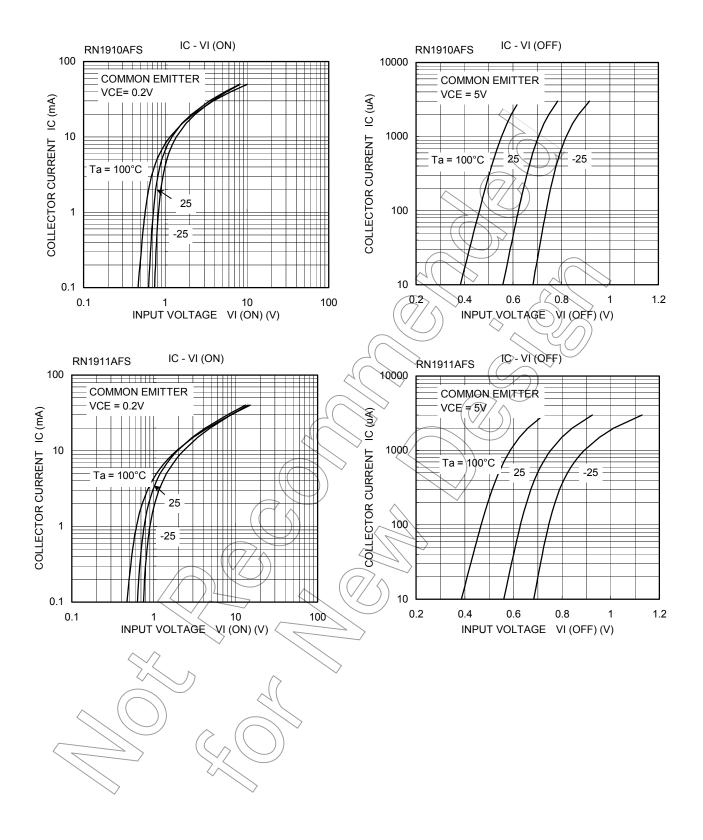
Weight: 0.001 g (typ.)

# Equivalent Circuit (top view)

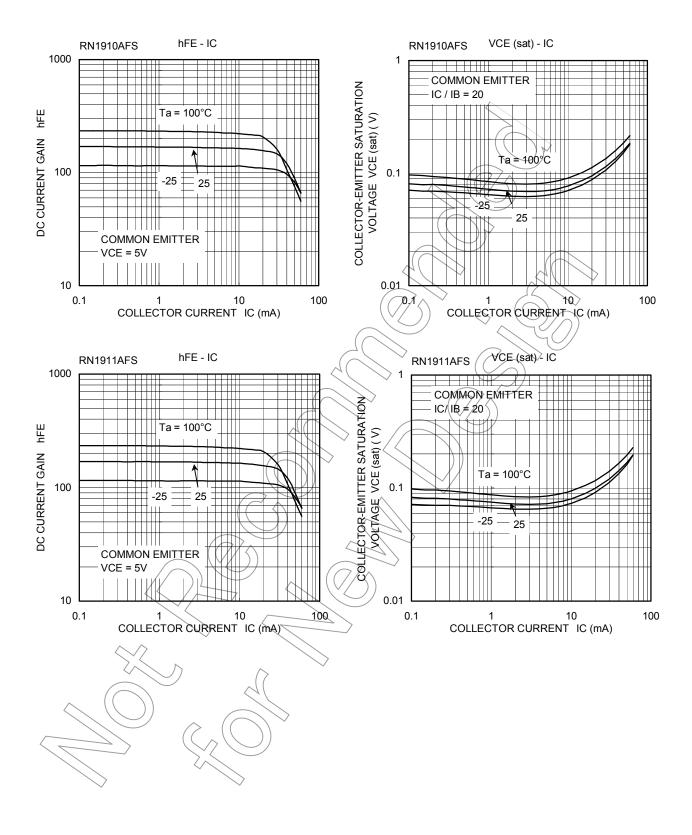


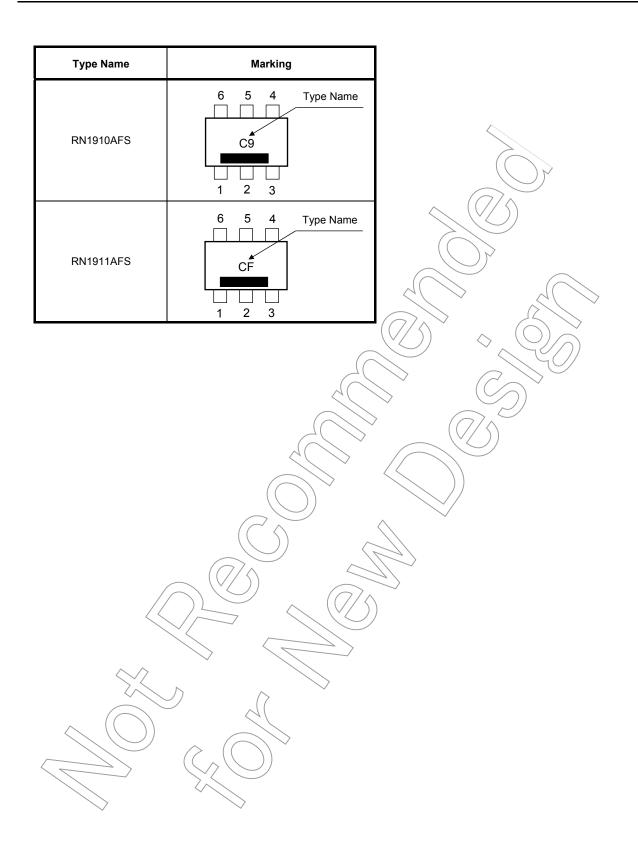
### Electrical Characteristics (Ta = 25°C) (Q1, Q2 common)

Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cutoff current		I <sub>CBO</sub>	V <sub>CB</sub> = 50 V, I <sub>E</sub> = 0	_	_	100	nA
Emitter cutoff current		I <sub>EBO</sub>	$V_{EB} = 5 \text{ V}, I_{C} = 0$	_	_	100	nA
DC current gain		h <sub>FE</sub>	$V_{CE} = 5 \text{ V}, I_{C} = 1 \text{ mA}$	120	_	700	
Collector-emitter saturation	voltage	V <sub>CE</sub> (sat)	$I_C = 5 \text{ mA}, I_B = 0.25 \text{ mA}$	_	_	0.15	V
Collector output capacitance		C <sub>ob</sub>	V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0, f = 1 MHz	_	0.7	_	pF
Input resistor	RN1910AFS	- R1	_	3.76	4.7	5.64	kΩ
	RN1911AFS			8	10	12	



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