TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SHU04FE

INVERTER (Un-Buffer)

Features

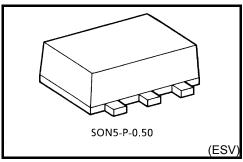
• Super high speed operation : t_{pd} = 3.5ns (typ.)

 $@V_{CC} = 5V, C_L = 15pF$

Low power dissipation : I_{CC} = 2µA (max) @Ta = 25°C
 High noise immunity : V_{NIH} = V_{NIH} = 10% V_{CC} (min)

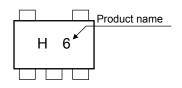
• 5.5V tolerant input.

Wide operation voltage range: V_{CC} = 2 to 5.5 V

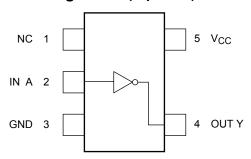


Weight: 0.003 g (typ.)

Marking



Pin Assignment (top view)



Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	–0.5 to 7	V
DC input voltage	V _{IN}	-0.5 to 7	V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V
Input diode current	lik	-20	mA
Output diode current	I _{OK}	±20 (Note1)	mA
DC output current	I _{OUT}	±25	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	P_{D}	150	mW
Storage temperature	T _{stg}	-65 to 150	°C

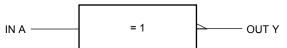
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: V_{OUT} < GND, V_{OUT} > V_{CC}

IEC Logic Symbol

______Г



Α	Υ
L	Н
Н	L

Truth Table

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	V_{CC}	2 to 5.5	V
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature	T _{opr}	-40 to 85	°C

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Electrical Characteristics

DC Characteristics

Characteristics Symbol		Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit
				V _{CC} (V)	Min	Тур.	Max	Min	Max	Ullit
High-level input voltage V _{IH}		_		2.0	1.7	_	_	1.7	_	V
				3.0 to 5.5	V _{CC} × 0.8	_	_	V _{CC} × 0.8	_	
		_		2.0	_	_	0.3	_	0.3	V
Low-level input voltage	V _{IL}			3.0 to 5.5	_		V _{CC} × 0.2	_	V _{CC} × 0.2	
	Vон	$V_{IN} = V_{IL}$	Ι _{ΟΗ} = -50 μΑ	2.0	1.8	2.0	_	1.8	_	V
High-level output voltage				3.0	2.7	3.0	_	2.7	_	
				4.5	4.0	4.5	_	4.0	_	
		V _{IN} =GND	$I_{OH} = -4 \text{ mA}$	3.0	2.58	_	_	2.48	_	
			$I_{OH} = -8 \text{ mA}$	4.5	3.94	_	_	3.80	_	
Low-level output voltage		$V_{IN} = V_{IH}$	I _{OL} = 50 μA	2.0	_	0	0.2	_	0.2	
				3.0		0	0.3	_	0.3	
	V _{OL}			4.5		0	0.5	_	0.5	
		V _{IN} =V _{CC}	$I_{OL} = 4 \text{ mA}$	3.0	_	_	0.36	_	0.44	
			$I_{OL} = 8 \text{ mA}$	4.5	_	_	0.36	_	0.44	
Input leakage current	I _{IN}	$V_{IN} = 5.5 \text{ V}$	or GND	0 to 5.5		_	±0.1	_	±1.0	μΑ
Quiescent supply current	Icc	$V_{IN} = V_{CC}$	or GND	5.5	_	_	2.0	_	20.0	μΑ

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AC Characteristics (unless otherwise specified, input: $t_r = t_f = 3$ ns)

Characteristics	Symbol		Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
			V _{CC} (V)	C _{L (} pF)	Min	Тур.	Max	Min	Max	Offic
Propagation delay time	t _P LH t _P HL	3.3 ± 0.3	15	_	5.0	8.9	1.0	10.5		
			3.5 ± 0.5	50	_	7.5	11.4	1.0	13.0	- ns
			5.0 ± 0.5	15	_	3.5	5.5	1.0	6.5	
				50	_	5.0	7.0	1.0	8.0	
Input capacitance	C _{IN}				_	5	10	_	10	pF
Power dissipation capacitance	C _{PD}			(Note 2)	_	6		_	_	pF

Note 2: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

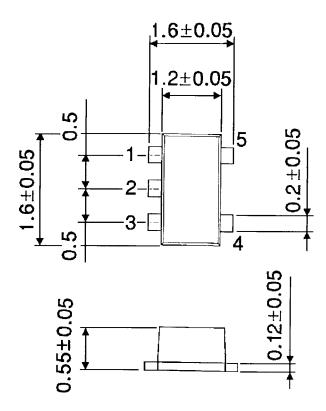
Average operating current can be obtained by the equation.

$$I_{CC \text{ (opr)}} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$



Package Dimensions

SON5-P-0.50 Unit: mm



Weight: 0.003 g (typ.)

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