

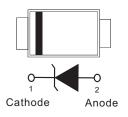
ESD PROTECTION DIODES

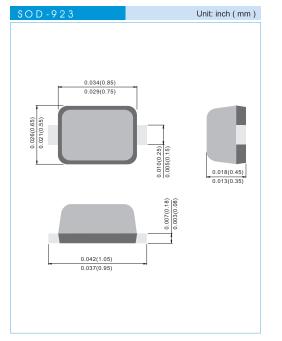
FEATURES

- IEC61000-4-2 Level 4 ESD Protection
- In compliance with EU RoHS 2002/95/EC directives

MECHANICAL DATA

Case: SOD-923, Plastic Terminals: Solderable per MIL-STD-750, Method 2026 Polarity : Color band cathode Approx.Weight : 0.0004gram





MAXIMUM RATINGS

Rating	Symbol	Value	Units
Peak Pulse Power Dissipation (8/20µs Waveform)	Ррр	40	W
Lead Solder Temperature-Maximum (10 Second Duration)	T∟	260	°C
Operating Junction and Storage Temperature Range	TJ,TSTG	-55 to + 150	°C

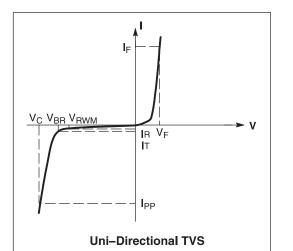
Stresses exceeding Maximum Ratings may damage the device. Maximum Rating are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Note : 1.FR-4 = 70 x 60 x 1mm.



ELECTRICAL CHARACTERISTICS (TA=25°C unless otherwise noted)

Parameter	Symbol
Maximum Reverse Peak Pulse Current	I pp
Clamping Voltage@I PP	Vc
Working Peak Reverse Voltage	Vrwm
Maximum Reverse Leakage Current@VRWM	l r
Breakdown Voltage @ I ⊤	Vbr
Test Current	lт
Forward Current	lf
Forward Voltage@I F	VF
Peak Pulse Power Dissipation	Ррр
Max.Capacitance@Vr=0 and f=1MHz	CJ



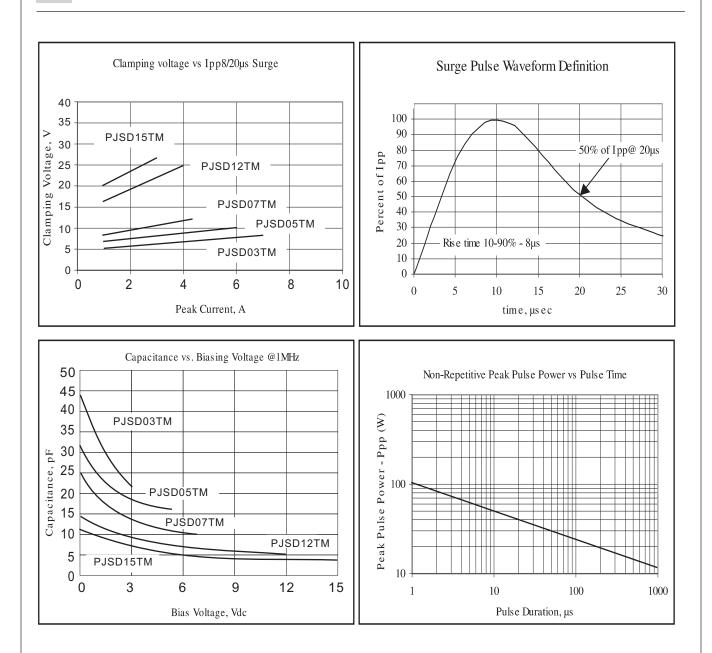
ELECTRICAL CHARACTERISTICS (TA=25°C unless otherwise noted, VF=1.1V Max.@I F=10mA)

Part Number	VRWM	Ir@Vrwm	V BR@I T (Note 2)	C J (Note 3)	Vc	I PP	Iτ	
	Max.	Max.	Min.	Max.	Max Per 8x20μs			Marking
	V	μA	V	рF	V	A	mA	
PJSD03TM	3.3	2.5	5.4	50	10	6	1.0	h
PJSD05TM	5	1	6.2	35	12	5	1.0	b
PJSD07TM	7	5	7.5	30	15	4	1.0	С
PJSD12TM	12	1	13.3	18	23	3	1.0	d
PJSD15TM	15	1	17.0	15	30	2	1.0	е

Note : 2.VBR is measured with a pulse test current IT at an ambinet temperature of 25°C

3.Capacitance at f=1MHz, VR=0V, TA=25°C

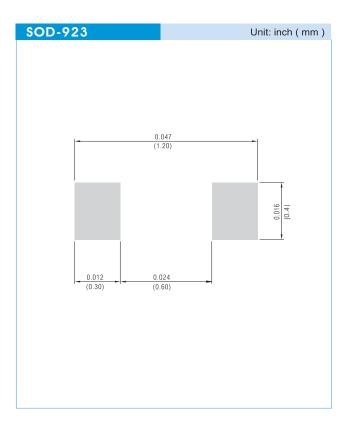








MOUNTING PAD LAYOUT



ORDER INFORMATION

Packing information
T/R - 8K per 7" plastic Reel

LEGAL STATEMENT

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