TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7WH08FC

### **Dual 2-Input AND Gate**

#### **Features**

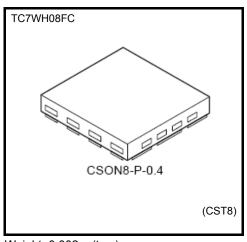
High speed operation : t<sub>pd</sub> = 4.3ns (typ.)

at  $V_{CC}$  = 5V,  $C_L$  = 15pF

 $\begin{array}{ll} \bullet & \text{Low power dissipation} & : I_{CC} = 2 \mu \text{A (max) at Ta} = 25 ^{\circ} \text{C} \\ \bullet & \text{High noise immunity} & : V_{NIH} = V_{NIL} = 28 \% \ V_{CC} \ (\text{min}) \\ \end{array}$ 

Operating voltage range : V<sub>CC</sub> = 2 to 5.5 V

• 5.5-V tolerant inputs



Weight: 0.002 g (typ.)

### Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	−0.5 to 7.0	V
DC input voltage	V <sub>IN</sub>	−0.5 to 7.0	V
DC output voltage	V <sub>OUT</sub>	-0.5 to V <sub>CC</sub> + 0.5 (Note1)	V
Input diode current	I <sub>IK</sub>	-20	mA
Output diode current	lok	±20 (Note2)	mA
DC output current	lout	±25	mA
DC V <sub>CC</sub> /GND current	Icc	±50	mA
Power dissipation	PD	150 (Note3)	mW
Storage temperature	T <sub>stg</sub>	-65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: High or Low State.

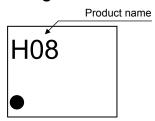
I<sub>OUT</sub> absolute maximum rating must be observed.

Note 2:  $V_{OUT} < GND$ ,  $V_{OUT} > V_{CC}$ 

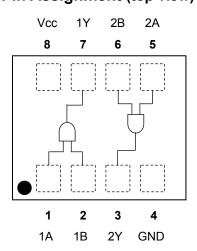
Note 3: Mounted on an FR4 board.

 $(25.4 \text{ mm} \times 25.4 \text{ mm} \times 1.6 \text{ t}, \text{ Cu Pad: } 11.56 \text{ mm}^2)$ 

### Marking



#### Pin Assignment (top view)





## IEC Logic Symbol



### **Truth Table**

Α	В	Y
L	L	L
L	Н	L
Н	L	L
Н	Н	Н

### **Operating Ranges**

Characteristics	Symbol	Rating	Unit	
Supply voltage	V <sub>CC</sub>	2.0 to 5.5	V	
Input voltage	V <sub>IN</sub>	0 to 5.5	V	
Output voltage	V <sub>OUT</sub>	0 to V <sub>CC</sub>	V	
Operating temperature	T <sub>opr</sub>	-40 to 85	°C	
Input rise and fall time	dt/dv	0 to 100 (V <sub>CC</sub> = 3.3 V $\pm$ 0.3 V)	ns/V	
input rise and fair time	avav	0 to 20 (V <sub>CC</sub> = 5.0 V $\pm$ 0.5 V)	115/ V	



### **Electrical Characteristics**

### **DC Characteristics**

Characteristic Symbol		Toot	Test Condition V <sub>CC</sub> (V)		Ta = 25°C			Ta = -40 to 85°C		Unit
		rest			Min	Тур.	Max	Min	Max	Unit
				2.0	1.5	_	_	1.5	_	V
High-level input voltage	V <sub>IH</sub>	V <sub>IH</sub> —		3.0 to 5.5	V <sub>CC</sub> × 0.7	_	_	V <sub>CC</sub> × 0.7		
				2.0		_	0.5	_	0.5	
Low-level input voltage	input voltage V <sub>IL</sub>		_	3.0 to 5.5	_	_	V <sub>CC</sub> × 0.3	_	$\begin{array}{c} V_{CC} \\ \times \ 0.3 \end{array}$	
	V <sub>OH</sub> \		I <sub>OH</sub> = -50 μA	2.0	1.9	2.0	_	1.9	_	V
		V <sub>IN</sub> =V <sub>IH</sub>		3.0	2.9	3.0	_	2.9	_	
High-level output voltage				4.5	4.4	4.5	_	4.4		
			I <sub>OH</sub> = -4 mA	3.0	2.58	_	_	2.48	_	
			I <sub>OH</sub> = -8 mA	4.5	3.94	_	_	3.80	_	
Low-level output voltage		V <sub>IN</sub> =V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OL</sub> = 50 μA	2.0		0.0	0.1	_	0.1	
				3.0		0.0	0.1	_	0.1	
	$V_{OL}$			4.5	_	0.0	0.1	_	0.1	
			I <sub>OL</sub> = 4 mA	3.0	_	_	0.36	_	0.44	
			I <sub>OL</sub> = 8 mA	4.5	_	_	0.36	_	0.44	
Input leakage current	I <sub>IN</sub>	$V_{IN} = 5.5$	V <sub>IN</sub> = 5.5 V or GND		_	_	±0.1	_	±1.0	μΑ
Quiescent supply current	Icc	V <sub>IN</sub> = V <sub>CC</sub> or GND		5.5	_	_	2.0	_	20.0	μΑ

3 2009-09-24

## AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3 \text{ ns}$ )

Characteristic	Symbol		Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
			V <sub>CC</sub> (V)	C <sub>L</sub> (pF)	Min	Тур.	Max	Min	Max	
Propagation delay time	t <sub>pLH</sub>		3.3 ± 0.3	15	_	6.2	8.8	1.0	10.5	- ns
			3.5 ± 0.5	50	_	8.7	12.3	1.0	14.0	
			5.0 ± 0.5	15	_	4.3	5.9	1.0	7.0	
				50	_	5.8	7.9	1.0	9.0	
Input capacitance	C <sub>IN</sub>		_		_	4	10	_	10	pF
Power dissipation capacitance	C <sub>PD</sub>			(Note 4)		18				pF

Note 4: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

4

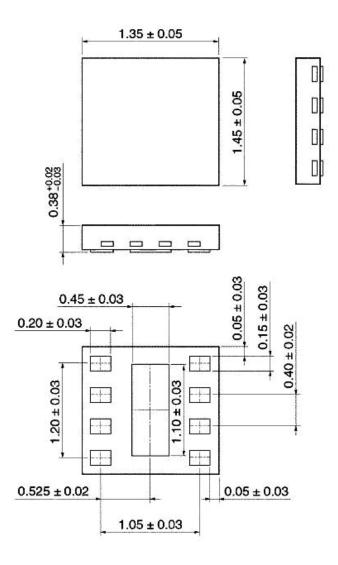
Average operating current can be obtained by the equation:

 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$ 



### **Package Dimensions**

CSON8-P-0.4 Unit: mm



Weight: 0.002 g (typ.)

5 2009-09-24

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