TOSHIBA Diodes for Protecting against ESD Epitaxial Planar Type

## DF5A6.8CJE

# Product for Use Only as Protection against Electrostatic Discharge (ESD)

- \* This product is for protection against electrostatic discharge (ESD) only and is not intended for any other usage, including without limitation, the constant voltage diode application.
- The mounting of four devices on an ultra-compact package allows the number of parts and the mounting cost to be reduced.
- Low Terminal capacitance (between Cathode and Anode)
   : C<sub>T</sub> =23pF (typ.)

### **Absolute Maximum Ratings (Ta = 25°C)**

Characteristic	Symbol	Rating	Unit
Power dissipation	Р	100	mW
Junction temperature	Tj	150	°C
Storage temperature range	T <sub>stg</sub>	-55~150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Unit: mm 1.6±0.05 1.2±0.05 1.6±0.05  $0.2 \pm 0.05$  $0.55 \pm 0.05$ 1.CATHODE 1 2. ANODE 3.CATHODE2 4.CATHODE3 5.CATHODE4 ESV **JEDEC** JEITA **TOSHIBA** 1-2W1A

Weight: 0.003 g (typ.)

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Electrical Characteristics (Ta = 25°C)**

Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Zener voltage	$V_Z$	I <sub>Z</sub> = 5 mA	6.4	6.8	7.2	V
Dynamic impedance	Z <sub>Z</sub>	I <sub>Z</sub> = 5 mA	_	_	25	Ω
Reverse current	I <sub>R</sub>	V <sub>R</sub> = 5 V	_	_	0.5	μΑ
Terminal capacitance (between Cathode and Anode)	C <sub>T</sub>	V <sub>R</sub> = 0 V, f = 1 MHz	_	23	_	pF

#### **Guaranteed Level of ESD Immunity**

Test Condition	ESD Immunity Level	
IEC61000-4-2 (Contact discharge)	± 25 kV	

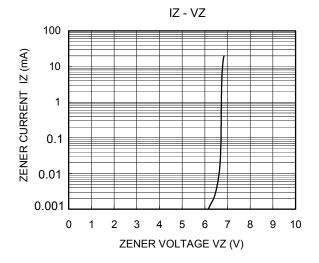
Criterion: No damage to device elements

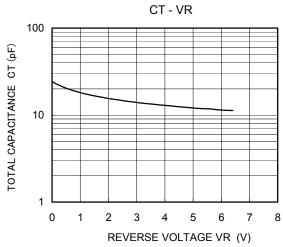
## Marking

## **Equivalent Circuit (Top View)**









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