

## Transistors

# 4V Drive Pch MOS FET

## RSR015P03

**●Structure**

Silicon P-channel MOS FET

**●Features**

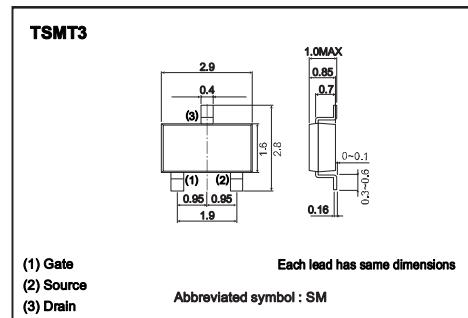
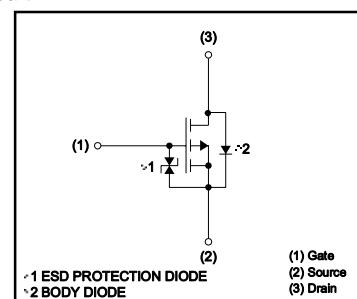
- 1) Low On-resistance
- 2) Space saving—small surface mount package (TSMT3)
- 3) 4V drive

**●Applications**

Switching

**●Packaging specifications**

Type	Package	Taping
	Code	TL
	Basic ordering unit (pieces)	3000
RSR015P03		○

**●External dimensions (Unit : mm)****●Inner circuit****●Absolute maximum ratings ( $T_a=25^\circ\text{C}$ )**

Parameter	Symbol	Limits	Unit	
Drain-source voltage	$V_{DSS}$	-30	V	
Gate-source voltage	$V_{GSS}$	$\pm 20$	V	
Drain current	Continuous	$I_D$	$\pm 1.5$	A
	Pulsed	$I_{DP}$	$\pm 6$	A
Source current (Body diode)	Continuous	$I_S$	-0.5	A
	Pulsed	$I_{SP}$	-6	A
Total power dissipation	$P_D$	1	W	
Channel temperature	$T_{ch}$	150	$^\circ\text{C}$	
Range of storage temperature	$T_{stg}$	-55 to +150	$^\circ\text{C}$	

• 1  $P_w \leq 10\mu\text{s}$ , Duty cycle  $\leq 1\%$ 

• 2 Mounted on a ceramic board

**●Thermal resistance**

Parameter	Symbol	Limits	Unit
Channel to ambient	$R_{th(ch-a)}$	125	$^\circ\text{C/W}$

• Mounted on a ceramic board

## Transistors

## ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Gate-source leakage	$I_{GS}$	—	—	$\pm 10$	$\mu A$	$V_{GS}=\pm 20V, V_{DS}=0V$
Drain-source breakdown voltage	$V_{(BR)DSS}$	-30	—	—	V	$I_D = -1mA, V_{GS}=0V$
Zero gate voltage drain current	$I_{DS}$	—	—	-1	$\mu A$	$V_{DS} = -30V, V_{GS}=0V$
Gate threshold voltage	$V_{GS(th)}$	-1.0	—	-2.5	V	$V_{DS} = -10V, I_D = -1mA$
Static drain-source on-state resistance	$R_{DS(on)}$	—	170	235	$m\Omega$	$I_D = -1.5A, V_{GS} = -10V$
		—	270	375	$m\Omega$	$I_D = -0.8A, V_{GS} = -4.5V$
		—	320	440	$m\Omega$	$I_D = -0.8A, V_{GS} = -4V$
Forward transfer admittance	$ Y_{fs} $	0.9	—	—	S	$V_{DS} = -10V, I_D = -0.8A$
Input capacitance	$C_{iss}$	—	190	—	pF	$V_{DS} = -10V$
Output capacitance	$C_{oss}$	—	45	—	pF	$V_{GS}=0V$
Reverse transfer capacitance	$C_{rss}$	—	30	—	pF	f=1MHz
Turn-on delay time	$t_d(on)$	—	6	—	ns	$V_{DD} = -15V$
Rise time	$t_r$	—	8	—	ns	$I_D = -0.8A$
Turn-off delay time	$t_d(off)$	—	22	—	ns	$V_{GS} = -10V$
Fall time	$t_f$	—	6	—	ns	$R_L = 10\Omega$
Total gate charge	$Q_g$	—	2.6	—	nC	$V_{DD} = -15V, V_{GS} = -5V$
Gate-source charge	$Q_{gs}$	—	1.0	—	nC	$I_D = -1.5A$
Gate-drain charge	$Q_{gd}$	—	0.7	—	nC	$R_L = 10\Omega, R_G = 10\Omega$

•Pulsed

## ●Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward voltage	$V_{SD}$	—	—	-1.2	V	$I_S = -0.5A, V_{GS}=0V$

## Appendix

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