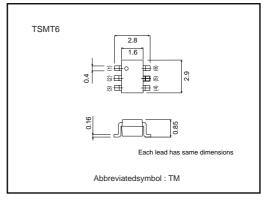
# DC-DC Converter (-30V, -3.5A) RSQ035P03

#### Features

- 1) Low On-resistance.(65mΩ at 4.5V)
- 2) High Power Package.
- 3) High speed switching.
- 4) Low voltage drive.(4.5V)

## •External dimensions (Units : mm)



## Applications

DC-DC converter

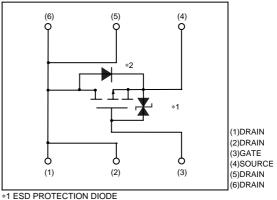
#### Structure

Silicon P-channel MOSFET

## Packaging specifications

| Туре      | Package                      | Taping |  |  |
|-----------|------------------------------|--------|--|--|
|           | Code                         | TR     |  |  |
|           | Basic ordering unit (pieces) | 3000   |  |  |
| RSQ035P03 |                              | 0      |  |  |

## Equivalent circuit



\*2 BODY DIODE

# Transistor

## •Absolute maximum ratings (Ta=25°C)

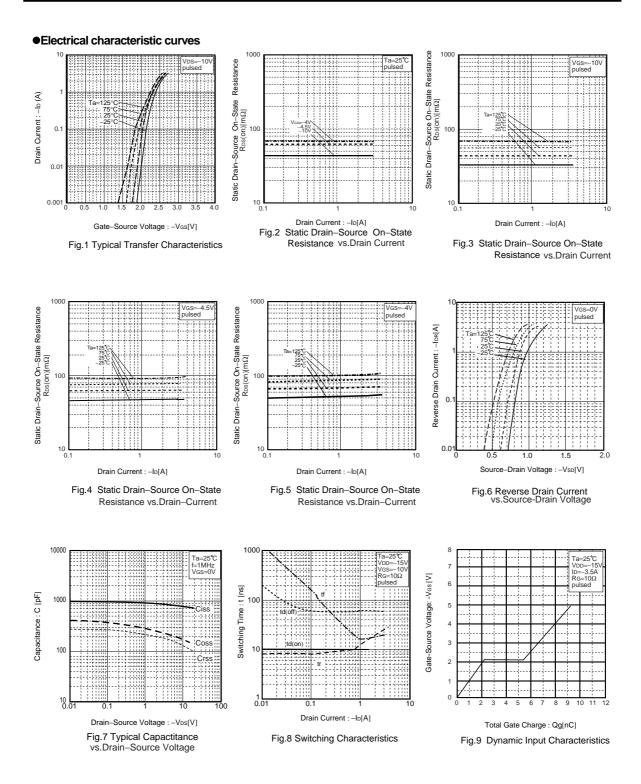
|                                | J          | /      | 1        |      |  |
|--------------------------------|------------|--------|----------|------|--|
| Parameter                      |            | Symbol | Limits   | Unit |  |
| Drain-source voltage           |            | VDSS   | -30      | V    |  |
| Gate-source voltage            |            | Vgss   | ±20      | V    |  |
| Drain current                  | Continuous | lo     | ±3.5     | А    |  |
|                                | Pulsed     | DP     | ±14      | A *1 |  |
| Source current<br>(Body diode) | Continuous | ls     | -1       | А    |  |
|                                | Pulsed     | Isp    | -4       | A *1 |  |
| Total power dissipation        |            | Po     | 1.25     | W*2  |  |
| Channel temperature            |            | Tch    | 150      | °C   |  |
| Range of Strage temperature    |            | Tstg   | -55~+150 | °C   |  |

\*1 Pw≦10μs, Duty cycle≦1% \*2 Mounted on a ceramic board

## •Electrical characteristics (Ta=25°C)

| Parameter  | Symbol             | Min. | Тур. | Max. | Unit | Conditions   |  |  |
|--|--------------------|------|------|------|------|--|--|--|
| Gate-source leakage  | lgss               | -    | -    | ±10  | μΑ   | Vgs=±20V, Vds=0V   |  |  |
| Drain-source breakdown voltage                                       | V(BR)DSS           | -30  | -    | -    | V    | ID=-1mA, VGS=0V  |  |  |
| Zero gate voltage drain current                                      | Idss               | -    | -    | -1   | μΑ   | Vds=-30V, Vgs=0V   |  |  |
| Gate threshold voltage   | VGS(th)            | -1.0 | -    | -2.5 | V    | VDS=-10V, ID=-1mA  |  |  |
| Static drain-source on-state resistance                              | RDS(on)            | -    | 45   | 65   | mΩ   | ID=-3.5A, VGs=-10V   |  |  |
|  |                    | -    | 65   | 90   | mΩ   | ID=-3.5A, Vgs=-4.5V  |  |  |
|  |                    | -    | 70   | 95   | mΩ   | ID=-1.75A, VGs=-4.0V   |  |  |
| Foward transfer admittance   | Y <sub>fs</sub>  * | 2.0  | I    | _    | S    | Vps=-10V, lp=-1.75mA   |  |  |
| Input capacitance  | Ciss               | -    | 780  | _    | pF   | V <sub>DS=</sub> -10V,V <sub>GS</sub> =0V<br>_ f=1MHz  |  |  |
| Output capacitance   | Coss               | -    | 180  | _    | pF   |  |  |  |
| Reverse transfer capacitance   | Crss               | _    | 130  | _    | pF   |  |  |  |
| Turn-on delay time   | td(on) *           | -    | 15   | _    | ns   | - I <sub>D</sub> =-1.75A<br>V <sub>D</sub> D≒-15V<br>V <sub>G</sub> s=-10V<br>R <sub>L</sub> =8.6Ω<br>R <sub>G</sub> s=10Ω |  |  |
| Rise time  | tr *               | -    | 35   | _    | ns   |  |  |  |
| Turn-off delay time  | td(off) *          | -    | 45   | _    | ns   |  |  |  |
| Fall time  | tr *               | -    | 25   | _    | ns   |  |  |  |
| Total gate charge  | Qg                 | -    | 9.2  | _    | nC   | - V <sub>DD</sub> ≒-15V<br>V <sub>GS</sub> =-5V<br>ID=-3.5mA   |  |  |
| Gate-source charge   | Qgs                | -    | 2.2  | -    | nC   |  |  |  |
| Gate-drain charge  | Qgd                | -    | 3.4  | _    | nC   |  |  |  |
| *PULSED<br>Body diode characteristics (source-drain characteristics) |                    |      |      |      |      |  |  |  |
| Forward voltage  | VSD                | -    | -    | -1.2 | V    | Is=-1A, Vgs=0V   |  |  |

## Transistor



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## Transistor

## Measurement circuits

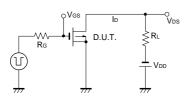


Fig.10 Switching Time Measurement Circuit

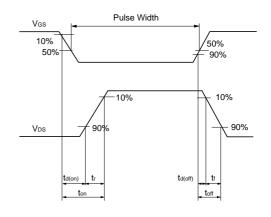


Fig.11 Switching Waveforms

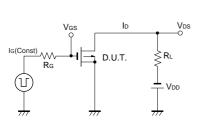


Fig.12 Gate Charge Measurement Circuit

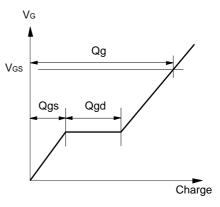


Fig.13 Gate Charge Waveforms

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