HSK122

Silicon Epitaxial Planar Diode for High Voltage Switching

HITACHI

ADE-208-172B (Z) Rev. 2 Aug. 1995

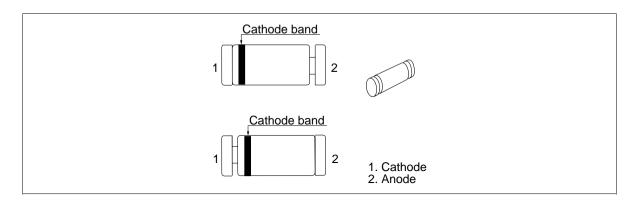
Features

- High reverse voltage. $(V_R = 400V)$
- LLD package is suitable for high density surface mounting and high speed assembly.

Ordering Information

Type No.	Cathode band	Package Code
HSK122	Orange	LLD

Outline





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Absolute Maximum Ratings $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Peak reverse voltage	V_{RM}	410	V
Reverse voltage	V _R	400	V
Peak forward current	I _{FM}	625	mA
Non-Repetitive peak forward surge current	I _{FSM} *	1	Α
Average forward current	Io	150	mA
Junction temperature	Tj	175	°C
Storage temperature	Tstg	-65 to +175	°C

Note: Within 1s forward surge current.

Electrical Characteristics ($Ta = 25^{\circ}C$)

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Forward voltage	V_{F}	_	_	1.2	V	I _F = 100mA
Reverse current	I _R	_	_	1.0	μΑ	V _R = 400V
Capacitance	С	_	_	10	pF	$V_R = 0V, f = 1MHz$
Reverse recovery time	t _{rr}	_	_	10	ns	$I_F = 30$ mA, $V_R = 10$ V, $R_L = 2$ k Ω

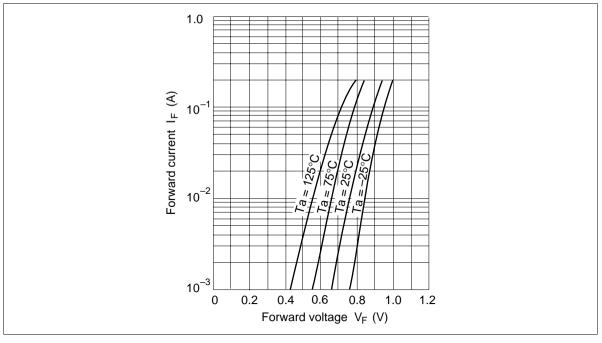


Fig.1 Forward current Vs. Forward voltage

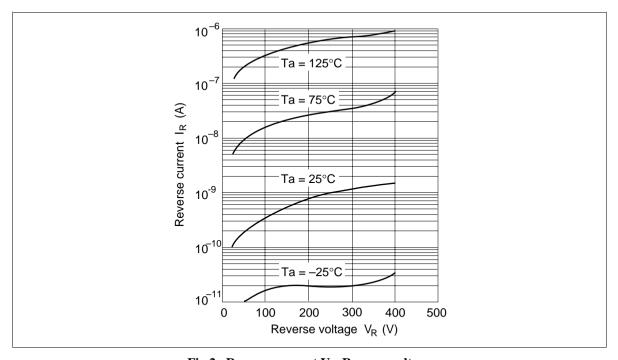


Fig.2 Reverse current Vs. Reverse voltage

HSK122

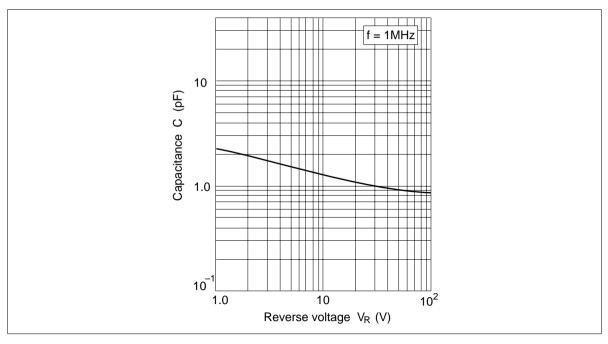
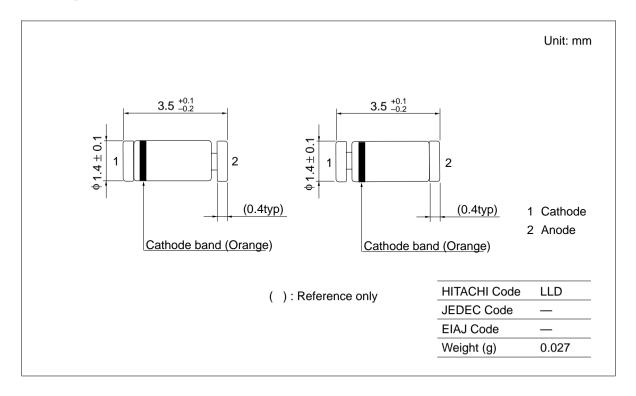


Fig.3 Capacitance Vs. Reverse voltage

Package Dimensions



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