# Surface Mount Schottky Power Rectifier SMB Power Surface Mount Package

... employing the Schottky Barrier principle in a metal-to-silicon power rectifier. Features epitaxial construction with oxide passivation and metal overlay contact. Ideally suited for low voltage, high frequency switching power supplies; free wheeling diodes and polarity protection diodes.

- Compact Package with J-Bend Leads Ideal for Automated Handling
- Highly Stable Oxide Passivated Junction
- Guardring for Over-Voltage Protection
- Low Forward Voltage Drop

## **Mechanical Characteristics:**

- Case: Molded Epoxy
- Epoxy Meets UL94, VO at 1/8"
- Weight: 95 mg (approximately)
- Cathode Polarity Band
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Available in 12 mm Tape, 2500 Units per 13" Reel, Add "T3" Suffix to Part Number
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- ESD Ratings: Machine Model = C Human Body Model = 3B

# MAXIMUM RATINGS

| Rating  | Symbol   | Value       | Unit |
|---|--|-------------|------|
| Peak Repetitive Reverse Voltage<br>Working Peak Reverse Voltage<br>DC Blocking Voltage SS22<br>SS24               | V <sub>RRM</sub><br>V <sub>RWM</sub><br>V <sub>R</sub> | 20<br>40    | V    |
| Average Rectified Forward Current<br>(At Rated V <sub>R</sub> , T <sub>L</sub> = 100°C)                           | Ι <sub>Ο</sub>   | 2.0         | A    |
| Peak Repetitive Forward Current<br>(At Rated V <sub>R</sub> , Square Wave,<br>100 kHz, T <sub>C</sub> = 105°C)    | I <sub>FRM</sub>                                       | 3.0         | A    |
| Non-Repetitive Peak Surge Current<br>(Surge Applied at Rated Load<br>Conditions Halfwave, Single<br>Phase, 60 Hz) | I <sub>FSM</sub>                                       | 75          | A    |
| Storage/Operating Case Temperature  | T <sub>stg</sub> , T <sub>C</sub>                      | -55 to +150 | °C   |
| Operating Junction Temperature  | TJ   | -55 to +125 | °C   |
| Voltage Rate of Change (Rated $V_R$ , $T_J = 25^{\circ}C$ )   | dv/dt  | 10,000      | V/µs |



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# SCHOTTKY BARRIER RECTIFIER 2 AMPERES 20, 40 VOLTS



SMB CASE 403A PLASTIC

#### MARKING DIAGRAM



SS2x = Device Code x = 2 or 4

#### **ORDERING INFORMATION**

| Device | Package | Shipping         |
|--------|---------|------------------|
| SS22T3 | SMB     | 2500/Tape & Reel |
| SS24T3 | SMB     | 2500/Tape & Reel |

# THERMAL CHARACTERISTICS

| Characteristic  | Symbol                              | Value    | Unit |
|---|-------------------------------------|----------|------|
| Thermal Resistance — Junction-to-Lead (Note 1)<br>Thermal Resistance — Junction-to-Ambient (Note 2) | $R_{	extsf{	heta}JL}$ $R_{	hetaJA}$ | 24<br>80 | °C/W |

## **ELECTRICAL CHARACTERISTICS**

| Maximum Instantaneous Forward Voltage (Note 3) |                          | ٧F             | $T_J = 25^{\circ}C$   | T <sub>J</sub> = 125°C | Volts |
|--|--------------------------|----------------|-----------------------|------------------------|-------|
| see Figure 2                                   | (i <sub>F</sub> = 2.0 A) |                | 0.50                  | 0.46                   |       |
| Maximum Instantaneous Reverse Current (Note 3) |                          | I <sub>R</sub> | T <sub>J</sub> = 25°C | T <sub>J</sub> = 100°C | mA    |
| see Figure 4                                   | (V <sub>R</sub> = 40 V)  |                | 0.4                   | 5.7                    |       |

1. Mounted with minimum recommended pad size, PC Board FR4.2. 1 inch square pad size (1 x 0.5 inch for each lead) on FR4 board.3. Pulse Test: Pulse Width  $\leq 250 \ \mu$ s, Duty Cycle  $\leq 2.0\%$ .

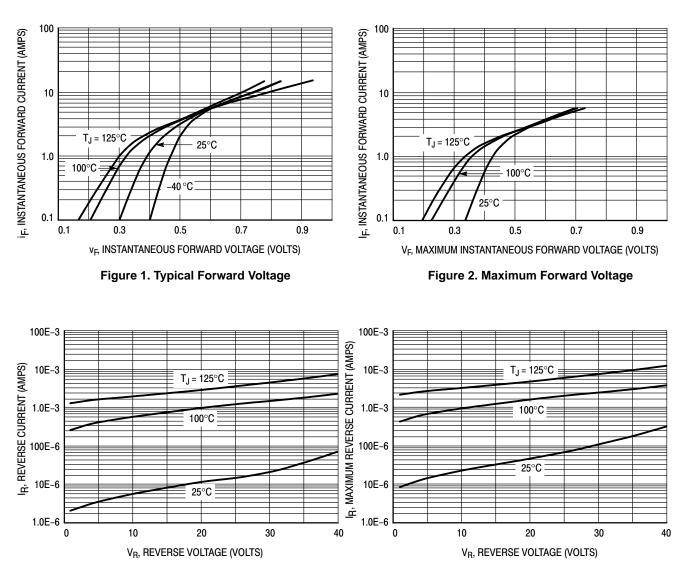
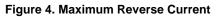
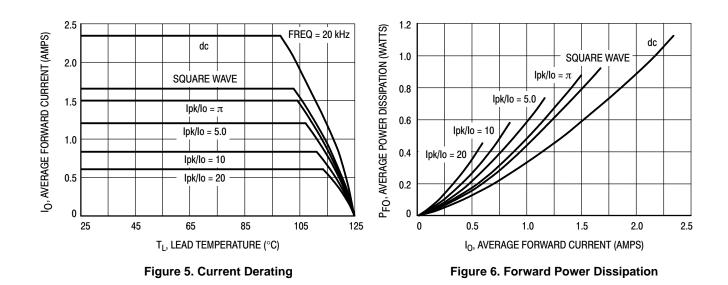
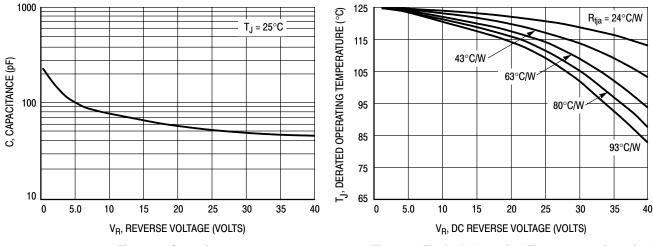


Figure 3. Typical Reverse Current











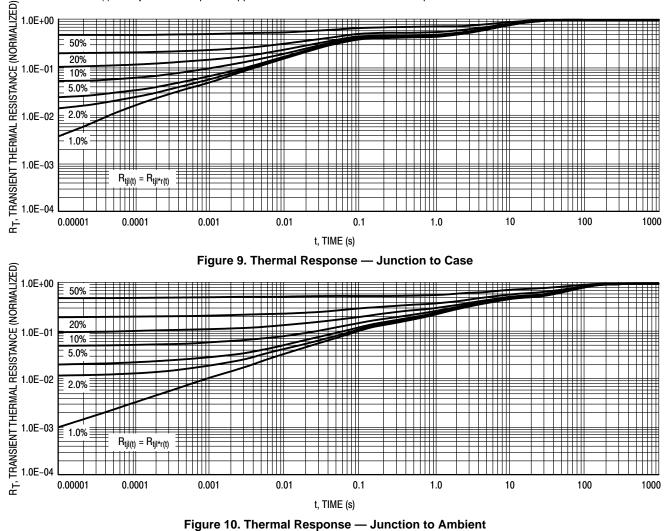
\* Reverse power dissipation and the possibility of thermal runaway must be considered when operating this device under any reverse voltage conditions. Calculations of  $T_J$  therefore must include forward and reverse power effects. The allowable operating  $T_J$  may be calculated from the equation:  $T_J = T_{Jmax} - r(t)(Pf + Pr)$  where

r(t) = thermal impedance under given conditions,

Pf = forward power dissipation, and

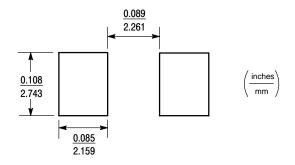
Pr = reverse power dissipation

This graph displays the derated allowable  $T_J$  due to reverse bias under DC conditions only and is calculated as  $T_J = T_{Jmax} - r(t)Pr$ , where r(t) = Rthja. For other power applications further calculations must be performed.



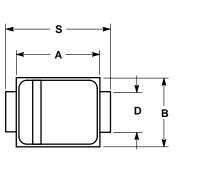
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# MINIMUM SOLDER PAD SIZES

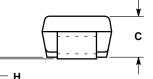


# SS22, SS24

### PACKAGE DIMENSIONS



SMB PLASTIC PACKAGE CASE 403A-03 ISSUE D



NOTES:

 DIMENSIONING AND TOLERANCING PER ANSI Y14.5M. 1982.

 CONTROLLING DIMENSION: INCH.
D DIMENSION SHALL BE MEASURED WITHIN DIMENSION P.

|     | INCHES    |        | MILLIMETERS |       |  |
|-----|-----------|--------|-------------|-------|--|
| DIM | MIN       | MAX    | MIN         | MAX   |  |
| Α   | 0.160     | 0.180  | 4.06        | 4.57  |  |
| В   | 0.130     | 0.150  | 3.30        | 3.81  |  |
| С   | 0.075     | 0.095  | 1.90        | 2.41  |  |
| D   | 0.077     | 0.083  | 1.96        | 2.11  |  |
| Н   | 0.0020    | 0.0060 | 0.051       | 0.152 |  |
| J   | 0.006     | 0.012  | 0.15        | 0.30  |  |
| K   | 0.030     | 0.050  | 0.76        | 1.27  |  |
| Р   | 0.020 REF |        | 0.51 REF    |       |  |
| S   | 0.205     | 0.220  | 5.21        | 5.59  |  |

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