## DESCRIPTION

RT1N432X is a one chip transistor with built- in bias resistor,PNP type is RT1P432X.

## FEATURE

- Built- in bias resistor (R1=4.7k, $\mathrm{R} 2=10 \mathrm{k} \Omega$ ).


## APPLICATION

Inverted circuit,switching circuit,interface circuit, driver circuit.

Equivalent circuit



MAXIMUM RATING ( $\mathrm{Ta}=25^{\circ} \mathrm{C}$ )

| SYMBOL | PARAMETER | RATING |  |  |  | UNIT |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  | RT1N432U | RT1N432M | RT1N432C | RT1N432S |  |
| $V_{\text {CBo }}$ | Collector to Base voltage | 50 |  |  |  | V |
| $V_{\text {Ebo }}$ | Emitter to Base voltage | 7 |  |  |  | V |
| $\mathrm{V}_{\text {CEO }}$ | Collector to Emitter voltage | 50 |  |  |  | V |
| $\mathrm{I}_{\mathrm{C}}$ | Collector current | 100 |  |  |  | mA |
| 1 CM | Peak Collector current | 200 |  |  |  | mA |
| $\mathrm{P}_{\mathrm{C}}$ | Collector dissipation( $\mathrm{Ta}=25^{\circ} \mathrm{C}$ ) | 150 |  |  | 450 | mW |
| Tj | J unction temperature | +150 | +150 |  |  | ${ }^{\circ} \mathrm{C}$ |
| Tstg | Storage temperature | $-55 \sim+150$ | $-55 \sim+150$ |  |  | ${ }^{\circ} \mathrm{C}$ |

ELECTRICAL CHARACTERISTICS ( $\mathrm{Ta}=25^{\circ} \mathrm{C}$ )

| SYMBOL | PARAMETER | TEST CONDITION | LIMIT |  |  | UNIT |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  |  |  | MIN | TYP | MAX |  |
| $\mathrm{V}_{\text {(BR) CEO }}$ | C to E break down voltage | $\mathrm{I}_{\mathrm{C}}=100 \mu \mathrm{~A}, \mathrm{R}_{\mathrm{BE}}=\infty$ | 50 |  |  | V |
| $\mathrm{I}_{\text {cbo }}$ | Collector cut off current | $\mathrm{V}_{\mathrm{CB}}=50 \mathrm{~V}, \mathrm{I}_{\mathrm{E}}=0$ |  |  | 0.1 | $\mu \mathrm{A}$ |
| $\mathrm{h}_{\text {FE }}$ | DC forward current gain | $\mathrm{V}_{\mathrm{CE}}=5 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=10 \mathrm{~mA}$ | 30 |  |  | - |
| $\mathrm{V}_{\text {CE (sat) }}$ | C to E saturation voltage | $\mathrm{I}_{\mathrm{C}}=10 \mathrm{~mA}, \mathrm{I}_{\mathrm{B}}=0.5 \mathrm{~mA}$ |  | 0.1 | 0.3 | V |
| $\mathrm{V}_{1 \text { (ON) }}$ | Input on voltage | $\mathrm{V}_{\mathrm{CE}}=0.2 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=5 \mathrm{~mA}$ |  | 1.0 | 1.8 | V |
| $V_{1}$ (OFF) | Input off voltage | $\mathrm{V}_{\mathrm{CE}}=5 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=100 \mu \mathrm{~A}$ | 0.5 | 0.8 |  | V |
| $\mathrm{R}_{1}$ | Input resistance |  | 3.3 | 4.7 | 6.1 | k $\Omega$ |
| $\mathrm{R}_{2} / \mathrm{R}_{1}$ | Resistance ratio |  | 1.7 | 2.1 | 2.6 |  |
| $\mathrm{f}_{\mathrm{T}}$ | Gain band width product | $\mathrm{V}_{\mathrm{CE}}=6 \mathrm{~V}, \mathrm{I}_{\mathrm{E}}=10 \mathrm{~mA}$ |  | 200 |  | MHz |

## TYPICAL CHARACTERISTICS



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