

**SONY****2SK613****Silicon N-Channel Junction FET***T-29-25***Description**

Making the best of Epitaxy and Pattern latest technology, 2SK613 accomplishes so far unattainable levels of performance.

Usage with head amplifiers for video cameras and the like, ensures the highest efficiency.

**Features**

- High figure of merit  

$$\left( \begin{array}{l} V_{DS} = 5 \text{ V} \\ I_D = 10 \text{ mA} \end{array} \right) |Y_{fs}| / C_{iss} 4.5$$
- High forward transfer admittance  

$$\left( \begin{array}{l} V_{DS} = 5 \text{ V} \\ V_{GS} = 0 \text{ V} \end{array} \right) |Y_{fs}| \quad 30 \text{ mS(Typ.)}$$
- Low input capacitance  
 $C_{iss} \quad 6.6 \text{ pF(Typ.)}$

**Structure**

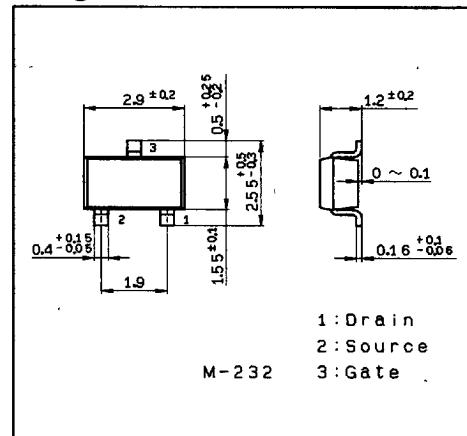
- Silicon N-Channel junction FET

**Absolute Maximum Ratings (Ta=25°C)**

● Drain to gate voltage	$V_{DGO}$	15	V
● Source to gate voltage	$V_{SGO}$	15	V
● Drain current	$I_D$	50	mA
● Gate current	$I_G$	5	mA
● Allowable power dissipation	$P_D$	150	mW
● Junction temperature	$T_j$	150	°C
● Storage temperature	$T_{stg}$	-55 to +150	°C

**Package Outline**

Unit: mm


 1: Drain  
 2: Source  
 3: Gate

60199A-HP

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**T-29-25****Electrical Characteristics**

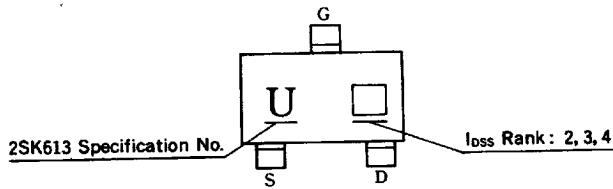
Unless otherwise specified (Ta = 25°C)

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Drain to Gate Voltage	V <sub>DG</sub>	I <sub>G</sub> = 10 μA	15			V
Source to Gate Voltage	V <sub>SG</sub>	I <sub>G</sub> = 10 μA	15			V
Drain to Source Voltage	V <sub>DSX</sub>	I <sub>D</sub> = 10 μA, V <sub>GS</sub> = -3 V	15			V
Gate Cutoff Current	I <sub>GS</sub>	V <sub>GS</sub> = -7 V, V <sub>DS</sub> = 0 V			-2	nA
Drain Current	I <sub>DS</sub>	* V <sub>GS</sub> = 5 V, V <sub>DS</sub> = 0 V	13.4		42.0	mA
Gate to Source Cutoff Voltage	V <sub>GS(OFF)*</sub>	V <sub>DS</sub> = 5 V, I <sub>D</sub> = 100 μA,	-0.65		-2.0	V
Forward Transfer Admittance	Y <sub>fs</sub>   *	V <sub>DS</sub> = 5 V, V <sub>GS</sub> = 0 V, f = 1 kHz	23	30		mS
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 5 V, V <sub>GS</sub> = 0 V, f = 1 MHz		6.6	7.5	pF
Equivalent Input Noise Voltage	—e <sub>n</sub>	V <sub>DS</sub> = 5 V, I <sub>D</sub> = 10 mA, R <sub>g</sub> = 0 Ω, f = 1 kHz		4.0	7.0	nV/Hz

(\*Drain current detail specification as follows.)

**Classification**

	I <sub>DS</sub> (mA) $\left( \begin{array}{l} V_{DS} = 5 \text{ V} \\ V_{GS} = 0 \text{ V} \end{array} \right)$	V <sub>GS(OFF)</sub> (V) $\left( \begin{array}{l} V_{DS} = 5 \text{ V} \\ I_D = 100 \mu\text{A} \end{array} \right)$	Y <sub>fs</sub>   (mS) $\left( \begin{array}{l} V_{DS} = 5 \text{ V} \\ V_{GS} = 0 \text{ V} \\ f = 1 \text{ kHz} \end{array} \right)$	Mark
2SK613-2	13.4 to 21.0	-0.65 to -1.26	23	2
2SK613-3	19.0 to 30.2	-0.85 to -1.6	25	3
2SK613-4	27.4 to 42.0	-1.05 to -2.0	29	4

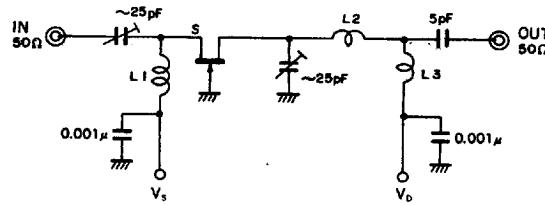
**Mark**

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**Standard Circuit Design Data****T-29-25**

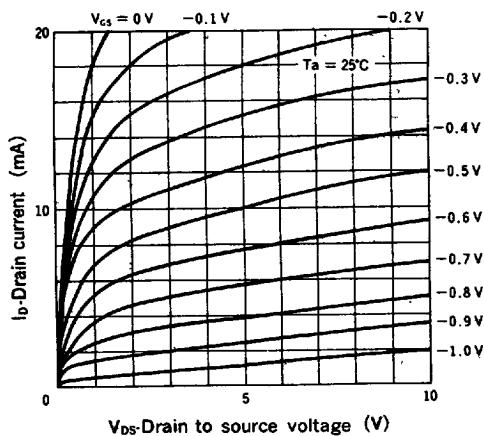
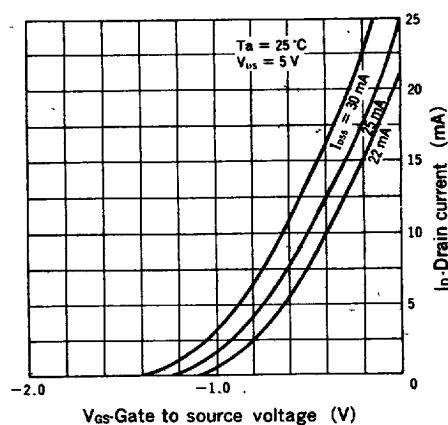
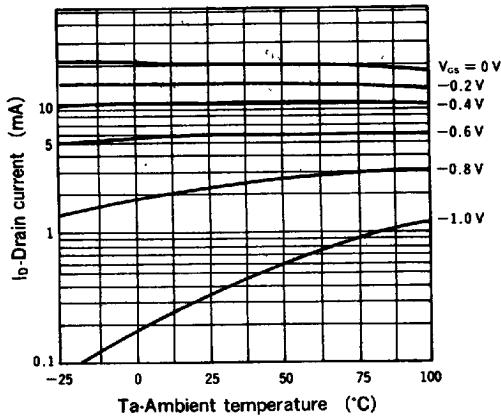
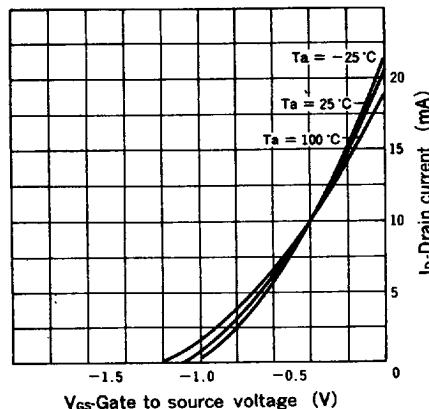
Item	Symbol	Condition	Typ.	Unit
Forward Transfer Admittance	$Y_{fs}$	$V_{ds} = 5 \text{ V}$ , $I_d = 10 \text{ mA}$ , $f = 1 \text{ kHz}$	.25	$\text{mS}$
Input Capacitance	$C_{iss}$	$V_{ds} = 5 \text{ V}$ , $I_d = 10 \text{ mA}$ , $f = 1 \text{ MHz}$	5.5	$\text{pF}$
Gate Cutoff Current	$I_g$	$V_{dg} = 5 \text{ V}$ , $I_d = 10 \text{ mA}$	10	$\text{pA}$
Input Resistance	$R_{is}$	$V_{ds} = 5 \text{ V}$ , $I_d = 10 \text{ mA}$ , $f = 100 \text{ MHz}$	3.5	$\text{k}\Omega$
Input Capacitance	$C_{is}$		5.5	$\text{pF}$
Output Resistance	$r_{os}$		2.0	$\text{k}\Omega$
Output Capacitance	$C_{os}$		1.5	$\text{pF}$
Power Gain	$PG$	$V_{ds} = 5 \text{ V}$ , $I_d = 10 \text{ mA}$ , $f = 100 \text{ MHz}$	14	$\text{dB}$
Noise Figure	$NF$		1.8	$\text{dB}$
Equivalent Input Noise Voltage	$e_n$	$V_{ds} = 5 \text{ V}$ , $I_d = 10 \text{ mA}$ , $f = 1 \text{ kHz}$ , $R_g = 0 \Omega$	4.0	$\text{nV}/\text{Hz}$
Reverse Transfer	$C_{rss}$	$V_{ds} = 5 \text{ V}$ , $V_s = 0 \text{ V}$ , $f = 1 \text{ MHz}$	1.6	$\text{pF}$

**100 MHz PG, NF Test Circuit**

L1:  $\phi 0.45 \text{ mm}$  Polyurethan wire  $\phi 3 \text{ mm}$  10.5 t  
 L2:  $\phi 0.45 \text{ mm}$  Polyurethan wire  $\phi 3 \text{ mm}$  5.5 t  
 L3:  $\phi 0.45 \text{ mm}$  Polyurethan wire  $\phi 3 \text{ mm}$  5.5 t

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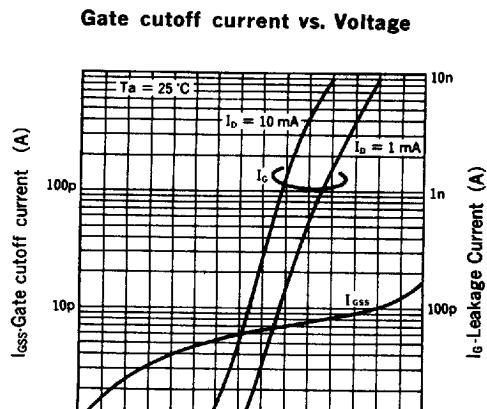
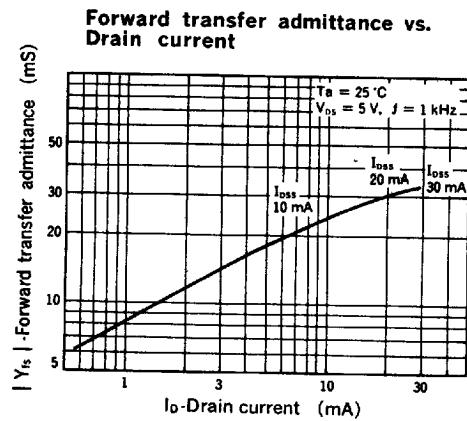
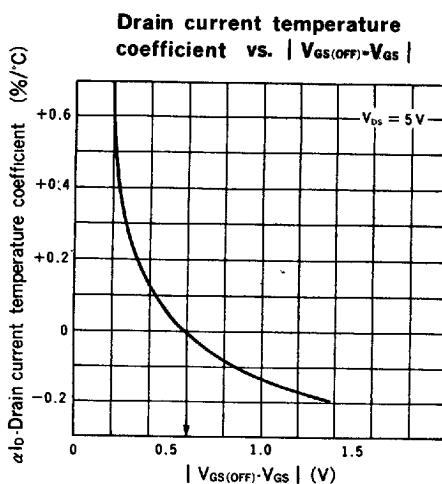
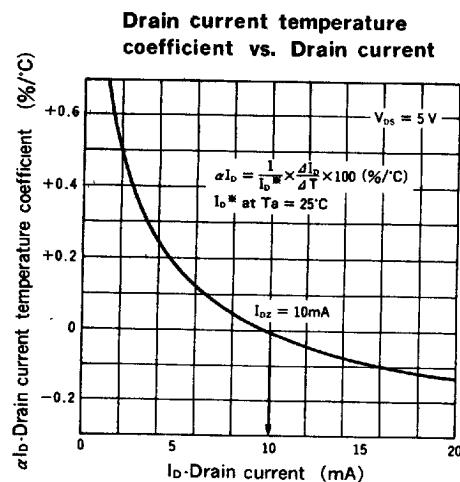
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**Drain current vs. Gate to source voltage****Drain current vs. Gate to source voltage****Drain current vs. Ambient temperature****Drain current vs. Gate to source voltage**

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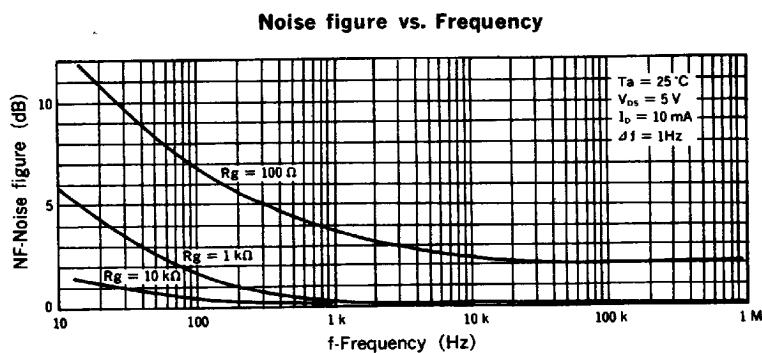
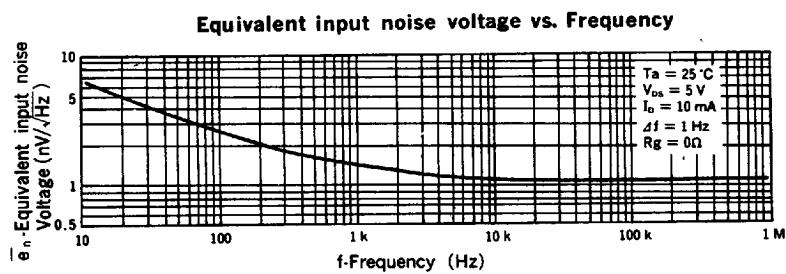
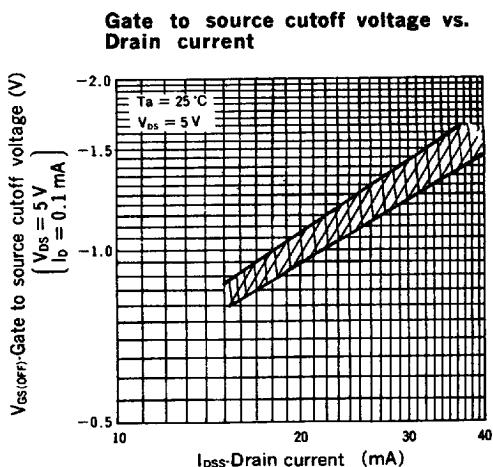
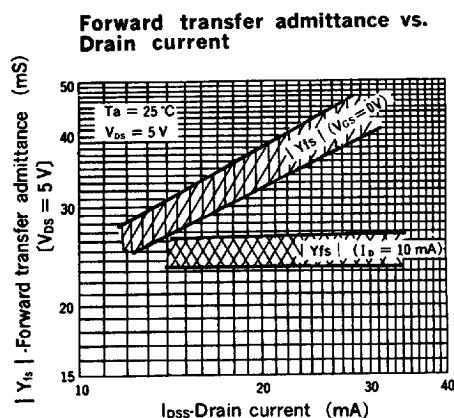
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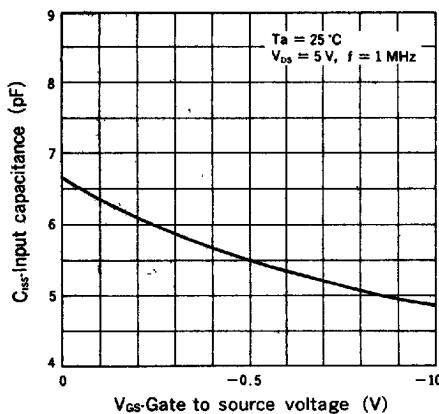
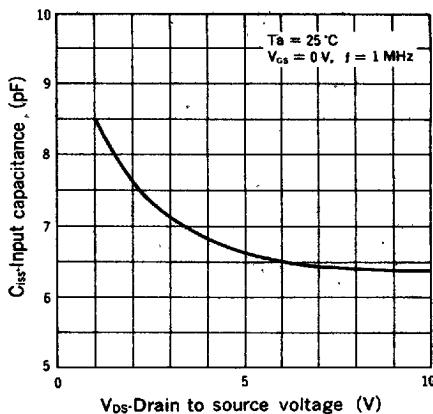
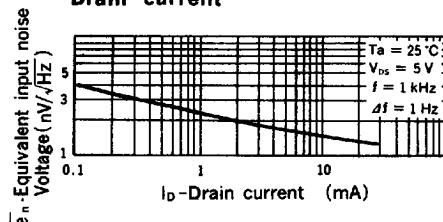
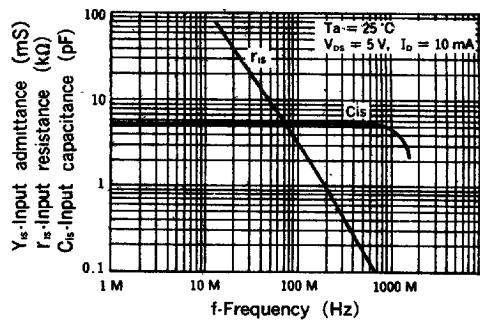
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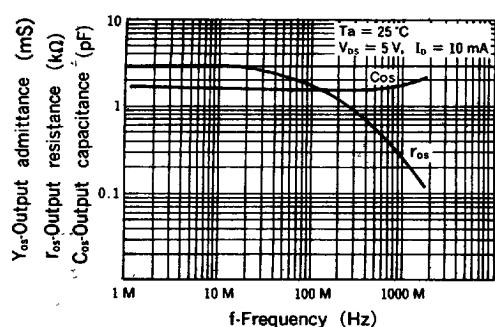
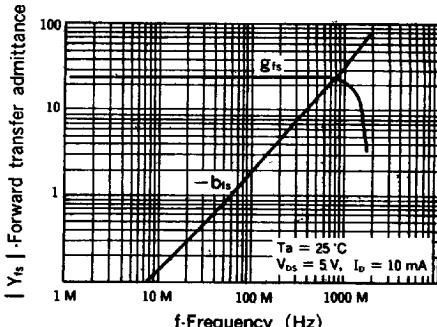
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**Input capacitance vs. Gate to source voltage****Input capacitance vs. Drain to source voltage****Equivalent input noise voltage vs. Drain current****Input admittance vs. Frequency**

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**Output admittance vs. Frequency****Forward transfer admittance vs. Frequency****Reverse transfer admittance vs. Frequency**