TOSHIBA Field Effect Transistor Silicon N Channel MOS Type

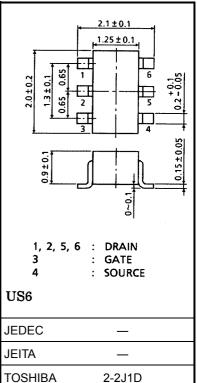
# SSM6K07FU

DC-DC Converters High Speed Switching Applications

- Small package
- Low on resistance :  $R_{on} = 130 \text{ m}\Omega \text{ max} (@V_{GS} = 10 \text{ V})$ 
  - $R_{on} = 220 \text{ m}\Omega \text{ max} (@V_{GS} = 4 \text{ V})$
- Low input capacitance :  $C_{iss} = 102 \text{ pF typ.}$ :  $C_{rss} = 22 \text{ pF typ.}$

#### Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V <sub>DS</sub>	30	V	
Gate-source voltage		V <sub>GSS</sub>	±20	V	
Drain current	DC	I <sub>D</sub>	1.5	А	
	Pulse	I <sub>DP</sub>	3.0	~	
Drain power dissipation		P <sub>D</sub> (Note 1)	300	mW	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55~150	°C	

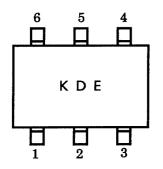


Note 1: Mounted on FR4 board.

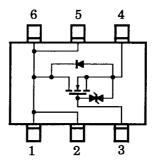
(25.4 mm  $\times$  25.4 mm  $\times$  1.6 t, Cu pad: 0.32 mm<sup>2</sup>  $\times$  6)

Weight: 6.8 mg (typ.)

#### Marking



### Equivalent Circuit (top view)



#### **Handling Precaution**

When handling individual devices (which are not yet mounting on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

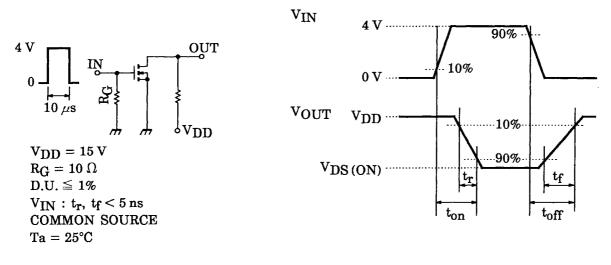
Unit: mm

**Electrical Characteristics (Ta = 25°C)** 

Chara	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage curr	rent	I <sub>GSS</sub>	$V_{GS} = \pm 16 \text{ V}, \text{ V}_{DS} = 0$			±1	μA
Drain-source brea	kdown voltage	V (BR) DSS	$I_D = 1 \text{ mA}, V_{GS} = 0$	30			V
Drain cut-off curre	ent	I <sub>DSS</sub>	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0$	_	_	1	μA
Gate threshold vo	Itage	V <sub>th</sub>	$V_{DS} = 5 \text{ V}, \text{ I}_{D} = 0.1 \text{ mA}$	1.1		1.8	V
Forward transfer a	admittance	Y <sub>fs</sub>	$V_{DS} = 5 \text{ V}, \text{ I}_{D} = 0.75 \text{ A}$ (Note 2	) 1.0			S
Drain-source ON resistance		R <sub>DS (ON)</sub>	$I_D = 0.75 \text{ A}, V_{GS} = 10 \text{ V}$ (Note 2	) —	105	130	mΩ
		R <sub>DS (ON)</sub>	$I_D = 0.75 \text{ A}, V_{GS} = 4 \text{ V}$ (Note 2	) —	170	220	
		R <sub>DS (ON)</sub>	$I_D = 0.75 \text{ A}, V_{GS} = 3.3 \text{ V}$ (Note 2	) —	230	500	
Input capacitance		C <sub>iss</sub>	$V_{DS}=15~V,~V_{GS}=0,~f$ = 1 MHz		102		pF
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = 15 \text{ V}, \text{ V}_{GS} = 0, \text{ f} = 1 \text{ MHz}$	_	22		pF
Output capacitance		C <sub>oss</sub>	$V_{DS} = 15 \text{ V}, \text{ V}_{GS} = 0, \text{ f} = 1 \text{ MHz}$	_	57		pF
Switching time	Turn-on time	t <sub>on</sub>	V <sub>DD</sub> = 15 V, I <sub>D</sub> = 0.75 A, V <sub>GS</sub> = 0~4 V,	—	46		ns
	Turn-off time	t <sub>off</sub>	$R_{G} = 10 \Omega$	—	65		

Note 2: Pulse test

#### **Switching Time Test Circuit**



#### Precaution

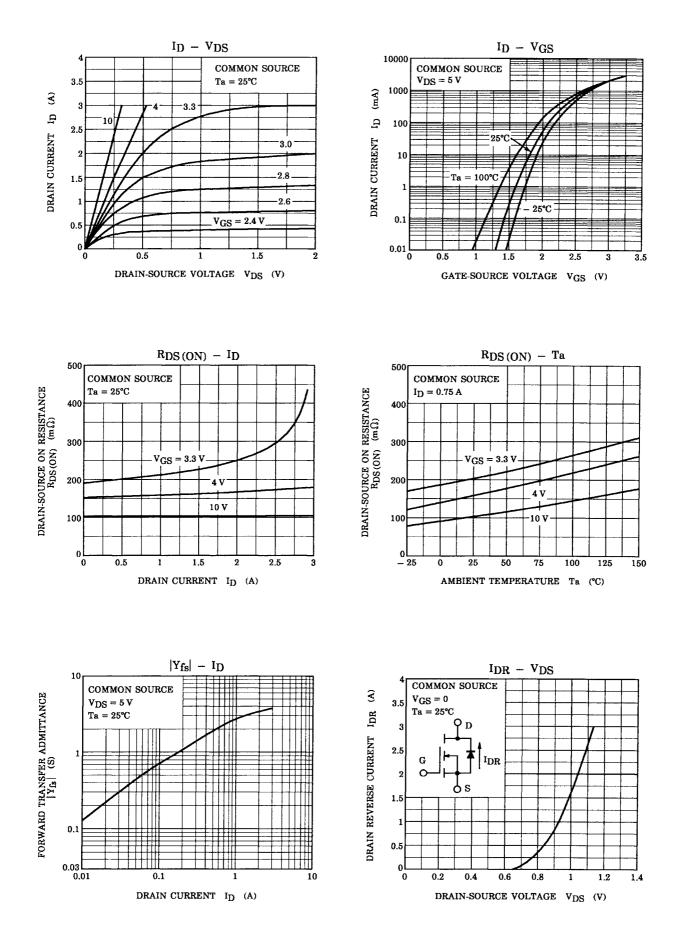
 $V_{th}$  can be expressed as voltage between gate and source when low operating current value is ID = 100  $\mu A$  for this product. For normal switching operation,  $V_{GS}$  (on) requires higher voltage than  $V_{th}$  and  $V_{GS}$  (off) requires lower voltage than  $V_{th}$ .

(Relationship can be established as follows:  $\rm VGS~(off) < Vth < VGS~(on))$ 

Please take this into consideration for using the device.

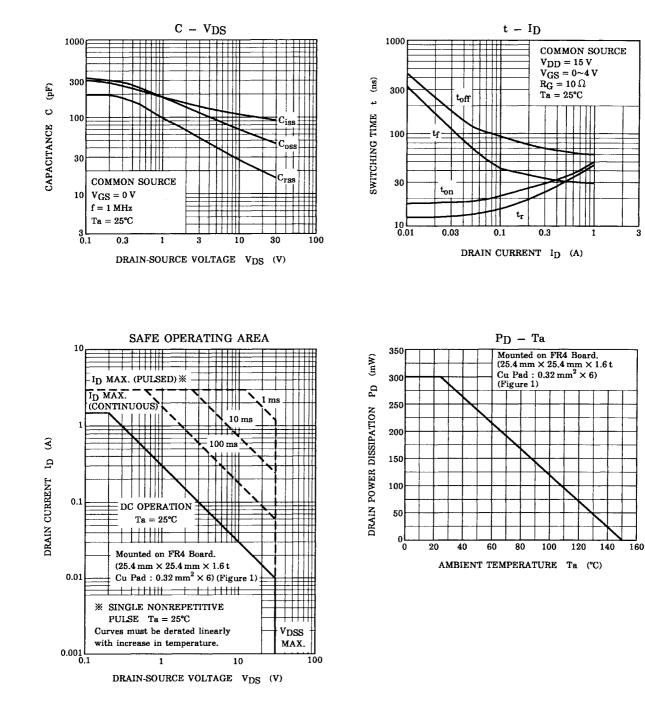
VGS recommended voltage of 4 V or higher to turn on this product.

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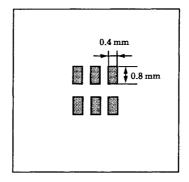


Figure 1 25.4 mm  $\times$  25.4 mm  $\times$  1.6 t, Cu Pad: 0.32 mm<sup>2</sup>  $\times$  6

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