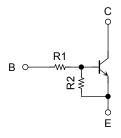
TOSHIBA Transistor Silicon NPN Epitaxial Type (PCT process) (Bias Resistor built-in Transistor)

## RN1961FE,RN1962FE,RN1963FE RN1964FE,RN1965FE,RN1966FE

Switching, Inverter Circuit, Interface Circuit and Driver Circuit Applications

- Two devices are incorporated into an Extreme-Super-Mini (6 pin) package.
- Incorporating a bias resistor into a transistor reduces parts count.
   Reducing the parts count enable the manufacture of ever more compact equipment and save assembly cost.
- Complementary to RN2961FE to RN2966FE

#### **Equivalent Circuit and Bias Resistor Values**

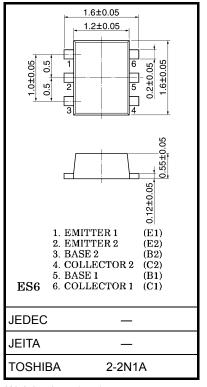


Type No.	R1 (kΩ)	R2 (kΩ)
RN1961FE	4.7	4.7
RN1962FE	10	10
RN1963FE	22	22
RN1964FE	47	47
RN1965FE	2.2	47
RN1966FE	4.7	47

# Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

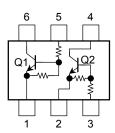
Characteristics	Symbol	Rating	Unit		
Collector-base voltage	RN1961FE	$V_{CBO}$	50	V	
Collector-emitter voltage	to 1966FE	V <sub>CEO</sub>	50	٧	
Emitter-base voltage	RN1961FE to 1964FE	V	10	V	
	RN1965FE, 1966FE	V <sub>EBO</sub>	5		
Collector current		IC	100	mA	
Collector power dissipation	RN1961FE	P <sub>C</sub> (Note 1)	100	mW	
Junction temperature	to 1966FE	Tj	150	°C	
Storage temperature range		T <sub>stg</sub>	-55 to150	°C	

Unit: mm



Weight: 3mg (typ.)

# Equivalent Circuit (top view)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

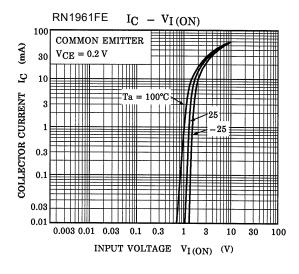
Note 1: Total rating

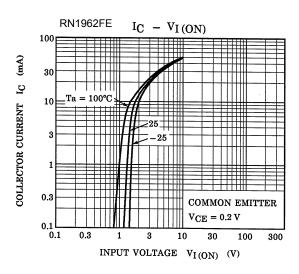


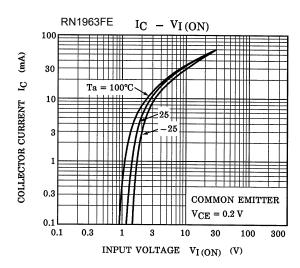
## Electrical Characteristics (Ta = 25°C) (Q1, Q2 common)

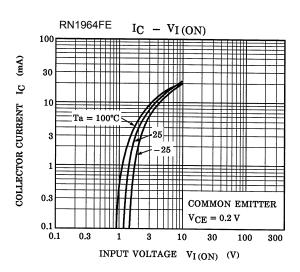
Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	RN1961FE to 1966FE	I <sub>CBO</sub>	$V_{CB} = 50 \text{ V}, I_{E} = 0$	_	_	100	nΔ
		I <sub>CEO</sub>	V <sub>CE</sub> = 50 V, I <sub>B</sub> = 0	_	_	500	nA
Emitter cut-off current	RN1961FE	I <sub>EBO</sub>	V <sub>EB</sub> = 10 V, I <sub>C</sub> = 0	0.82	_	1.52	mA
	RN1962FE			0.38	_	0.71	
	RN1963FE			0.17	_	0.33	
	RN1964FE			0.082	_	0.15	
	RN1965FE		V <sub>EB</sub> = 5 V, I <sub>C</sub> = 0	0.078	_	0.145	
	RN1966FE			0.074	_	0.138	
DC current gain	RN1961FE		V <sub>CE</sub> = 5 V, I <sub>C</sub> = 10 mA	30	_	_	_
	RN1962FE			50	_	_	
	RN1963FE	h <sub>FE</sub>		70	_	_	
	RN1964FE			80	_	_	
	RN1965FE			80	_	_	
	RN1966FE			80	_	_	
Collector-emitter saturation voltage	RN1961FE to 1966FE	V <sub>CE</sub> (sat)	I <sub>C</sub> = 5 mA, I <sub>B</sub> = 0.25 mA	_	0.1	0.3	٧
Input voltage (ON)	RN1961FE	V <sub>I</sub> (ON)	$V_{CE} = 0.2 \text{ V}, I_{C} = 5 \text{ mA}$	1.1	_	2.0	. V
	RN1962FE			1.2	_	2.4	
	RN1963FE			1.3	_	3.0	
	RN1964FE			1.5	_	5.0	
	RN1965FE			0.6	_	1.1	
	RN1966FE			0.7	_	1.3	
Input voltage (OFF)	RN1961FE to 1964FE	.,	V <sub>CE</sub> = 5 V, I <sub>C</sub> = 0.1 mA	1.0	_	1.5	V
	RN1965FE, 1966FE	V <sub>I (OFF)</sub>		0.5	_	0.8	
Transition frequency	RN1961FE to 1966FE	f <sub>T</sub>	$V_{CE} = 10 \text{ V}, I_{C} = 5 \text{ mA}$	_	250	_	MHz
Collector output capacitance	RN1961FE to 1966FE	C <sub>ob</sub>	V <sub>CB</sub> = 10 V, I <sub>E</sub> = 0, f = 1 MHz	_	3	6	pF
	RN1961FE	- R1	_	3.29	4.7	6.11	kΩ
Input resistor	RN1962FE			7	10	13	
	RN1963FE			15.4	22	28.6	
	RN1964FE			32.9	47	61.1	
	RN1965FE			1.54	2.2	2.86	
	RN1966FE			3.29	4.7	6.11	
Resistor ratio	RN1961FE to 1964FE	R1/R2	_	0.9	1.0	1.1	_
	RN1965FE			0.0421	0.0468	0.0515	
	RN1966FE	1		0.09	0.1	0.11	

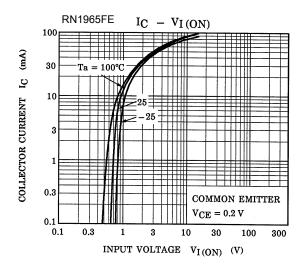
#### Q1, Q2 Common

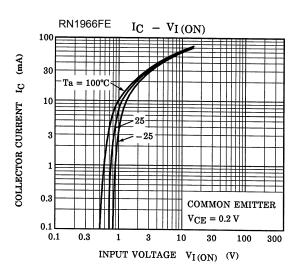




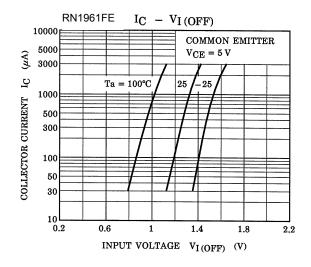


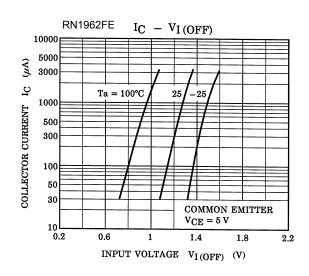


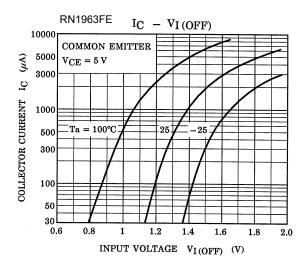


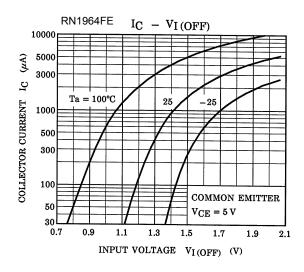


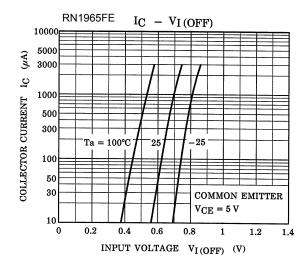
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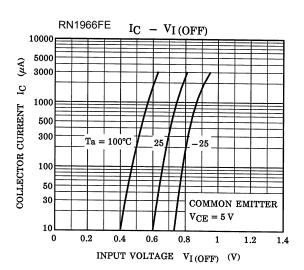






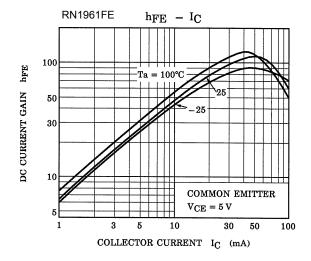


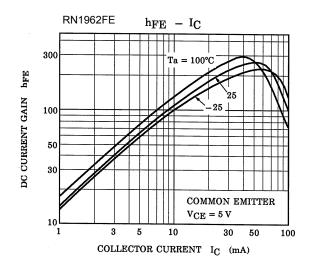


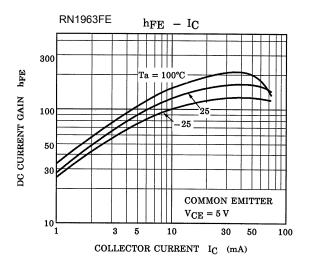


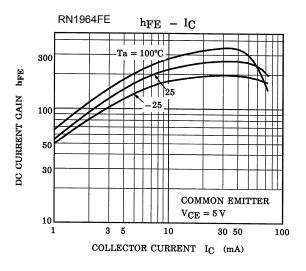


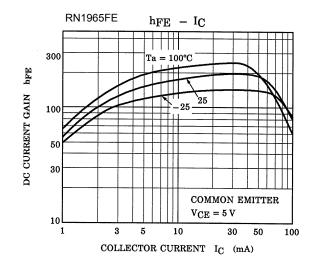
#### Q1,Q2 Common

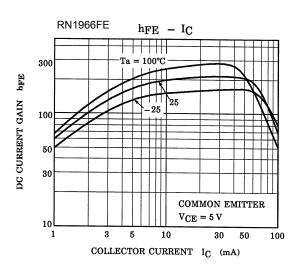




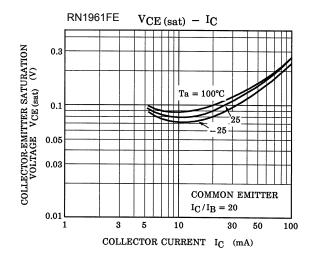


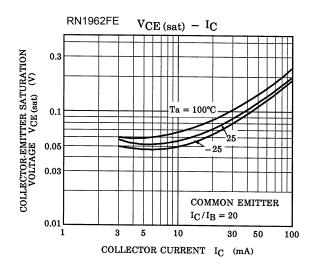


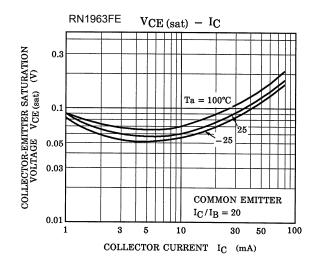


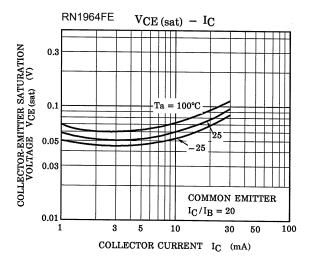


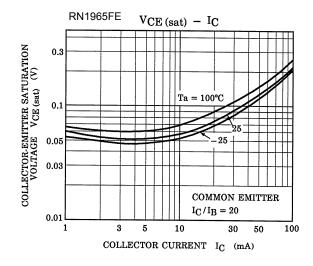
### Q1,Q2 Common

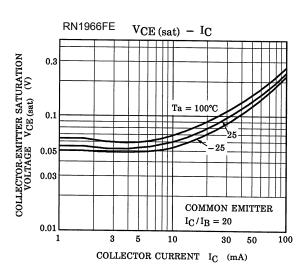




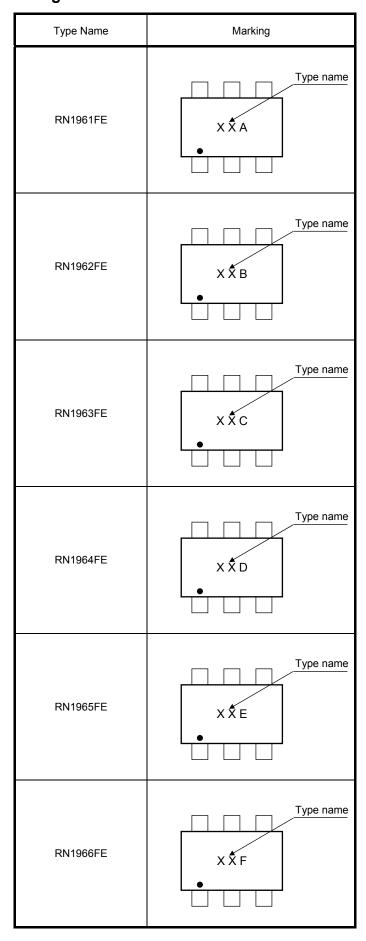








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