TOSHIBA Diode Silicon Epitaxial Planar Type

HN1D04FU

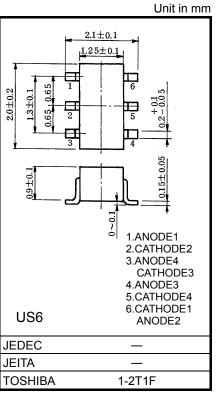
Ultra High Speed Switching Application

Low forward voltage : V_{F(3)} = 0.90V (typ.)
 Fast reverse recovery time : t_{rr} = 1.6ns (typ.)
 Small total capacitance : C_T = 0.9pF (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit	
Maximum (peak) reverse voltage	V_{RM}	85	V	
Reverse voltage	V _R	80	٧	
Maximum (peak) forward current	I _{FM}	300*	mA	
Average forward current	Io	100*	mA	
Surge current (10ms)	I _{FSM}	2*	Α	
Power dissipation	Р	200**	mW	
Junction temperature	Tj	150	°C	
Storage temperature	T _{stg}	-55~150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings



Weight: 6.8 g (typ.)

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

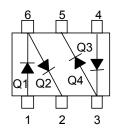
*: Where Q1 and Q2 or Q3 and Q4 are used independently or simultaneously, the Absolute Maximum Ratings per diode are 50% of those of the single diode.

Electrical Characteristics (Q1, Q2, Q3, Q4 Common; Ta = 25°C)

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit	
Forward voltage	V _{F (1)}	_	I _F = 1mA	-	0.60	_		
	V _{F (2)}	_	I _F = 10mA	ı	0.75	_	V	
	V _{F (3)}	_	I _F = 100mA	-	0.90	1.20		
Reverse current	I _{R (1)}	_	V _R = 30V	1	1	0.1	μA	
	I _{R (2)}	_	V _R = 80V	-	-	0.5	μΑ	
Total capacitance	C _T	_	V _R = 0, f = 1MHz	ı	0.9	_	pF	
Reverse recovery time	t _{rr}	_	I _F = 10mA (fig.1)	-	1.6	_	ns	

^{**:} Total rating

Pin Assignment (Top View)



Marking

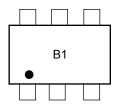
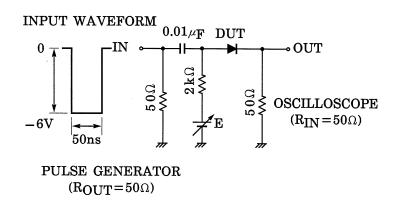
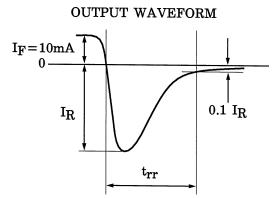
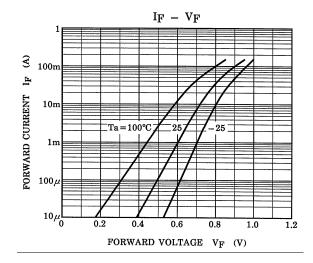


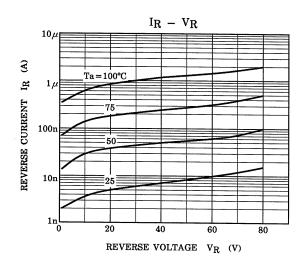
Fig. 1 Reverse Recovery Time (trr) Test Circuit

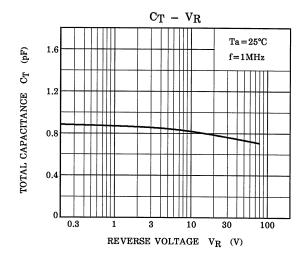


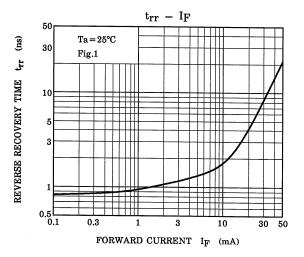


Q1, Q2, Q3, Q4 Common









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