# General purpose (dual digital transistors) EMB19

#### Features

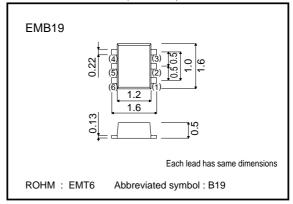
- 1) Two DTA115E chips in a EMT package.
- 2) Same size as EMT package, so same mounting machine can be used for both.
- 3) Transistor elements are independent, eliminating interference.

## ●Structure

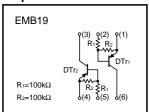
Epitaxial planar type PNP silicon transistor (Built-in resistor type)

The following characteristics apply to both DTr1 and DTr2.

## ●External dimensions (Unit : mm)



# ●Equivalent circuit



## ● Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit	
Supply voltage	Vcc	-50	V	
Input voltage	Vin	-40	V	
input voltage	VIN	10		
Outrat surrent	lo	-30	mA	
Output current	Ic (Max.)	-20	mA	
Power dissipation	Pd	Pd 150 (TOTAL)		
Junction temperature	Tj	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

st 120mW per element must not be exceeded.

# ●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Input voltage	VI(off)	-	_	-0.5	V	Vcc= -5V, Io= -100μA
	VI(on)	-3	_	_		Vo= -0.3V, Io= -1mA
Output voltage	Vo(on)	_	-0.1	-0.3	V	Io= -5mA, I:= -0.25mA
Input current	lı	_	_	-0.15	mA	V≔-5V
Output current	IO(off)	_	-	-0.5	μΑ	Vcc= -50V, V⊫0V
DC current gain	Gı	82	_	-	_	Io= -5mA, Vo= -5V
Input resistance	R <sub>1</sub>	70	100	130	kΩ	_
Resistance ratio	R2/R1	0.8	1	1.2	_	-
Transition frequency	fτ	_	250	_	MHz	Vc=-10V, I=5mA, f=100MHz *

<sup>\*</sup>Transition frequency of the device.

# Packaging specifications

	Package	Taping
	Code	T2R
Туре	Basic ordering unit (pieces)	8000
EMB19		0

#### Electrical characteristic curves

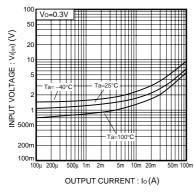


Fig.1 Input voltage vs. Output current (ON characteristics)

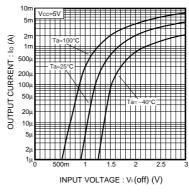


Fig.2 Output current vs. Input voltage (OFF characteristics)

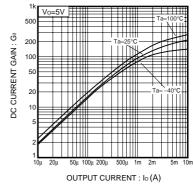


Fig.3 DC current gain vs. Output current

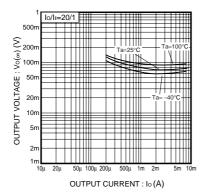


Fig.4 Output voltage vs. Output current

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