# **UNR51A9G**

## Silicon PNP epitaxial planar type

#### For digital circuits

#### ■ Features

- Costs can be reduced through downsizing of the equipment and reduction of the number of parts.
- SMini type package allowing easy automatic insertion through tape packing

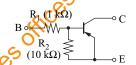
#### Package Code

- SMini3-F2
- Pin Name
  - 1: Base
  - 2: Emitter
  - 3: Collector

#### ■ Absolute Maximum Ratings $T_a = 25$ °C

| Parameter                             | Symbol           | Rating      | Unit  |  |
|---------------------------------------|------------------|-------------|-------|--|
| Collector-base voltage (Emitter open) | V <sub>CBO</sub> | -50         | V     |  |
| Collector-emitter voltage (Base open) | V <sub>CEO</sub> | -50         | V     |  |
| Collector current                     | $I_{C}$          | -80         | mA    |  |
| Total power dissipation               | P <sub>T</sub>   | 150         | mW    |  |
| Junction temperature                  | T <sub>j</sub>   | 150         | 5°C N |  |
| Storage temperature                   | T <sub>stg</sub> | -55 to +150 | CO    |  |

#### Marking Symbol: DC

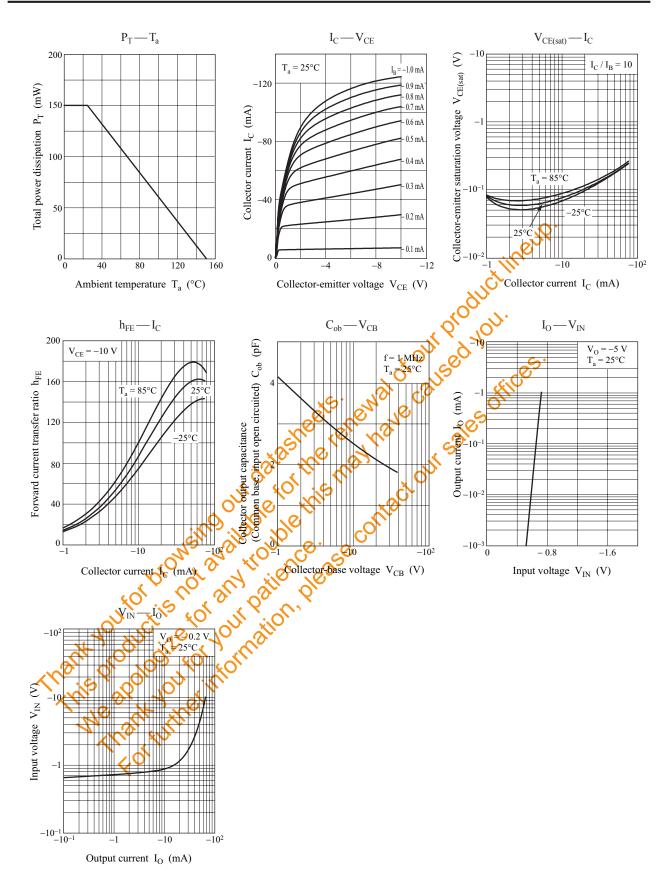


### ■ Electrical Characteristics $T_a = 2$

| Conector-base voltage (Enfluer open)  | v CBO                | -30                        | L v  |           | •        |          |      |  |  |  |
|---|----------------------|----------------------------|--|-----------|----------|----------|------|--|--|--|
| Collector-emitter voltage (Base open)   | V <sub>CEO</sub>     | -50                        | V  | ernal Con | nection  |          |      |  |  |  |
| Collector current   | $I_{\rm C}$          | -80                        | mA (   | CHIAICUI  | inection |          |      |  |  |  |
| Total power dissipation   | P <sub>T</sub>       | 150                        | mW O   | R,        | (T kΩ)   | , C      |      |  |  |  |
| Junction temperature  | T <sub>j</sub>       | 150                        | 15°C 11'0' C'0                               | Во        | $R_2$    | <b>L</b> |      |  |  |  |
| Storage temperature   | T <sub>stg</sub>     | -55 to +1 <b>5</b> 0       | 300  | (10       | ) kΩ) ₹  | • E      |      |  |  |  |
| Collector-emitter voltage (Base open) $V_{CEO}$ $-50$ $V$ Collector current $I_C$ $-80$ $mA$ Total power dissipation $P_T$ $150$ $mW$ Junction temperature $T_j$ $150$ °C  Storage temperature $T_{stg}$ $-55$ to $+150$ °C  Total power dissipation $P_T$ $150$ $mW$ Min $P_T$ |                      |                            |  |           |          |          |      |  |  |  |
| Parameter   | Symbo                | 0                          | Conditions                                   | Min       | Тур      | Max      | Unit |  |  |  |
| Collector-base voltage (Emitter open)   | VcBO                 | J⊘ <del>=</del> −10 g      | $\mathbf{L}_{\mathrm{E}} = 0$                | -50       |          |          | V    |  |  |  |
| Collector-emitter voltage (Base open)   | V <sub>CEO</sub>     | $I_C = -2 \text{ m}$       | $A, I_B = 0$                                 | -50       |          |          | V    |  |  |  |
| Collector-base cutoff current (Emitter oper   | i) I <sub>GBO</sub>  | $\nabla_{\text{CB}} = -50$ | $V, I_{E} = 0$                               |           |          | - 0.1    | μΑ   |  |  |  |
| Collector-emitter cutoff current (Base ope  | n) I <sub>CEO</sub>  | $V_{CE} = -50$             | $V, I_{B} = 0$                               |           |          | - 0.5    | μΑ   |  |  |  |
| Emitter-base cutoff current (Collector oper   | i) debo              | $V_{EB} = -6$              | $V, I_C = 0$                                 |           |          | -1.5     | mA   |  |  |  |
| Forward current transfer ratio  | h <sub>FE</sub>      | $V_{\rm CE} = -10$         | $V, I_C = -5 \text{ mA}$                     | 30        |          |          |      |  |  |  |
| Collector-emitter saturation voltage  | V <sub>CE(sat)</sub> | $I_C = -10 \text{ n}$      | $_{\rm nA}$ , $_{\rm I_B} = -0.3 \text{ mA}$ |           |          | - 0.25   | V    |  |  |  |
| Output voltage high-level   | V <sub>OH</sub>      | $V_{\rm CC} = -5$          | $V, V_B = -0.5 V, R_L = 1 k\Omega$           | -4.9      |          |          | V    |  |  |  |
| Output voltage low-level  | V <sub>OL</sub>      | $V_{\rm CC} = -5$          | $V, V_B = -2.5 V, R_L = 1 k\Omega$           |           |          | - 0.2    | V    |  |  |  |
| Input resistance  | R <sub>1</sub>       |                            |  | -30%      | 1        | +30%     | kΩ   |  |  |  |
| Resistance ratio  | $R_1/R_2$            |                            |  | 0.08      | 0.1      | 0.12     |      |  |  |  |
| Transition frequency  | $f_T$                | $V_{\rm CB} = -10$         | $V, I_E = 1 \text{ mA}, f = 200 \text{ MHz}$ |           | 80       |          | MHz  |  |  |  |

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

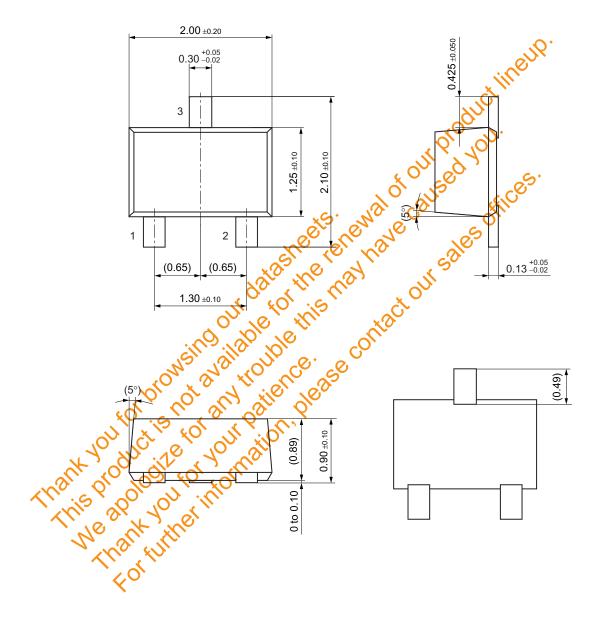
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SMini3-F2 Unit: mm



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