TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7PAU04FU

Dual Inverter (unbuffer) with 3.6 V Tolerant Input

Features

- Low voltage operation: $V_{CC} = 1.8 \sim 3.6 \text{ V}$
- Quiescent supply current: I_{CC} < 20 μA (max)

$$V_{CC} = 3.6 \text{ V}, \text{ Ta} = -40 \sim 85^{\circ}\text{C}$$

• High-speed operation: $t_{pd} = 3.5 \text{ ns (max)} (V_{CC} = 3.0 \sim 3.6 \text{ V})$

$$t_{pd} = 4.2 \text{ ns (max) (VCC} = 2.3 \sim 2.7 \text{ V})$$

$$t_{pd} = 8.4 \text{ ns (max) (V}_{CC} = 1.8 \text{ V})$$

• High-output current: $I_{OH}/I_{OL} = \pm 24 \text{ mA (min) (V}_{CC} = 3.0 \text{ V)}$

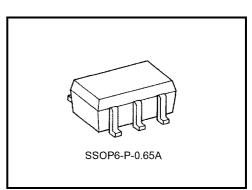
 $I_{OH}/I_{OL} = \pm 18 \text{ mA (min) (V}_{CC} = 2.3 \text{ V)}$

 I_{OH}/I_{OL} = ±6 mA (min) (V_{CC} = 1.8 V)

- Latch-up performance: ±300 mA
- ESD Performance: ±200 V (JEITA)

±2000 V (MIL)

3.6 V tolerant function for input and power down protection are provided.



Weight: 0.0068 g (typ.)

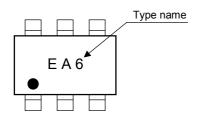
Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Power supply voltage	V _{CC}	-0.5~4.6	V
DC input voltage	V _{IN}	-0.5~4.6	V
DC output voltage	V _{OUT}	-0.5~V _{CC} + 0.5(Note 1)	V
Input diode current	I _{IK}	-50	mA
Output diode current	lok	±50 (Note 2)	mA
DC output current	l _{OUT}	±50	mA
DC V _{CC} /ground current	I _{CC}	±100	mA
Power dissipation	PD	200	mW
Storage temperature	T _{stg}	-65~150	°C

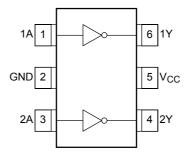
Note 1: Date retention only

Note 2: High or low state. V_{OUT} absolute maximum rating must be observed.

Marking



Pin Assignment (top view)





Logic Diagram

IN A _____ 1 OUT Y

Truth Table

А	Y
L	Н
Н	L

Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit	
Supply voltage	Vac	1.8~3.6	V	
Supply voltage	V _{CC}	1.2~3.6 (Note 3)	V	
Input voltage	V _{IN}	-0.3~3.6	V	
Output voltage	V _{OUT}	0~V _{CC} (Note 4)	٧	
	I _{OH} /I _{OL}	±24 (Note 5)		
Output Current		±18 (Note 6)	mA	
		±6 (Note 7)		
Operating temperature	T _{opr}	-40~85	°C	
Input rise and fall time	dt/dv	0~10 (Note 8)	ns/V	

Note 3: Date Retention Only

Note 4: High or low state

Note 5: $V_{CC} = 3.0 \sim 3.6 \text{ V}$

Note 6: $V_{CC} = 2.3 \sim 2.7 \text{ V}$

Note 7: $V_{CC} = 1.8 \text{ V}$

Note 8: $V_{CC} = 3.0 \text{ V}$

Electrical Characteristics

DC Characteristics (Ta = -40~85°C)

Charac	torietice	Symbol	Test Condition			Min	Max	Unit
Cilarac	teristics	Symbol			V _{CC} (V)	IVIIII	IVIAX	Offic
"H" level					1.8	0.85 × V _{CC}		
		V _{IH} —		2.3~3.6	0.8 × V _{CC}	_	V	
Input voltage	"L" level			1.8	_	0.15 × V _{CC}	V	
	L level	V _{IL}		_	2.3~3.6	_	0.2 × V _{CC}	
				$I_{OH} = -100 \mu A$	1.8~3.6	V _{CC} - 0.2	1	
				$I_{OH} = -6 \text{ mA}$	1.8	1.4	_	
				$I_{OH} = -12 \text{ mA}$	2.3	1.8	_	
	"H" level	Voн	V _{IN} = V _{IL}	$I_{OH} = -18 \text{ mA}$	2.3	1.7	_	
				$I_{OH} = -12 \text{ mA}$	2.7	2.2		
				$I_{OH} = -18 \text{ mA}$	3.0	2.4		
Output voltage				$I_{OH} = -24 \text{ mA}$	3.0	2.2	_	V
			I _{OL} = 100 μA	I _{OL} = 100 μA	1.8~3.6	_	0.2	
	I _{OH} = 6 mA	$I_{OH} = 6 \text{ mA}$	1.8	_	0.3			
			I _{OL} = 12 mA	$I_{OL} = 12 \text{ mA}$	2.3	_	0.4	
	"L" level	V_{OL}	$V_{IN} = V_{IH}$	I _{OL} = 18 mA	2.3	_	0.6	
				I _{OL} = 12 mA	2.7	_	0.4	
			I _{OL} = 18 mA	3.0	_	0.4		
			I _{OL} = 24 mA	3.0	_	0.55		
Input leakage curre	nt	I _{IN}	V _{IN} = 0~3.6 V		2.7~3.6	_	±5.0	μΑ
Quiescent supply o	urrent	Icc	V _{IN} = V _{CC} or GND		2.7~3.6	_	20.0	^
Quiescent supply current		icc	$V_{CC} \le (V_{IN}, V_{OU})$	T) ≦ 3.6 V	2.7~3.6		±20.0	μА

AC Characteristics (Ta = $-40\sim85^{\circ}$ C, input $t_r = t_f = 2.0$ ns, $C_L = 30$ pF, $R_L = 500$ Ω)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
	t _{pLH}	(Fig.1, 2)	1.8	1.0	8.4	
Propagation delay time			2.5 ± 0.2	0.8	4.2	ns
	^t pHL		3.3 ± 0.3	0.6	3.5	

For $C_L = pF$, add approximately 300 ps to the Ac maximum specification.

Dynamic Switching Characteristics (Ta = 25°C, input $t_r = t_f = 2.0$ ns, $C_L = 30$ pF)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
		$V_{IN} = 1.8 \text{ V}, V_{II} = 0 \text{ V}$ (Note 9)		0.25	
		VIN = 1.0 V, VIL = 0 V (140te 9)	1.0	0.23	
Quiet output maximum dynamic V _{OL}	V_{OLP}	$V_{IN} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$ (Note 9)	2.5	0.6	ns
		$V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ (Note 9)	3.3	0.8	
		$V_{IN} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$ (Note 9)	1.8	-0.25	
Quiet output maximum dynamic VOL	V_{OLV}	$V_{IN} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$ (Note 9)	2.5	-0.6	ns
		$V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ (Note 9)	3.3	-0.8	
		$V_{IN} = 1.8 \text{ V}, V_{IL} = 0 \text{ V}$ (Note 9)	1.8	1.5	
Quiet output maximum dynamic VOH	V_{OHP}	$V_{IN} = 2.5 \text{ V}, V_{IL} = 0 \text{ V}$ (Note 9)	2.5	1.9	ns
		$V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$ (Note 9)	3.3	2.2	

Note 9: Parameter guaranteed by design.

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Тур.	Unit
Input capacitance	C _{IN}	_		1.8, 2.5, 3.3	4	pF
Power dissipation capacitance	C _{PD}	f _{IN} = 10 MHz	(Note 10)	1.8, 2.5, 3.3	7	pF

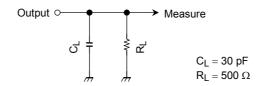
Note 10: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$

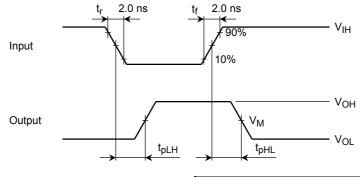
Test Circut

Figure 1



AC Waveform

Figure 2 t_{pLH}, t_{pHL}



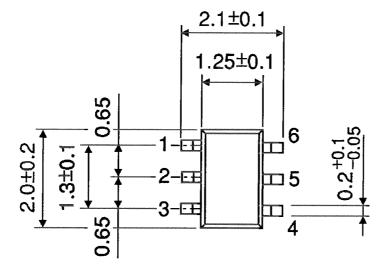
Symbol	V _{CC}					
Syllibol	$3.3\pm0.3~\textrm{V}$	$2.5\pm0.2\textrm{V}$	1.8 V			
V_{IH}	2.7 V	V _{CC}	V _{CC}			
V _M	1.5 V	V _{CC} /2	V _{CC} /2			

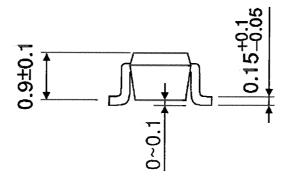
Package Dimensions

SSOP6-P-0.65A

TOSHIBA

Unit: mm





Weight: 0.0068 g (typ.)

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