TOSHIBA Field-Effect Transistor Silicon N-Channel MOS Type

## SSM5N03FE

## High-Speed Switching Applications Analog-Switch Applications

- Input impedance is high; driving current is extremely low.
- Can be directly driven by a CMOS device even at low voltage due to low gate threshold voltage.
- · High-speed switching
- · Housed in an ultra-small package suitable for high density mounting

# Absolute Maximum Ratings (Ta = 25°C) (Q1, Q2 Common)

Characteristic	Symbol	Rating	Unit
Drain-source voltage	$V_{DS}$	20	V
Gate-source voltage	$V_{GSS}$	10	V
Drain current	ΙD	100	mA
Drain power dissipation	P <sub>D</sub> (Note 1)	150	mW
Channel temperature	T <sub>ch</sub>	150	°C
Storage temperature	T <sub>stg</sub>	-55 to 150	°C

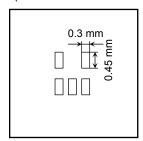
Note: Usir

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

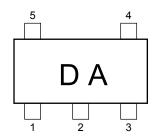
Please design the appropriate reliability upon reviewing the

Toshiba Semiconductor Reliability Handbook ("Handling Precautions") "Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

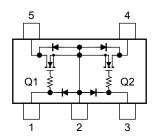
Note 1: Total rating, mounted on an FR4 board (25.4 mm  $\times$  25.4 mm  $\times$  1.6 t, Cu Pad: 0.135 mm<sup>2</sup>  $\times$  5)



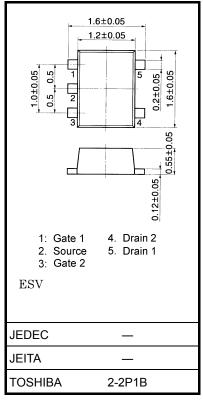
#### Marking



#### **Equivalent Circuit (top view)**



Unit: mm



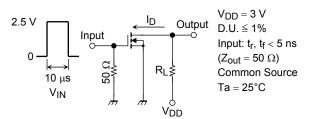
Weight: 0.003 g (typ.)

### Electrical Characteristics (Ta = 25°C) (Q1, Q2 Common)

Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	V <sub>GS</sub> = 10 V, V <sub>DS</sub> = 0 V	_	_	1	μА
Drain-source breakdown voltage		V (BR) DSS	$I_D = 100 \mu A, V_{GS} = 0 V$	20	_	_	V
Drain cutoff current		I <sub>DSS</sub>	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V	_	_	1	μА
Gate threshold voltage		V <sub>th</sub>	$V_{DS} = 3 \text{ V}, I_D = 0.1 \text{ mA}$	0.7	_	1.3	V
Forward transfer admittance		Y <sub>fs</sub>	$V_{DS} = 3 \text{ V}, I_D = 10 \text{ mA}$	25	60	_	mS
Drain-source ON-resistance		R <sub>DS</sub> (ON)	$I_D = 10 \text{ mA}, V_{GS} = 2.5 \text{ V}$	_	4	12	Ω
Input capacitance		C <sub>iss</sub>	$V_{DS} = 3 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	11.0	_	pF
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = 3 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	3.3	_	pF
Output capacitance		Coss	$V_{DS} = 3 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	9.3	_	pF
Switching time	Turn-on time	t <sub>on</sub>	$V_{DD} = 3 \text{ V, } I_{D} = 10 \text{ mA,} $ $V_{GS} = 0 \text{ to } 2.5 \text{ V}$	_	0.16	_	μ\$
	Turn-off time	t <sub>off</sub>	$V_{DD}=3$ V, $I_{D}=10$ mA, $V_{GS}=0$ to 2.5 V	_	0.19	_	

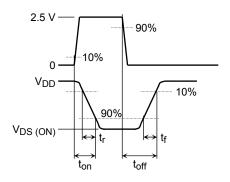
#### **Switching Time Test Circuit**









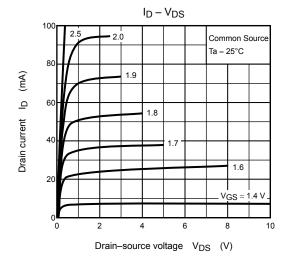


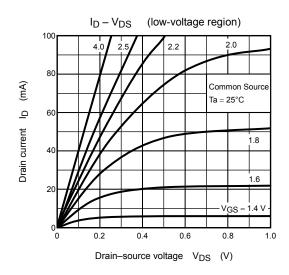
#### **Precaution**

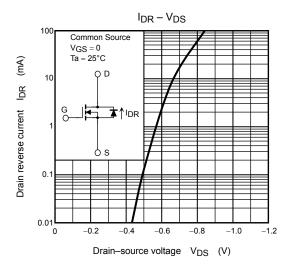
 $V_{th}$  can be expressed as the voltage between gate and source when the low operating current value is  $I_D$  = 100  $\mu$ A for this product. For normal switching operation,  $V_{GS}$  (on) requires a higher voltage than  $V_{th}$ , and  $V_{GS}$  (off) requires a lower voltage than  $V_{th}$ . (The relationship can be established as follows:  $V_{GS}$  (off)  $< V_{th} < V_{GS}$  (on).)

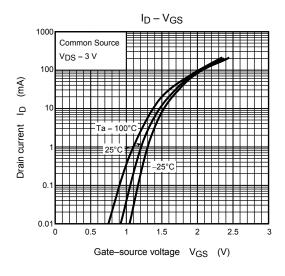
Take this into consideration when using the device.

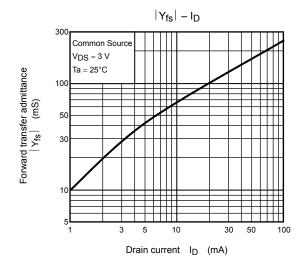
### (Q1, Q2 Common)

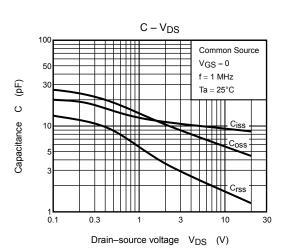




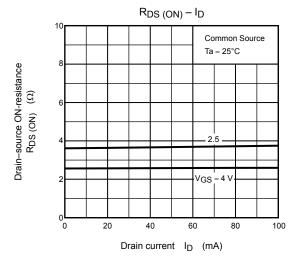


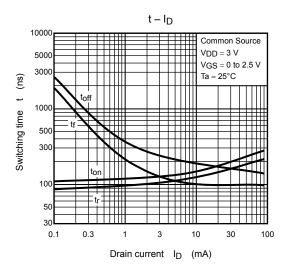


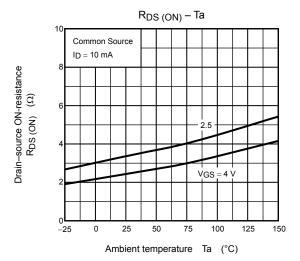


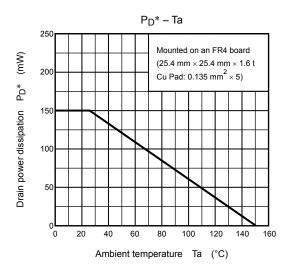


## (Q1, Q2 Common)









\*: Total rating

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20070701-EN GENERAL

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