ETR0801\_001

## Charge Pump Voltage Inverter IC

## ■ GENERAL DESCRIPTION

The XC6351A series are charge pump voltage inverter ICs that have 4 MOSFETs built in. Since highly efficient negative voltages can be generated with only 2 external capacitors connected, GaAs bias power supplies & OpAmp's negative power supplies etc., can be easily accommodated on a standard PCB.

A mini-molded, 6 pin, SOT-26 and USP-6B packages provides for space saving and makes high density mounting possible. Low power consumption and high efficiency make this series perfect for use with battery operated applications.

Since the IC's operations stop when output is shutdown via the CE (chip enable) function, total power consumption reduction is possible in applications which use this IC.

## ■ APPLICATIONS

- Cellular and portable phones
- Miniature LCD panels
- Palmtop computers, PDAs
- Various battery powered systems

### ■FEATURES

Operating Voltage Range	: 1.2V ~ 5.0V
Oscillation Frequency	: 120kHz
	: 35kHz ( custom )
Low Supply Current	: 310 µ A ( TYP. )
	100 <i>µ</i> A
	(35kHz custom TYP.)
High Efficiency	: 90% (TYP.) ( RL = $2k\Omega$ )
Stand-by Current	: 2.0 µ A (MAX.)
CE(Chip Enable) Function	
Ultra Small Package	: SOT-26 ,USP-6B

## ■ TYPICAL APPLICATION CIRCUIT



### ■ TYPICAL PERFORMANCE CHARACTERISTICS



# XC6351A Series

## ■ PIN CONFIGURATION





\*The dissipation pad for the USP-6B package should be solder-plated in recommended mount pattern and metal masking so as to enhance mounting strength and heat release.

If the pad needs to be connected to other pins, it should be connected to the VIN pin.

### SOT-26

(TOP VIEW)

## ■ PIN ASSIGNMENT

PIN NUMBER		SVMROI	FUNCTION	
SOT-26	USP-6B	STMBOL	FUNCTION	
1	6	GND	Ground	
2	5	CE/	Chip Enable (Low Active)	
3	4	C1+	External Capacitor +Pin	
4	3	Vout	Reverse Output	
5	2	Vin	Power Supply	
6	1	C1-	External Capacitor -Pin	

## ■PRODUCT CLASSIFICATION

### Ordering Information

### XC6351A 12345

DESIGNATOR	DESCRIPTION	SYMBOL	DESCRIPTION
1 9 9		120	: 120kHz
	Oscillation Frequency	035	: 35kHz (custom)
	Package	М	: SOT-26
		D	: USP-6B
Ē	Dovice Orientation	R	: Embossed tape, standard feed
3		L	: Embossed tape, reverse feed



Note:

1. In operation, the following conditions will be repeated alternately:

P1 & N3 ON: N2 & N4 OFF P1 & N3 OFF: N2 & N4 ON

2. In standby mode, P1, N3 & N4 will be ON and N2 will be OFF. The output pin Vour will be connected to GND.

				Ta = 25°C	
PARAMETER		SYMBOL RATINGS		UNITS	
VIN Input Volta	ige	Vin	6.0	V	
Vout Pin Volta	ige	Vout	-6~0.3	V	
C1+ Pin Volta	ge	C1+	-0.3~VIN + 0.3	V	
C1- Pin Voltage		C1-	Vout - 0.3~0.3	V	
CE/ Pin Voltage		CE/	-0.3~VIN + 0.3	V	
IOUT Pin Current		Ιουτ	50	mA	
Power Dissipation	SOT-26	Dd	150	m\\/	
Power Dissipation	USP-6B	ru ru	100	11100	
Operating Temperature Range		Topr	-30~+80	C°	
Storage Temperature Range		Tstg	-40~+125	C°	

## ■ABSOLUTE MAXIMUM RATINGS

Note: Voltage is all ground standardized.

## ■ ELECTRICAL CHARACTERISTICS

FOSC=120kHz, Ta=25°C

PARAMETER	SYMBOL	CONDITIONS	MIN.	TYP.	MAX.	UNITS	CIRCUIT
Supply Current	IDD		-	310	520	μA	1
Operating Voltage Range	Vin	$RL=5k\Omega$	1.2	-	5.0	V	2
Oscillation Frequency	FOSC		75	120	192	kHz	1
Power Transition Efficiency	EFFI	$R_L=2k\Omega$	-	90	-	%	2
Voltage Transition Efficiency	Veffi	RL=∞	95	-	-	%	2
Output Impedance	Rout	$RL=5k\Omega$	-	45	90	Ω	2
Stand -by Current	Istb	CE/=VIN	-	-	2.0	μA	3
CE/ 'H' Level Voltage	VCEH		0.9	-	-	V	3
CE/ 'L' Level Voltage	VCEL		-	-	0.25	V	3

Measuring Conditions: Unless otherwise stated, VIN = 5.0V, CE/ = 0V

## ■ TYPICAL APPLICATION CIRCUIT

Standard Circuit



\* With the custom 35kHz frequency, C1 = C2 = 3.3  $\mu$  F

## ■ NOTES ON USE

- 1. Please use the IC & external components: within the specified electrical characteristics range and ensure that absolute maximum ratings are not exceeded.
- 2. For C1 & C2, please use a capacitor with as small an ESR value as possible.
- 3. In order to reduce impedance between the IC's input pin and the power supply, we recommend that a capacitor (CIN) be connected to the input side.
- 4. If an external power supply is applied to the output pin in order to have VOUT connected to GND during standby, large current flows through the IC are a possibility. Further, do not use a capacitor at C2 that has a large capacitance value.

## ■TEST CIRCUITS

Circuit 1



Circuit 2



Circuit 3



External components:

 $C_{IN} = 1 \, \mu \, F$  (ceramic capacitor)

C1 = C2 = 1  $\mu$  F (ceramic capacitor)\*

\* With the custom 35kHz frequency, C1 = C2 =  $3.3 \,\mu$  F

## ■TYPICAL PERFORMANCE CHARACTERISTICS



CIN=1.0  $\mu$  F(ceramic)

CIN=1.0 µ F(ceramic)

#### Input Voltage:VIN (V)

-40 -20 Temp.:Topr

(°C)





●USP-6B



Note : Pin 1 is larger than the other pins.

## ■ PACKAGING INFORMATION (Continued)



### ●USP-6B Recommended Metal Mask Design



## ■MARKING RULE

### SOT-26



### 1 Represents product series

MARK	PRODUCT SERIES
A	XC6351AxxxMx

#### 2,3 Represents oscillation frequency

MARK				
2	3	USCILLATION FREQUENCE	PRODUCT SERIES	
0	3	35kHz	XC6351A035Mx	
1	2	120kHz	XC6351A120Mx	

#### ④ Represents production lot number

0 to 9, A to Z repeated (G, I, J, O, Q, W excepted)

### (1,2,3) Represents product series

	MARK		
1	2	3	FRODUCT SERIES
5	1	А	XC6351AxxxDx

#### (4),(5) Represents oscillation frequency

MARK				
4	5	USCILLATION FREQUENCY	PRODUCT SERIES	
0	3	35kHz	XC6351A035Dx	
1	2	120kHz	XC6351A120Dx	

6 Represents production lot number

0 to 9,A to Z repeated (G, I, J, O, Q, W excepted) Note: No character inversion used.

### OUSP-6B



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