Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type

SSM3K15FS

High Speed Switching Applications Analog Switching Applications

• Compact package suitable for high-density mounting

• Low ON-resistance : $R_{on} = 4.0 \Omega (max) (@V_{GS} = 4 V)$

: $R_{on} = 7.0 \Omega (max) (@V_{GS} = 2.5 V)$

Maximum Ratings (Ta = 25°C)

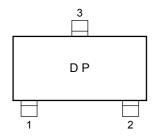
Characteristic		Symbol	Rating	Unit	
Drain-Source voltage		V _{DS}	30	V	
Gate-Source voltage		V _{GSS}	±20	V	
Drain current	DC	I _D	100	mA	
	Pulse	I _{DP}	200		
Drain power dissipation (Ta = 25°C)		P _D	100	mW	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	

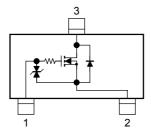
1. GATE 2. SOURCE 3. DRAIN SSM JEDEC — JEITA — TOSHIBA 2-2H1B

Weight: 2.4 mg (typ.)

Marking







Handling Precaution

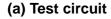
When handling individual devices (which are not yet mounted on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

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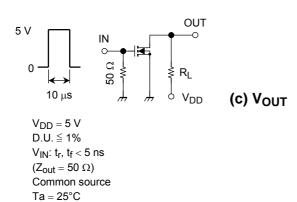
Electrical Characteristics (Ta = 25°C)

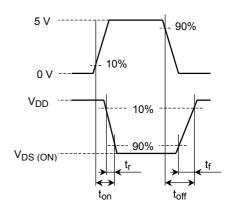
Characteristic		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$	_	_	±1	μΑ
Drain-Source breakdown voltage		V _{(BR)DSS}	$I_D = 0.1 \text{ mA}, V_{GS} = 0$	30	_	_	V
Drain Cut-off current		I _{DSS}	V _{DS} = 30 V, V _{GS} = 0	_	_	1	μА
Gate threshold voltage		V _{th}	$V_{DS} = 3 \text{ V}, I_D = 0.1 \text{ mA}$	0.8	_	1.5	V
Forward transfer admittance		Y _{fs}	$V_{DS} = 3 \text{ V}, I_D = 10 \text{ mA}$	25	_	_	mS
Drain-Source ON resistance		R _{DS (ON)}	$I_D = 10 \text{ mA}, V_{GS} = 4 \text{ V}$	_	2.2	4.0	Ω
			I _D = 10 mA, V _{GS} = 2.5 V	_	4.0	7.0	
Input capacitance		C _{iss}	$V_{DS} = 3 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	7.8	_	pF
Reverse transfer capacitance		C _{rss}	$V_{DS} = 3 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	3.6	_	pF
Output capacitance		Coss	$V_{DS} = 3 \text{ V}, V_{GS} = 0, f = 1 \text{ MHz}$	_	8.8	_	pF
Switching time	Turn-on time	t _{on}	V _{DD} = 5 V, I _D = 10 mA, V _{GS} = 0~5 V	_	50	_	ns
	Turn-off time	t _{off}		_	180	_	

Switching Time Test Circuit



(b) V_{IN}





Precaution

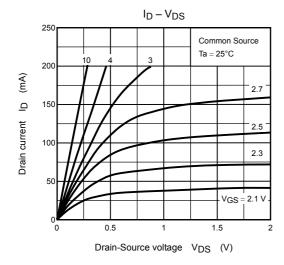
 V_{th} can be expressed as voltage between gate and source when low operating current value is ID = 100 μA for this product. For normal switching operation, V_{GS} (on) requires higher voltage than V_{th} and V_{GS} (off) requires lower voltage than V_{th} .

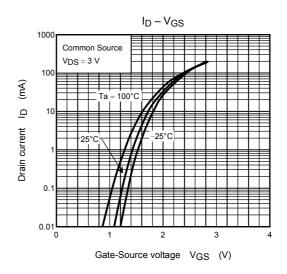
2

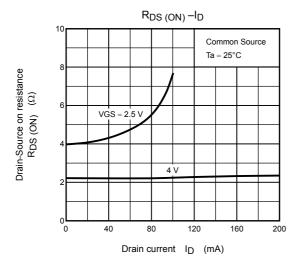
(relationship can be established as follows: $V_{\rm GS}$ (off) < $V_{\rm th}$ < $V_{\rm GS}$ (on))

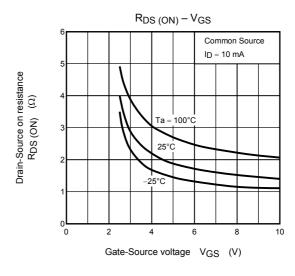
Please take this into consideration for using the device.

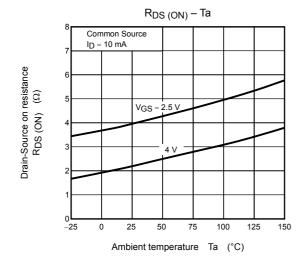
VGS recommended voltage of 2.5 V or higher to turn on this product.

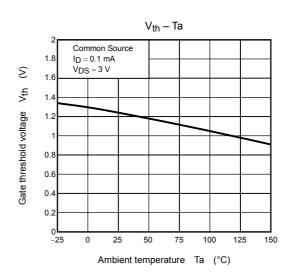


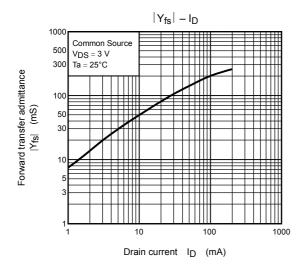


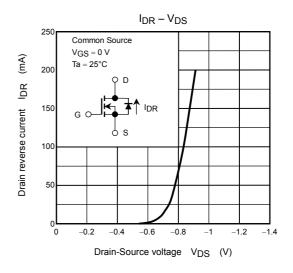


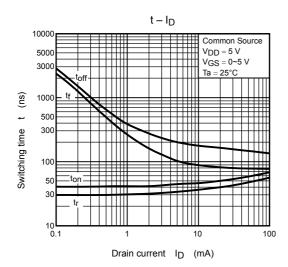


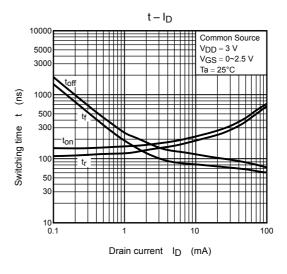


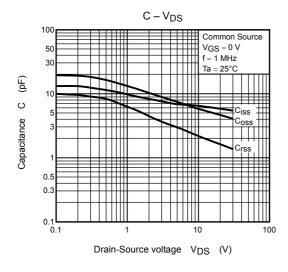


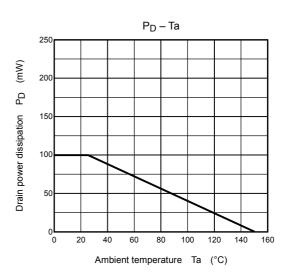












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RESTRICTIONS ON PRODUCT USE

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