

# HSC276A

# Silicon Schottky Barrier Diode for Mixer

REJ03G0600-0100

(Previous: ADE-208-836)

Rev.1.00 Apr 13, 2005

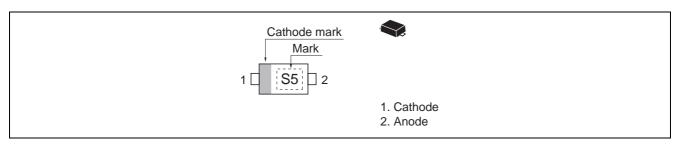
### **Features**

- High forward current, Low capacitance.
- Ultra small Flat Lead Package (UFP) is suitable for surface mount design.

## **Ordering Information**

| Type No. | Laser Mark | Package Name | Package Code<br>(Previous Code) |
|----------|------------|--------------|---------------------------------|
| HSC276A  | S5         | UFP          | PWSF0002ZA-A                    |
|          |            |              | (UFP)                           |

## **Pin Arrangement**



## **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

| Item                            | Symbol Value   |             | Unit |
|---------------------------------|----------------|-------------|------|
| Repetitive peak reverse voltage | $V_{RRM}$      | 5           | V    |
| Reverse voltage                 | V <sub>R</sub> | 3           | V    |
| Average rectified current       | Io             | 30          | mA   |
| Junction temperature            | Tj             | 125         | °C   |
| Storage temperature             | Tstg           | −55 to +125 | °C   |

## **Electrical Characteristics**

 $(Ta = 25^{\circ}C)$ 

| Item             | Symbol         | Min | Тур | Max  | Unit | Test Condition   |
|------------------|----------------|-----|-----|------|------|--|
| Reverse voltage  | $V_R$          | 3.0 | _   | _    | V    | I <sub>R</sub> = 1 mA  |
| Reverse current  | I <sub>R</sub> | _   | _   | 50   | μА   | V <sub>R</sub> = 0.5 V   |
| Forward current  | I <sub>F</sub> | 35  | _   | _    | mA   | $V_F = 0.5V$   |
| Capacitance      | С              | _   | _   | 0.85 | pF   | V <sub>R</sub> = 0.5 V, f = 1 MHz  |
| ESD-Capability * | _              | 30  | _   | _    | V    | C = 200 pF, R = 0 $\Omega$ , Both forward and reverse direction 1 pulse. |

Note: Failure criterion ;  $I_R \ge 100 \mu A$  at  $V_R = 0.5 V$ 

## **Main Characteristic**

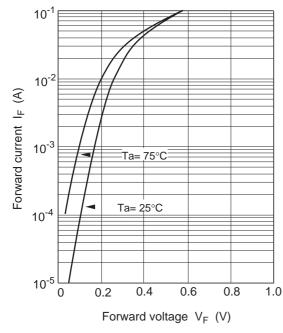


Fig.1 Forward current vs. Forward voltage

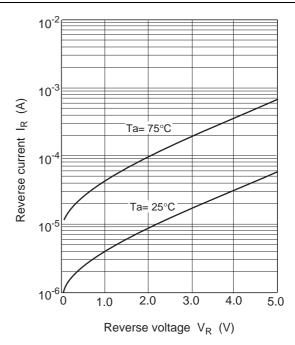


Fig.2 Reverse current vs. Reverse voltage

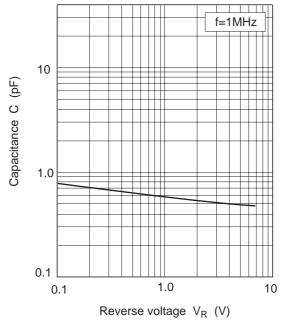
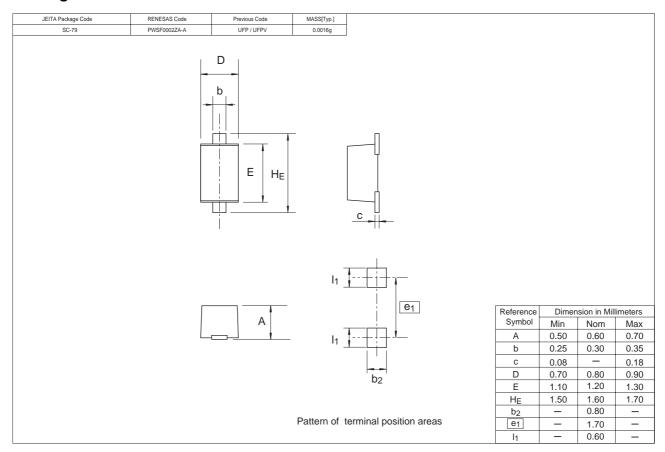


Fig.3 Capacitance vs. Reverse voltage

## **Package Dimensions**



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