### TENTATIVE

TOSHIBA Diode Silicon Epitaxial Planar Type

## **1SS403**

#### **High Voltage Switching Applications**

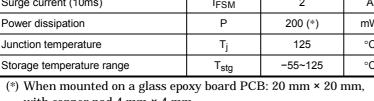
Unit in mm

Two-pin small packages are suitable for higher mounting densities.

Excellent in forward current and forward voltage characteristics  $: V_{F}(2) = 0.90V \text{ (typ.)}$ Fast reverse recovery time :  $t_{rr} = 60$ ns (typ.) Small total capacitance  $: C_T = 1.5pF (typ.)$ 

#### Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit
Maximum (peak) reverse voltage	$V_{RM}$	250	V
Reverse voltage	V <sub>R</sub>	200	V
Maximum (peak) forward current	I <sub>FM</sub>	300	mA
Average forward current	Io	100	mA
Surge current (10ms)	I <sub>FSM</sub>	2	Α
Power dissipation	Р	200 (*)	mW
Junction temperature	Tj	125	°C
Storage temperature range	T <sub>stg</sub>	-55~125	°C



CATHODE MARK 25 0±0.05 +0.1 0.3 - 0.05 + 0.1 0.15 - 0.06 **JEDEC EIAJ TOSHIBA** 1-1E1A

Weight: 4.5 mg

#### **Electrical Characteristics (Ta = 25°C)**

Characteristic	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit	
Forward voltage	V <sub>F (1)</sub>	_	I <sub>F</sub> = 10mA	-	0.72	1.0	V	
	V <sub>F (2)</sub>	_	I <sub>F</sub> = 100mA	_	0.90	1.2	v	
Reverse current	I <sub>R (1)</sub>	_	V <sub>R</sub> = 50V	1	1	0.1		
	I <sub>R (2)</sub>	_	V <sub>R</sub> = 200V	-	-	1.0	μА	
Total capacitance	C <sub>T</sub>	_	V <sub>R</sub> = 0, f = 1MHz	_	1.5	3.0	pF	
Reverse recovery time	t <sub>rr</sub>	_	I <sub>F</sub> = 10mA (Fig. 1)	_	10	60	ns	

damage to property.

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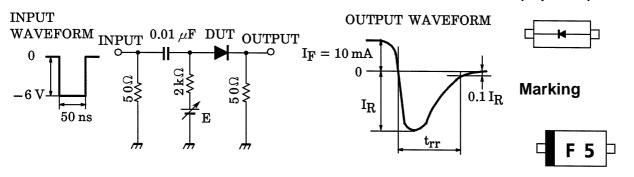
with copper pad 4 mm × 4 mm.

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#### Fig.1 Reverse Recovery Time (t<sub>rr</sub>) Test Circuit

# Equivalent Circuit (Top View)



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