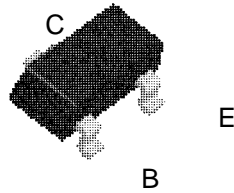


FMMT449



SuperSOT™-3

NPN Low Saturation Transistor

These devices are designed with high current gain and low saturation voltage with collector currents up to 2A continuous. Sourced from Process NB.

Absolute Maximum Ratings* T_A = 25°C unless otherwise noted

Symbol	Parameter	FMMT449	Units
V _{CEO}	Collector-Emitter Voltage	30	V
V _{CBO}	Collector-Base Voltage	50	V
V _{EBO}	Emitter-Base Voltage	5	V
I _C	Collector Current - Continuous - Peak Pulse Current	1 2	A
T _J , T _{stg}	Operating and Storage Junction Temperature Range	-55 to +150	°C

*These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

NOTES:

- 1) These ratings are based on a maximum junction temperature of 150°C.
- 2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

Thermal Characteristics T_A = 25°C unless otherwise noted

Symbol	Characteristic	Max	Units
		FMMT449	
P _D	Total Device Dissipation* Derate above 25°C	500 4	mW mW/°C
R _{θJA}	Thermal Resistance, Junction to Ambient	250	°C/W

*Device mounted on FR-4 PCB 4.5" X 5"; mounting pad 0.02 in² of 2oz copper.

NPN Low Saturation Transistor

(continued)

Electrical Characteristics

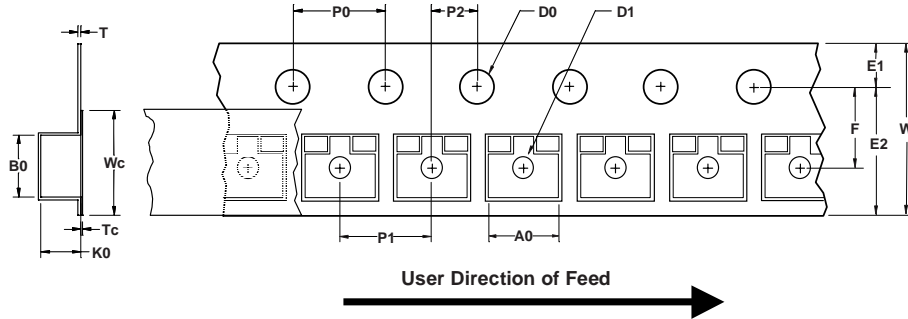
$T_A = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Max	Units
OFF CHARACTERISTICS					
BV_{CEO}	Collector-Emitter Breakdown Voltage	$I_C = 10\text{ mA}$	30		V
BV_{CBO}	Collector-Base Breakdown Voltage	$I_C = 1\text{ mA}$	50		V
BV_{EBO}	Emitter-Base Breakdown Voltage	$I_E = 100\ \mu\text{A}$	5		V
I_{CBO}	Collector Cutoff Current	$V_{CB} = 40\text{ V}$ $V_{CB} = 40\text{ V}, T_A = 100^\circ\text{C}$		100 10	nA uA
I_{EBO}	Emitter Cutoff Current	$V_{EB} = 4\text{ V}$		100	nA
ON CHARACTERISTICS*					
h_{FE}	DC Current Gain	$I_C = 50\text{ mA}, V_{CE} = 2\text{ V}$ $I_C = 500\text{ mA}, V_{CE} = 2\text{ V}$ $I_C = 1\text{ A}, V_{CE} = 2\text{ V}$ $I_C = 2\text{ A}, V_{CE} = 2\text{ V}$	70 100 80 40	300	-
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C = 1\text{ A}, I_B = 100\text{ mA}$ $I_C = 2\text{ A}, I_B = 200\text{ mA}$		500 1.0	mV V
$V_{BE(sat)}$	Base-Emitter Saturation Voltage	$I_C = 1\text{ A}, I_B = 100\text{ mA}$		1.25	V
$V_{BE(on)}$	Base-Emitter On Voltage	$I_C = 1\text{ A}, V_{CE} = 2\text{ V}$		1	V
SMALL SIGNAL CHARACTERISTICS					
C_{obo}	Output Capacitance	$V_{CB} = 10\text{ V}, I_E = 0, f = 1\text{ MHz}$		15	pF
f_T	Transition Frequency	$I_C = 50\text{ mA}, V_{CE} = 10\text{ V}, f = 100\text{ MHz}$	150		MHz

*Pulse Test: Pulse Width $\leq 300\ \mu\text{s}$, Duty Cycle $\leq 2.0\%$

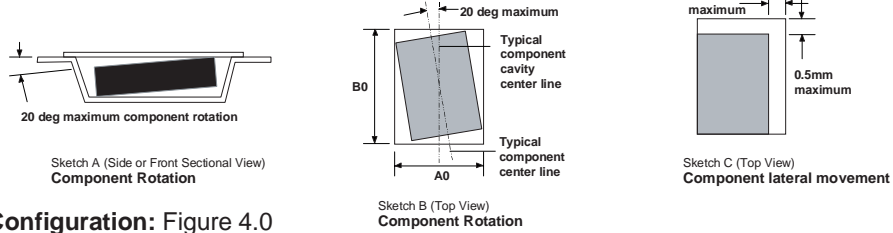
SuperSOT™-3 Tape and Reel Data and Package Dimensions, continued

SSOT-3 Embossed Carrier Tape Configuration: Figure 3.0

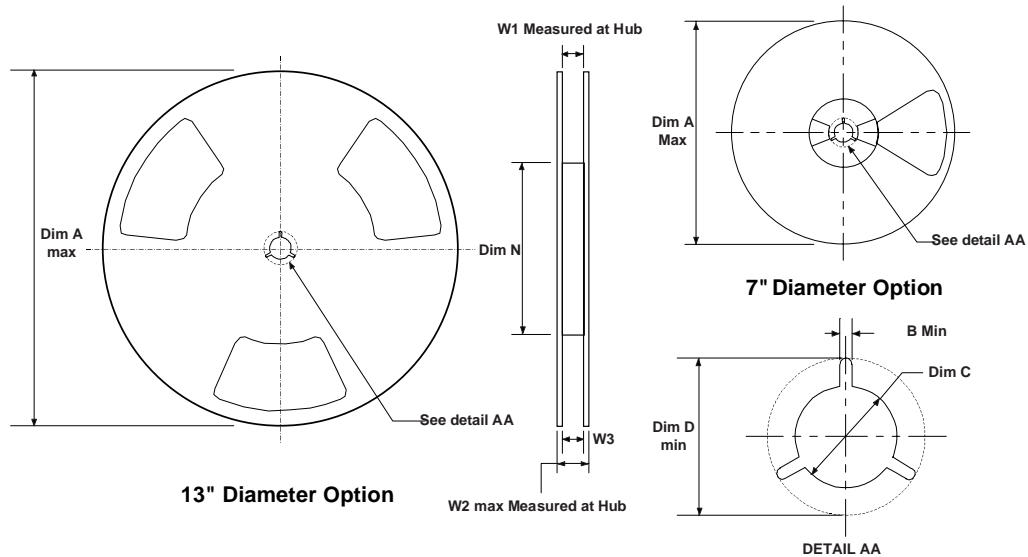


Dimensions are in millimeter														
Pkg type	A0	B0	W	D0	D1	E1	E2	F	P1	P0	K0	T	Wc	Tc
SSOT-3 (8mm)	3.15 +/-0.10	2.77 +/-0.10	8.0 +/-0.3	1.55 +/-0.05	1.125 +/-0.125	1.75 +/-0.10	6.25 min	3.50 +/-0.05	4.0 +/-0.1	4.0 +/-0.1	1.30 +/-0.10	0.228 +/-0.013	5.2 +/-0.3	0.06 +/-02

Notes: A0, B0, and K0 dimensions are determined with respect to the EIA/Jedec RS-481 rotational and lateral movement requirements (see sketches A, B, and C).



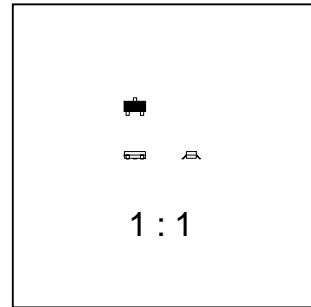
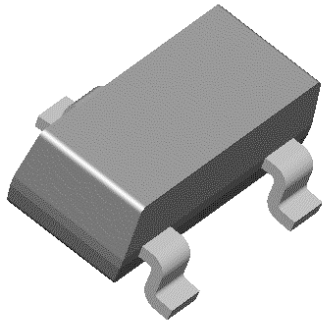
SSOT-3 Reel Configuration: Figure 4.0



Dimensions are in inches and millimeters									
Tape Size	Reel Option	Dim A	Dim B	Dim C	Dim D	Dim N	Dim W1	Dim W2	Dim W3 (LSL-USL)
8mm	7" Dia	7.00 177.8	0.059 1.5	512 +0.020/-0.008 13 +0.5/-0.2	0.795 20.2	2.165 55	0.331 +0.059/-0.000 8.4 +1.5/0	0.567 14.4	0.311 - 0.429 7.9 - 10.9
8mm	13" Dia	13.00 330	0.059 1.5	512 +0.020/-0.008 13 +0.5/-0.2	0.795 20.2	4.00 100	0.331 +0.059/-0.000 8.4 +1.5/0	0.567 14.4	0.311 - 0.429 7.9 - 10.9

SuperSOT™-3 Tape and Reel Data and Package Dimensions, continued

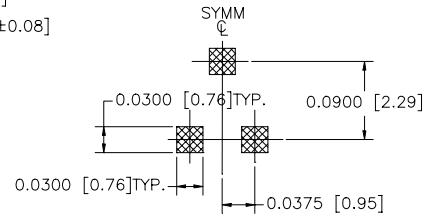
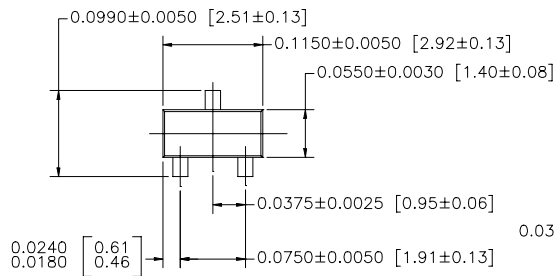
SuperSOT™-3 (FS PKG Code 32)



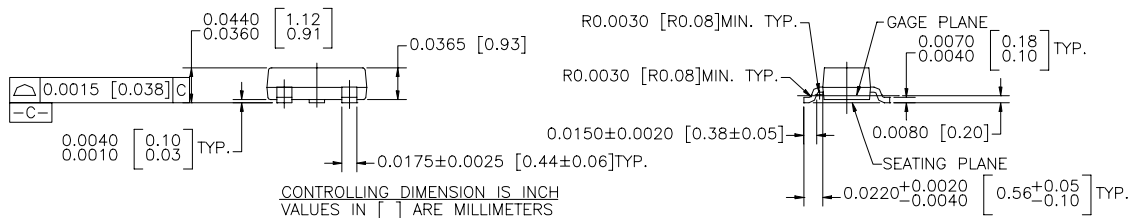
Scale 1:1 on letter size paper

Dimensions shown below are in:
inches [millimeters]

Part Weight per unit (gram): 0.0097



LAND PATTERN RECOMMENDATION



CONTROLLING DIMENSION IS INCH
 VALUES IN [] ARE MILLIMETERS

NOTES : UNLESS OTHERWISE SPECIFIED

SUPER SOT , 3 LEADS

- STANDARD LEAD FINISH TO BE 150 MICRINCHES / 3.81 MICROMETERS
MINIMUM TIN/LEAD (SOLDER) ON COPPER.
- NO JEDEC REGISTRATION AS OF DEC. 1995.

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FACT Quiet Series™	Quiet Series™	
FAST®	SuperSOT™-3	
FASTr™	SuperSOT™-6	
GTO™	SuperSOT™-8	
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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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