

NPN & PNP General Purpose Amplifier

This complementary device is for use as a medium power amplifier and switch requiring collector currents up to 500 mA. Sourced from Process 19 and 63. See FFB2222A (NPN) and FFB2907A (PNP) for characteristics.

Absolute Maximum Ratings* T₄ = 25°C unless otherwise noted

Symbol	Parameter	Value	Units	
V _{CEO}	Collector-Emitter Voltage	30	V	
V _{CBO}	Collector-Base Voltage	60	V	
V _{EBO}	Emitter-Base Voltage	5.0	V	
lc	Collector Current - Continuous	500	mA	
T _J , T _{stg}	Operating and Storage Junction Temperature Range	-55 to +150	°C	

*These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

NOTES:

1) These ratings are based on a maximum junction temperature of 150 degrees C.

2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.
3) All voltages (V) and currents (A) are negative polarity for PNP transistors.

Thermal Characteristics $T_A = 25^{\circ}C$ unless otherwise noted

Symbol	Characteristic	м	Max Units	
		FFB2227A	FMB2227A	
PD	Total Device Dissipation Derate above 25°C	300 2.4	700 5.6	mW mW/°C
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	415	180	°C/W

© 1998 Fairchild Semiconductor Corporation

NPN & PNP General Purpose Amplifier (continued)

20

ns

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
OFF CHAP	RACTERISTICS					
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage*	$I_{\rm C} = 10 \text{ mA}, I_{\rm B} = 0$	30			V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	$I_{C} = 10 \ \mu A, \ I_{E} = 0$	60			V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	$I_{E} = 10 \ \mu A, I_{C} = 0$	5.0			V
I _{CBO}	Collector Cutoff Current	$V_{CB} = 50 \text{ V}, I_E = 0$			30	nA
I _{EBO}	Emitter Cutoff Current	$V_{EB} = 3.0 \text{ V}, I_{C} = 0$			30	nA
h _{FE}	DC Current Gain	$I_{C} = 1.0 \text{ mA}, V_{CE} = 10 \text{ V}$ $I_{C} = 10 \text{ mA}, V_{CE} = 10 \text{ V}$ $I_{C} = 150 \text{ mA}, V_{CE} = 10 \text{ V}^{*}$	50 75 100			
V _{CE(sat)}	Collector-Emitter Saturation Voltage*	$\label{eq:lc} \begin{array}{l} I_{C} = 300 \text{mA}, \ V_{CE} = 10 \ \text{V}^{\star} \\ I_{C} = 150 \ \text{mA}, \ I_{B} = 15 \ \text{mA} \\ I_{C} = 300 \ \text{mA}, \ I_{B} = 30 \ \text{mA} \end{array}$	30		0.4	V V
V _{BE(sat)}	Base-Emitter Saturation Voltage*	$I_{\rm C} = 150 \text{ mA}, I_{\rm B} = 15 \text{ mA}$			1.3	V
SMALL SI	GNAL CHARACTERISTICS Current Gain - Bandwidth Product	$I_{C} = 50 \text{ mA}, V_{CE} = 20 \text{ V},$ f = 100 MHz		250		MHz
C _{obo}	Output Capacitance	$V_{CB} = 10 \text{ V}, \text{ I}_{E} = 0, \text{ f} = 100 \text{ kHz}$		4.0		pF
Cibo	Input Capacitance	V _{EB} = 2.0 V, I _C = 0, f = 100 kHz		12		pF
NF	Noise Figure	I_{C} = 100 μA, V _{CE} = 10 V, R _S = 1.0 kΩ, f = 1.0 kHz		2.0		dB
SWITCHII	NG CHARACTERISTICS					
t _{on}	Turn-on Time	$V_{CC} = 30 \text{ V}, \text{ I}_{C} = 150 \text{ mA},$		30		ns
t _d	Delay Time	I _{B1} = 15 mA		8.0		ns
tr	Rise Time	1		20		ns
t _{off}	Turn-off Time	$V_{CC} = 6.0 \text{ V}, I_{C} = 150 \text{ mA}$	1	80		ns
ts	Storage Time	I _{B1} = I _{B2} = 15 mA		60		ns
		4			+	

*****Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%

Fall Time

t_f

NOTE: All voltages (V) and currents (A) are negative polarity for PNP transistors.

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEx™ Bottomless™ CoolFET™ CROSSVOLT™ DOME™ E²CMOS[™] EnSigna™ FACT™ FACT Quiet Series[™] FAST[®]

FASTr™ GlobalOptoisolator™ GTO™ HiSeC™ **ISOPLANAR™** MICROWIRE™ OPTOLOGIC™ **OPTOPLANAR™** PACMAN™ POP™

PowerTrench[®] QFET™ QS™ QT Optoelectronics[™] Quiet Series[™] SILENT SWITCHER® SMART START™ SuperSOT[™]-3 SuperSOT[™]-6 SuperSOT[™]-8

SyncFET™ TinyLogic™ UHC™ VCX™

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.

2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.
	1	Rev G