Ordering number: EN3775A

SANYO

No.3775A

2SK1474

N-Channel MOS Silicon FET

Very High-Speed Switching Applications

Features

- Low ON resistance.
- Very high-speed switching.
- Low-voltage drive.

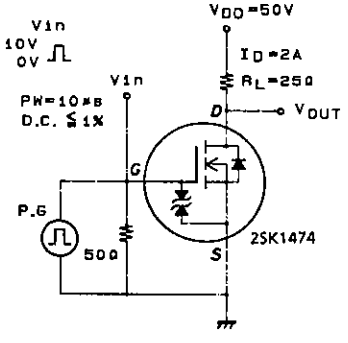
Absolute Maximum Ratings at Ta = 25°C

				unit
Drain to Source Voltage	V_{DSS}		100	V
Gate to Source Voltage	V_{GSS}		±15	V
Drain Current(DC)	I_D		4	A
Drain Current(Pulse)	I_{DP}	PW≦10μs, duty cycle≦1%	16	A
Allowable Power Dissipation	P_D	Tc = 25°C	20	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		−55 to +150	°C

Electrical Characteristics at Ta = 25°C

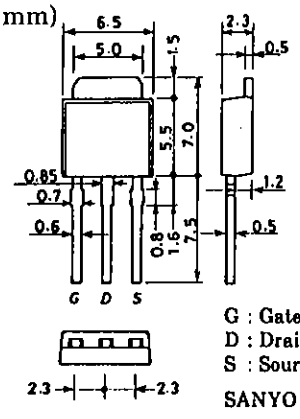
			min	typ	max	unit
D-S Breakdown Voltage	$V_{(BR)DSS}$	I_D = 1mA, V_{GS} = 0	100			V
G-S Breakdown Voltage	$V_{(BR)GSS}$	I_G = ±100μA, V_{DS} = 0	±15			V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = 100V, V_{GS} = 0			100	μA
Gate to Source Leakage Current	I_{GSS}	V_{GS} = ±12V, V_{DS} = 0			±10	μA
Cutoff Voltage	$V_{GS(off)}$	V_{DS} = 10V, I_D = 1mA	1.0		2.0	V
Forward Transfer Admittance	$\|y_{fs}\|$	V_{DS} = 10V, I_D = 2A	2.5	4		S
Static Drain to Source on State Resistance	$R_{DS(on)}$	I_D = 2A, V_{GS} = 10V		0.3	0.4	Ω
	$R_{DS(on)}$	I_D = 2A, V_{GS} = 4V		0.4	0.55	Ω
Input Capacitance	Ciss	V_{DS} = 20V, f = 1MHz		380		pF
Output Capacitance	Coss	V_{DS} = 20V, f = 1MHz		80		pF
Reverse Transfer Capacitance	Crss	V_{DS} = 20V, f = 1MHz		15		pF
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit.		10		ns
Rise Time	t_r	〃		13		ns
Turn-OFF Delay Time	$t_{d(off)}$	〃		70		ns
Fall Time	t_f	〃		30		ns
Diode Forward Voltage	V_{SD}	I_S = 4A, V_{GS} = 0		1.0	1.5	V

Switching Time Test Circuit



Package Dimensions 2083A

(unit : mm)

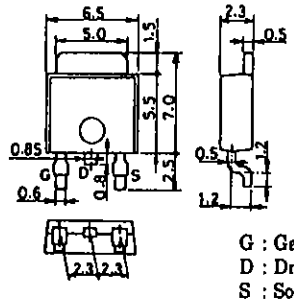


G : Gate
D : Drain
S : Source

SANYO : TP

Package Dimensions 2092A

(unit : mm)



G : Gate
D : Drain
S : Source

SANYO : TP-FA

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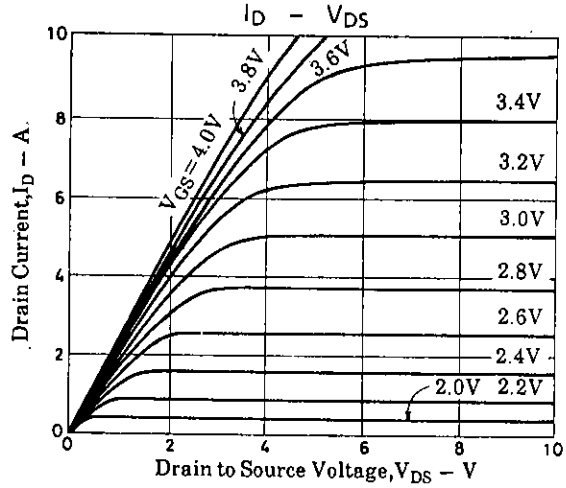
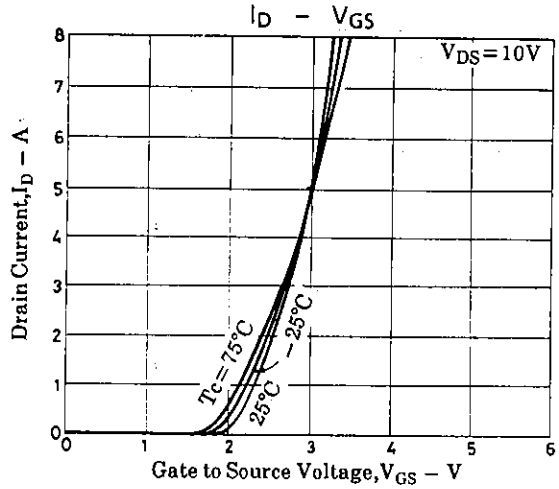
TOKYO OFFICE Tokyo Bldg., 1-10, 1 Chome, Ueno, Taito-ku, TOKYO, 110 JAPAN

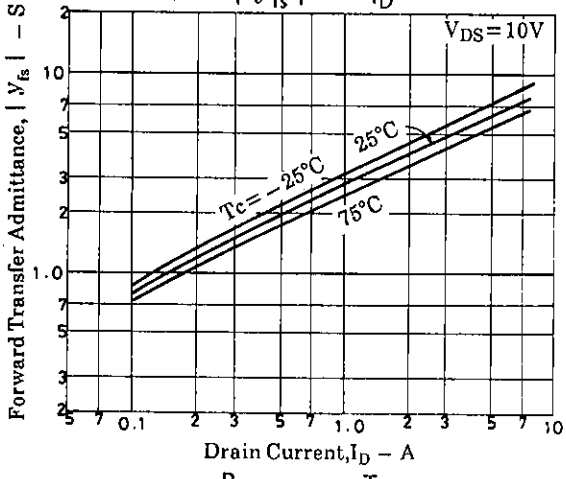
I_D – V_{DS}



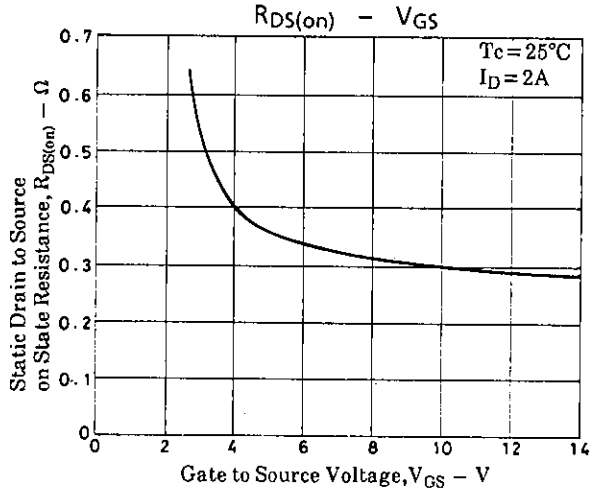
I_D – V_{GS}



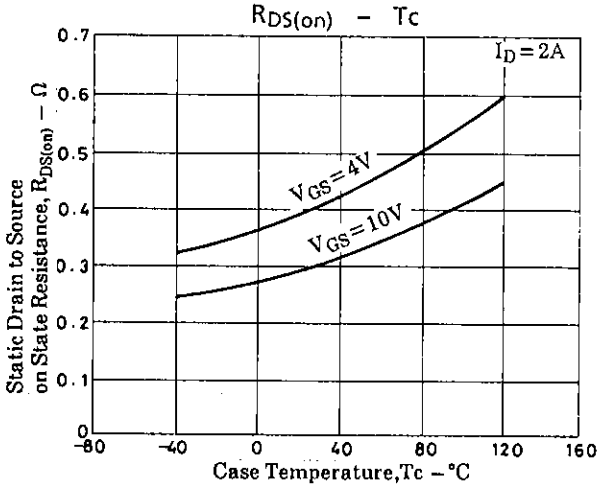
$|y_{fs}|$ – I_D



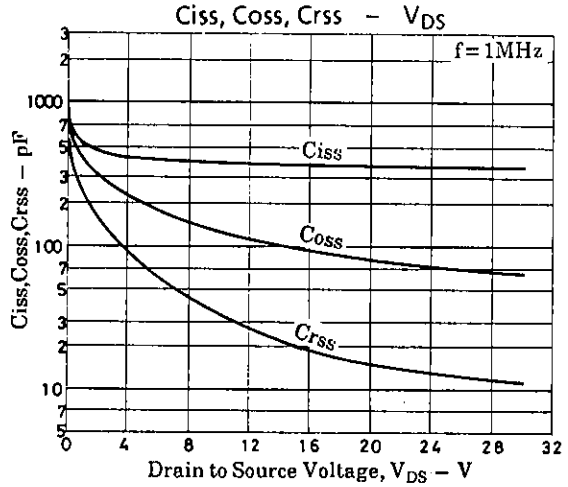
$R_{DS(on)}$ – V_{GS}



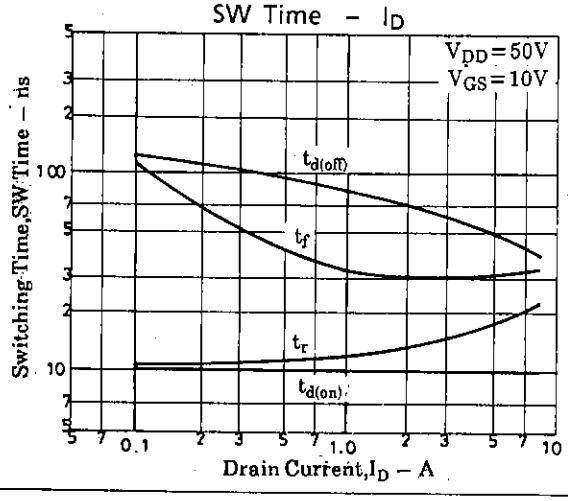
$R_{DS(on)}$ – Tc



Ciss, Coss, Crss – V_{DS}



SW Time – I_D



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