TOSHIBA Transistor Silicon NPN Epitaxial Type

2SC5785

High-Speed Switching Applications DC-DC Converter Applications Strobe Applications

- High DC current gain: h_{FE} = 400 to 1000 (I_C = 0.2 A)
- Low collector-emitter saturation voltage: V_{CE} (sat) = 0.12 V (max)
- High-speed switching: $t_f = 25$ ns (typ.)

Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Collector-base voltage		V _{CBO}	20	V	
Collector-emitter voltage		V _{CEO}	10	V	
Emitter-base voltage		V _{EBO}	7	V	
Collector current	DC	Ι _C	2.0	A	
	Pulse	I _{CP}	3.5		
Base current		Ι _Β	200	mA	
Collector power dissipation	t = 10 s	P _C	2.0	W	
	DC	(Note 1)	1.0		
Junction temperature		Tj	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	

1.6MAX 4.6MAX 1.7MAX 0.4±0.05 2.5±0.1 4.2MAX + 0.08 0.45 - 0.05 + 0.08 + 0.08 1.5±0.1 1.5 ± 0.1 1. Base 2. Collector (heat sink) 3. Emitter JEDEC ____ JEITA SC-62 TOSHIBA 2-5K1A

Weight: 0.05 g (typ.)

Note 1: Mounted on FR4 board (glass epoxy, 1.6 mm thick, Cu area: 645 mm²)

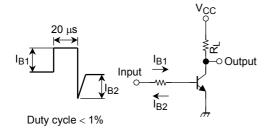
Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit	
Collector cut-off current		I _{CBO}	$V_{CB} = 20 \text{ V}, \ I_E = 0$	_	_	100	nA	
Emitter cut-off current		I _{EBO}	$V_{EB} = 7 V, I_C = 0$	_	_	100	nA	
Collector-emitter breakdown voltage		V (BR) CEO	$I_{C} = 10 \text{ mA}, I_{B} = 0$	10	_	_	V	
DC current gain		h _{FE} (1)	$V_{CE} = 2 V, I_C = 0.2 A$	400	_	1000		
		h _{FE} (2)	$V_{CE} = 2 V, I_C = 0.6 A$	200	_	_		
Collector-emitter saturation voltage		V _{CE (sat)}	$I_{C} = 0.6 \text{ A}, I_{B} = 12 \text{ mA}$	_	_	0.12	V	
Base-emitter saturation voltage		V _{BE (sat)}	$I_{C} = 0.6 \text{ A}, I_{B} = 12 \text{ mA}$	_	_	1.10	V	
Switching time	Rise time	tr	See Figure 1 circuit diagram.	_	60	_	ns	
	Storage time	t _{stg}	$V_{CC}\simeq 6~V,~R_L=10~\Omega$	_	215	_		
	Fall time	t _f	$I_{B1} = -I_{B2} = 12 \text{ mA}$	_	25	_		

Industrial Applications



Marking



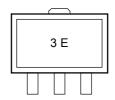
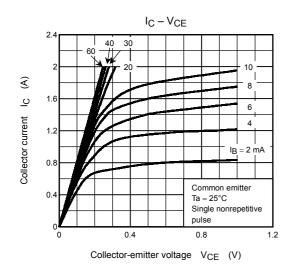
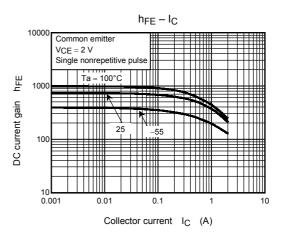
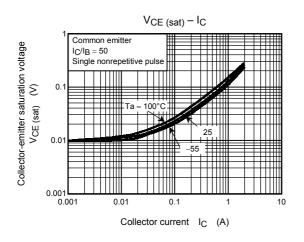


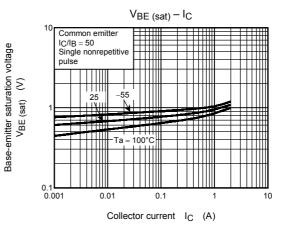
Figure 1 Switching Time Test Circuit & Timing Chart

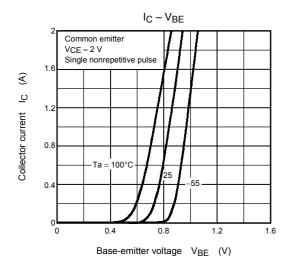
TOSHIBA

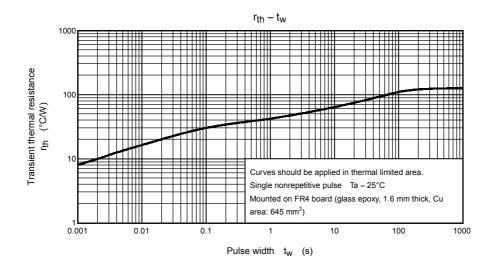












Safe Operating Area 10 [C max (pulsed) ♦ _1 ms♦ 10 ms♦ 100 μs IC max (continuous) 1 Πļ € ns ♦* **1** -10 s ♦ * 100 ms (<u>ں</u> Collector current DC operation (Ta = 25°C) •: Single nonrepetitive pulse Ta = 25°C Note that the curves for 100 ms* 10 s* and DC operation* will be different when the devices aren't mounted on an FRA board (glass once 1.6 ms thick Curston: 0.1 max epoxy, 1.6 mm thick, Cu area: 645 mm²). These characteristic curves must be derated linearly VCEO r with increase in temperature. 0.01 0.1 10 100 Collector-emitter voltage V_{CE} (V)

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