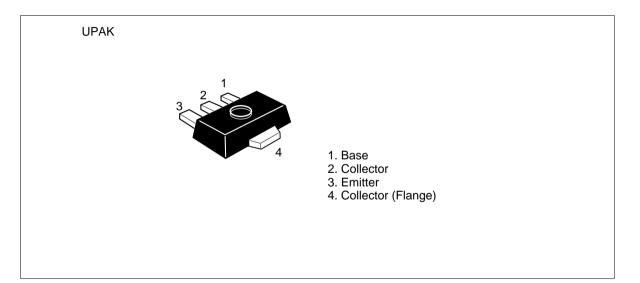
Silicon PNP Epitaxial

# HITACHI

#### Application

- Low frequency power amplifier
- Complementary pair with 2SD1418

#### Outline





# **Absolute Maximum Ratings** ( $Ta = 25^{\circ}C$ )

| Item                         | Symbol                        | Ratings     | Unit |
|------------------------------|-------------------------------|-------------|------|
| Collector to base voltage    | V <sub>CBO</sub>              | -120        | V    |
| Collector to emitter voltage | V <sub>CEO</sub>              | -80         | V    |
| Emitter to base voltage      | V <sub>EBO</sub>              | -5          | V    |
| Collector current            | Ι <sub>c</sub>                | -1          | А    |
| Collector peak current       | i <sub>C(peak)</sub> *1       | -2          | А    |
| Collector power dissipation  | P <sub>c</sub> * <sup>2</sup> | 1           | W    |
| Junction temperature         | Tj                            | 150         | °C   |
| Storage temperature          | Tstg                          | -55 to +150 | °C   |

Notes: 1.  $PW \le 10 \text{ ms}$ , Duty cycle  $\le 20\%$ 

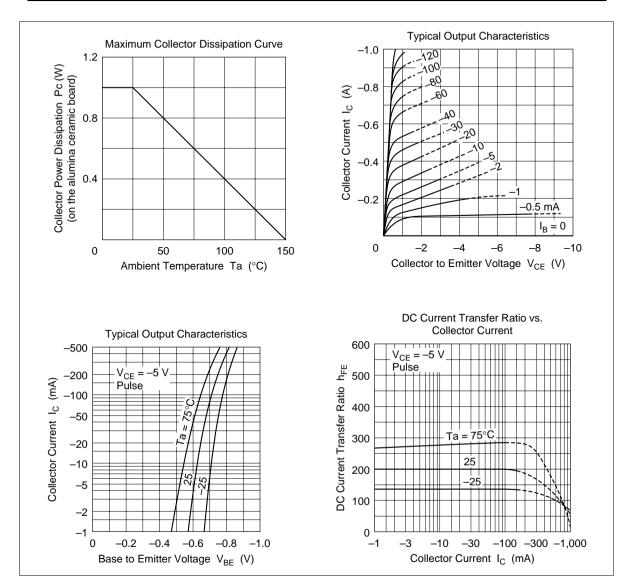
2. Value on the alumina ceramic board ( $12.5 \times 20 \times 0.7$  mm)

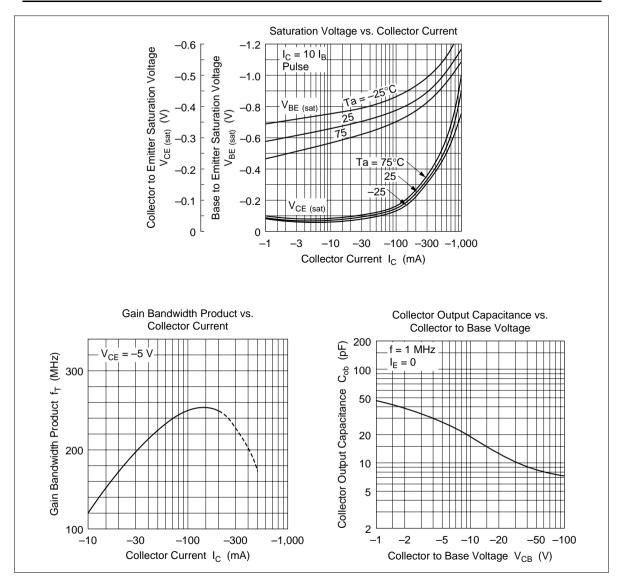
#### **Electrical Characteristics** (Ta = 25°C)

| Item                                    | Symbol               | Min  | Тур | Max  | Unit | Test conditions   |
|---|----------------------|------|-----|------|------|---|
| Collector to base breakdown voltage     | $V_{\rm (BR)CBO}$    | -120 | _   | _    | V    | $I_{c} = -10 \ \mu A, \ I_{E} = 0$                                      |
| Collector to emitter breakdown voltage  | $V_{\rm (BR)CEO}$    | -80  | _   | _    | V    | $I_c = -1 \text{ mA}, \text{ R}_{BE} = \infty$                          |
| Emitter to base breakdown voltage       | $V_{\rm (BR)EBO}$    | -5   | —   | —    | V    | $I_{\rm E} = -10 \ \mu A, \ I_{\rm C} = 0$                              |
| Collector cutoff current                | I <sub>CBO</sub>     | _    | _   | -10  | μΑ   | $V_{\rm CB} = -100 \text{ V}, I_{\rm E} = 0$                            |
| DC current transfer ratio               | h <sub>FE1</sub> *1  | 60   | —   | 320  |      | $V_{ce} = -5 \text{ V}, \text{ I}_{c} = -150 \text{ mA}$                |
|   | $h_{\text{FE2}}$     | 30   | _   | _    |      | $V_{ce} = -5 V,$<br>$I_c = -500 mA (Pulse test)$                        |
| Collector to emitter saturation voltage | $V_{\text{CE(sat)}}$ | _    | _   | -1   | V    | $I_c = -500 \text{ mA},$<br>$I_B = -50 \text{ mA} \text{ (Pulse test)}$ |
| Base to emitter voltage                 | $V_{\text{BE}}$      | _    | _   | -0.9 | V    | $V_{ce} = -5 \text{ V}, \text{ I}_{c} = -150 \text{ mA}$                |
| Gain bandwidth product                  | $f_{\tau}$           | _    | 140 | _    | MHz  | $V_{ce} = -5 \text{ V}, \text{ I}_{c} = -150 \text{ mA}$                |
| Collector output capacitance            | Cob                  | —    | 20  | —    | pF   | $V_{_{CB}} = -10 \text{ V}, \text{ I}_{_{E}} = 0,$<br>f = 1 MHz         |

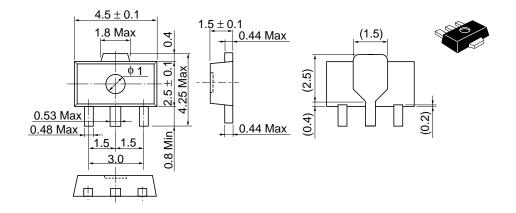
| NOLE.                     | 1. The 23B10251 | is grouped by fi | FE1 as IOliows. |
|---------------------------|-----------------|------------------|-----------------|
| Mark                      | DH              | DJ               | DK              |
| $\mathbf{h}_{\text{FE1}}$ | 60 to 120       | 100 to 200       | 160 to 320      |

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| Hitachi Code             | UPAK     |
|--------------------------|----------|
| JEDEC                    |          |
| EIAJ                     | Conforms |
| Weight (reference value) | 0.050 g  |

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