TOSHIBA TRANSISTOR SILICON, SILICON GERMANIUM NPN EPITAXIAL PLANAR TYPE

# MT6L74FS

#### VHF~UHF BAND LOW NOISE AMPLIFIER APPLICATIONS

Two devices are built in to the fine pich small mold package (6pins):fs6

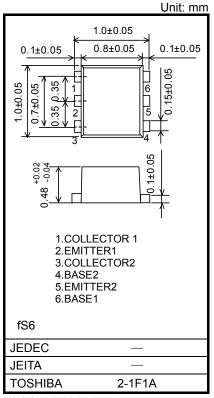
• It exsels in the buffer and oscillation use.

#### **Mounted Devices**

	Q1	Q2
Three-pin fSM mold products are corresponded	MT3S07FS	MT3S110FS

## **Absolute Maximum Ratings (Ta = 25°C)**

CHARACTERISTICS	SYMBOL	RAT	UNIT	
		Q1	Q2	OIVII
Collector-Base Voltage	V <sub>CBO</sub>	10	13	V
Collector-Emitter Voltage	V <sub>CEO</sub>	5	6	V
Emitter-Base Voltage	V <sub>EBO</sub>	1.5	1	V
Collector Current	IC	25	80	mA
Base Current	ΙΒ	10	20	mA
Collector power dissipation	P <sub>C</sub> (Note 1)	100		mW
		110 (Note 2)		
Junction temperature	Tj	125		°C
Storage temperature range	T <sub>stg</sub>	<i>–</i> 55~125		°C



Weight: 0.001g (typ.)

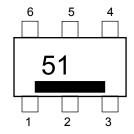
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

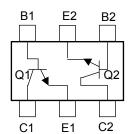
Note 1 : 10  $\text{mm}^2 \times 1.0 \text{ mm}$  (t) at the time of glass epoxy printed circuit board mounting.

Note 2: At the time of two-element operation

#### Marking (top view)



#### Pin Assignment (top view)



## **ELECTRICAL CHARACTERISTICS Q1 (Ta = 25°C)**

CHARACTERISTICS	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	
Collector Cut-off Current	I <sub>CBO</sub>	V <sub>CB</sub> = 5 V, I <sub>E</sub> = 0	_	_	0.1	μА	
Emitter Cut-off Current	I <sub>EBO</sub>	V <sub>EB</sub> = 1 V, I <sub>C</sub> = 0		_	1	μА	
DC Current Gain	h <sub>FE</sub>	V <sub>CE</sub> = 1 V, I <sub>C</sub> = 5 mA	70	_	140	_	
Reverse Transfer Capacitance	C <sub>re</sub> (Note)	$V_{CB} = 1 \text{ V}, I_{E} = 0, f = 1 \text{ MHz}$	_	0.4	0.65	pF	
Transition Frequency	f <sub>T</sub>	V <sub>CE</sub> = 3 V, I <sub>C</sub> = 10 mA	10	12	_	GHz	
Insertion Gain	S <sub>21e</sub>   <sup>2</sup> (1)	$V_{CE} = 1 \text{ V}, I_{C} = 5 \text{ mA}, f = 2 \text{ GHz}$	_	8	_	40	
	S <sub>21e</sub>   <sup>2</sup> (2)	$V_{CE} = 3 \text{ V}, I_{C} = 15 \text{ mA}, f = 2 \text{ GHz}$	7.5	10	_	dB	
Noise Figure	NF	V <sub>CE</sub> = 1 V, I <sub>C</sub> = 5 mA, f = 2 GHz	_	1.5	3	dB	

# **ELECTRICAL CHARACTERISTICS Q2 (Ta = 25°C)**

CHARACTERISTICS	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	
Collector Cut-off Current	I <sub>CBO</sub>	$V_{CB} = 5 \text{ V}, I_{E} = 0$	_	_	0.1	μА	
Emitter Cut-off Current	I <sub>EBO</sub>	V <sub>EB</sub> = 1 V, I <sub>C</sub> = 0	_	_	0.5	μА	
DC Current Gain	h <sub>FE</sub>	V <sub>CE</sub> = 1 V, I <sub>C</sub> = 5 mA	75	_	125	_	
Reverse Transfer Capacitance	C <sub>re</sub> (Note)	V <sub>CB</sub> = 1 V, I <sub>E</sub> = 0, f = 1 MHz	_	0.6	0.8	pF	
Transition Frequency	f <sub>T</sub>	V <sub>CE</sub> = 1 V, I <sub>C</sub> = 10 mA	5.5	7.5	_	GHz	
Insertion Gain	S <sub>21e</sub>   <sup>2</sup> (1)	V <sub>CE</sub> = 1 V, I <sub>C</sub> = 10 mA, f = 2 GHz	_	7	_	dB	
	S <sub>21e</sub>   <sup>2</sup> (2)	$V_{CE} = 3 \text{ V}, I_{C} = 20 \text{ mA}, f = 2 \text{ GHz}$	7.5	9	_		
Noise Figure	NF	V <sub>CE</sub> = 1 V, I <sub>C</sub> = 15 mA, f = 2 GHz	_	1.3	2	dB	

Note: Cre is measured by 3 terminal method capacitance bridge.

## Caution

This device is sensitive to electrostatic discharge due to applied the high frequency transistor process of fT=60GHz class is used for this product.

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Please make enough tool and equipment earthed when you handle.

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20070701-EN GENERAL

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