TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (U-MOSIII)

TPCT4202

Lithium Ion Battery Applications

- Lead(Pb)-Free
- · Small footprint due to small and thin package
- Low drain-source ON resistance: RSS (ON) =30.5mΩ (typ.)
- High forward transfer admittance: $|Y_{fs}| = 15S$ (typ.)
- Low leakage current: $ISSS = 10 \mu A (max) (VDS = 30 V)$
- Enhancement-mode: $V_{th} = 0.5 \sim 1.2 \text{ V (Vss} = 10 \text{ V, Is} = 200 \mu\text{A})$
- · Common drain

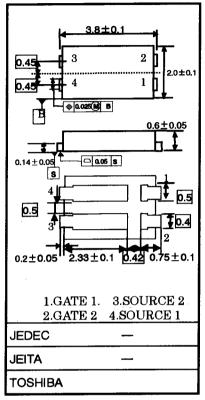
Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Source-source v	oltage	V _{SSS} 30		٧	
Gate-source volt	e voltage V _{GSS} ±12		±12	V	
D i	DC (Note 1)	Is	6	Α.	
Drain current	Pulse (Note 1)	I _{SP}	24	^ ^	
Drain power dissipation (t = 10 s) (Note 2a)	Single-device value at dual operation (Note 3)	PD	1.7	W	
Drain power dissipation (t = 10 s) (Note 2b)	Single-device value at dual operation (Note 3)	PD	0.51	W	
Single pulse ava	lanche energy (Note 4)	EAS	46.8	mJ	
Avalanche current		IAR	6	Α	
Repetitive avalanche energy Single-device value at dual operation (Note 2a, 3, 5)		EAR	0.17	mJ .	
Channel temperature		T _{ch}	150	°C	
Storage tempera	ture range	T _{stg}	-55~150	°C	

Note: (Note 1), (Note 2), (Note 3), (Note 4), (Note 5) please refer to the next page.

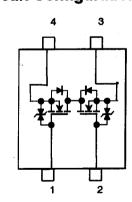
This transistor is an electrostatic sensitive device. Please handle with caution.

Unit: mm



Weight: 0.010 g (typ.)

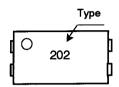
Circuit Configuration



Thermal Characteristics

Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	Single-device value at dual operation (Note 3)	Rth (ch-a)	76	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	Single-device value at dual operation (Note 3)	Rth (ch-a)	244	°C/W

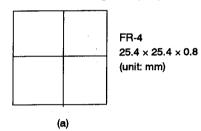
Marking (Note 5)



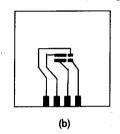
Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2:

(a) Device mounted on a glass-epoxy board



(b) Device mounted on a glass-epoxy board



FR-4 25.4 × 25.4 × 0.8 (unit: mm)

- Note 3: The power dissipation and thermal resistance values are shown for a single device (During dual operation, power is evenly applied to both devices.)
- Note 4: V_{DD} = 24 V, T_{ch} = 25°C (initial), L = 1.0 mH, R_G = 25 Ω , I_{AR} = 6 A
- Note 5: Repetitive rating: pulse width limited by max channel temperature
- Note 6: on lower right of the marking indicates Pin 1.

Electrical Characteristics (Ta = 25°C)

Ch	aracteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage cui	rent	lgss	V _{GS} = ±10 V, V _{SS} = 0 V	_		±10	μА	
Source cut-OFF	current	Isss	V _{SS} = 30 V, V _{GS} = 0 V	_	_	10	μΑ	
		V (BR) SSS	I _S = 10 mA, V _{GS} = 0 V	30	_	_	v	
Source -source b	reakdown voltage	V (BR) SSX	I _S = 10 mA, V _{GS} = -12 V	15	_		·	
Gate threshold ve	oltage	V _{th}	V _{SS} = 10 V, I _S = 200 μA	0.5		1.2	٧	
			V _{GS} = 2.5 V, I _S = 3 A	30	40	52		
Source -source ON resistance		Rss (ON)	V _{GS} = 4.0 V, I _S = 3 A	26	32	39	mΩ	
			V _{GS} = 4.5 V, I _S = 3 A	24	30.5	38		
Forward transfer	admittance	Y _{fs}	Vss = 10 V, I _S = 3 A	7.5	15		Ø	
Input capacitance		C _{iss}	V _{SS} = 10 V, V _{GS} = 0 V, f = 1 MHz		1540	1	pF	
Reverse transfer capacitance		C _{rss}		_	180			
Output capacitan	ce	Coss				_		
Reverse transfer capacion Coutput capacitance Ris Tur Switching time Fal	Rise time	t _r	A ² 2 A A A A A A A A A A A A A A A A A A		34	1		
	Turn-ON time	ton			41	_	ns	
	Fall time	t _f ,			60	-		
	Turn-OFF time	toff	V _{SS} ≃ 15 V Duty ≤ 1%, t _w = 10 μs	1	161		. •	
Total gate charge (gate-source plus gate-drain)		Qg	V _{SS} = 24 V, V _{GS} = 5 V, I _D = 6 A		21	_	_	
Gate-source charge 1		Q _{gs1}		_	3	_	пC	
Gate- source ("miller") charge		Q _{gs}]	_	6	_		

Source- Source Ratings and Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Source reverse current	Pulse (Note 1)	ISRP	: , ,		_	24	. A
Forward voltage (diode)		V _{SSF}	I _{SR} = 6 A, V _{GS} = 0 V	- , . —	_	-1.2	V

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