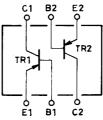


FC117 PNP Epitaxial Planar Silicon Composite Transistor Low-Frequency General-Purpose Amp Applications

Features

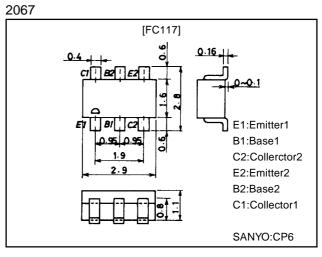
- Composite type with 2 transistors contained in the CP package currently in use, improving the mounting efficiency greatly.
- The FC117 is formed with two chips, being equivalent to the 2SA1753, placed in one package.
- \cdot Low collector to emitter saturation voltage.
- · Excellent in thermal equilibrium and pair capability.

Electrical Connection



Package Dimensions

unit:mm



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	VCBO		-20	V
Collector-to-Emitter Voltage	VCEO		-15	V
Emitter-to-Base Voltage	VEBO		-5	V
Collector Current	IC		-500	mA
Collector Current (Pulse)	ICP		-1	A
Base Current	IB		-100	mA
Collector Dissipation	PC	1 unit	200	mW
Total Power Dissipation	PT		300	mW
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to+150	°C

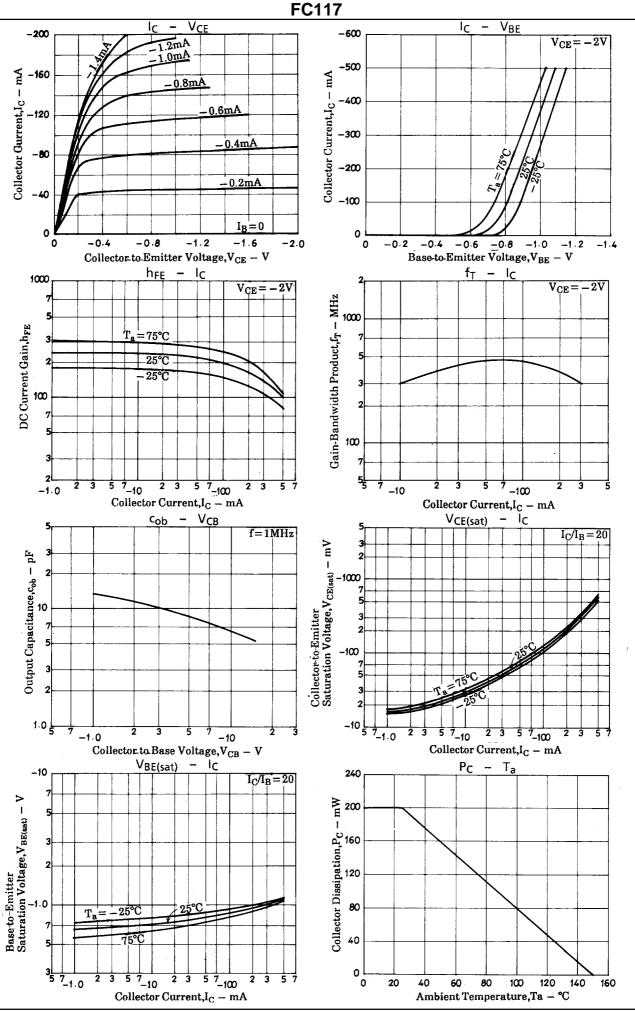
Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions		Ratings		
			min	typ	max	Unit
Collector Cutoff Current	ICBO	V _{CB} =-15V, I _E =0			-0.1	μA
Emitter Cutoff Current	IEBO	V _{EB} =-4V, I _C =0			-0.1	μA
DC Current Gain	h _{FE} (1)	V _{CE} =-2V, I _C =-10mA	160		560	
	h _{FE} (2)	V _{CE} =-2V, I _C =-400mA	70			
DC Current Gain Ratio	h _{FE} (small/ large)	V _{CE} =-2V, I _C =-10mA	0.8	0.98		
Gain-Bandwidth Product	fT	V _{CE} =-2V, I _C =-50mA		400		MHz
Output Capacitance	Cob	V _{CE} =-10V, f=1MHz		6.5		pF
C-E Saturation Voltage	V _{CE(sat)} 1	I _C =-5mA. I _B =-0.5mA		-15	-35	mV
	V _{CE(sat)} 2	I _C =-200mA. I _B =-10mA		-200	-360	mV
B-E Saturation Voltage	V _{BE(sat)}	I _C =-200mA. I _B =-10mA		-0.95	-1.2	V
C-B Breakdown Voltage	V(BR)CBO	I _C =-10µA, I _E =0	-20			V
C-E Breakdown Voltage	V _(BR) CEO	I _C =−1mA, R _{BE} =∞	-15			V
E-B Breakdown Voltage	V _{(BR)EBO}	I _E =-10μΑ, I _C =0	-5			V

Note: The specifications shown above are for each individual transistor.

Marking:117





No.3115-2/3

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