



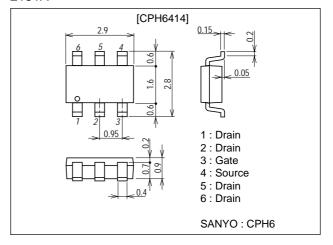
Ultrahigh-Speed Switching Applications

Features

- · Low ON-resistance.
- · Ultrahigh-speed switcing.
- · 4V drive.

Package Dimensions

unit : mm 2151A



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		30	V
Gate-to-Source Voltage	VGSS		±20	V
Drain Current (DC)	ΙD		5	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	20	Α
Allowable Power Dissipation	PD	Mounted on a ceramic board(1200mm²X0.8mm)	1.6	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Drain-to-Source Breakdown Voltage	V(BR)DSS	I _D =1mA, V _{GS} =0	30			V
Zero-Gate Voltage Drain Current	IDSS	V _{DS} =30V, V _{GS} =0			1	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} = ±16V, V _{DS} =0			±10	μΑ
Cutoff Voltage	Vgs(off)	V _{DS} =10V, I _D =1mA	1.2		2.6	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =3A	3.1	4.5		S
Static Drain-to-Source On-State Resistance	R _{DS} (on)1	I _D =3A, V _G S=10V		37	48	mΩ
	R _{DS} (on)2	I _D =1.5A, V _{GS} =4V		63	88	mΩ

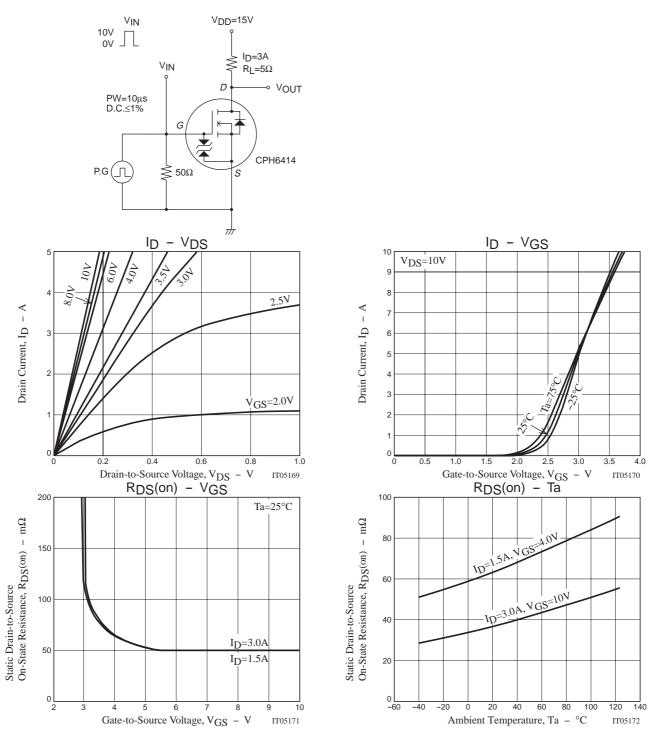
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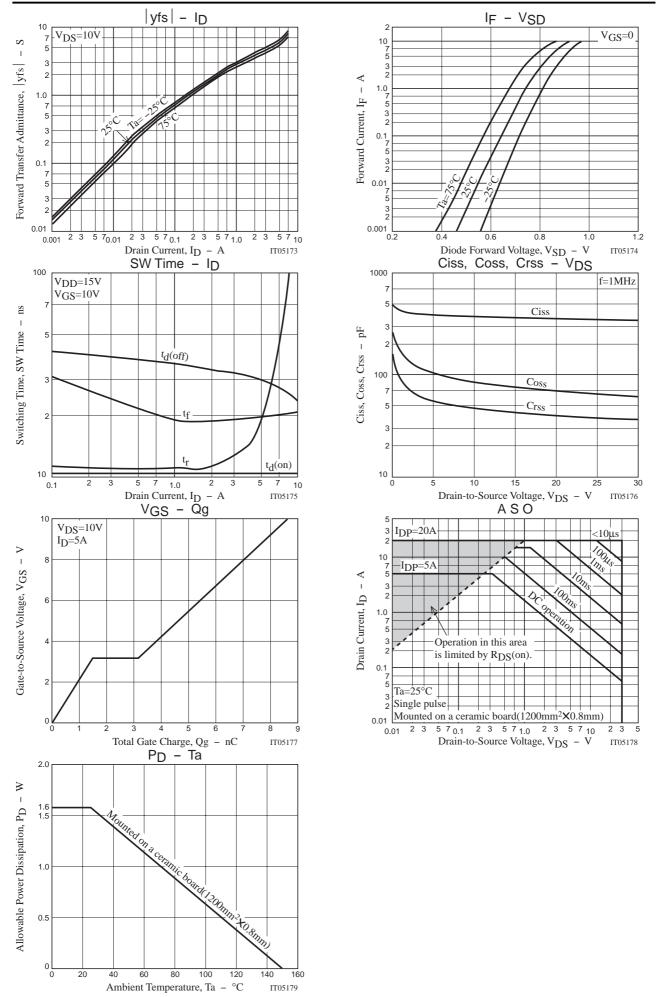
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Parameter	Cumhal	Conditions	Ratings			1.1-14
	Symbol		min	typ	max	Unit
Input Capacitance	Ciss	V _{DS} =10V, f=1MHz		370		pF
Output Capacitance	Coss	V _{DS} =10V, f=1MHz		85		pF
Reverse Transfer Capacitance	Crss	V _{DS} =10V, f=1MHz		47		pF
Turn-ON Delay Time	t _d (on)	See specified Test Circuit.		11		ns
Rise Time	t _r	See specified Test Circuit.		12		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		31		ns
Fall Time	tf	See specified Test Circuit.		18		ns
Total Gate Charge	Qg	V _{DS} =10V, V _{GS} =10V, I _D =5A		8.5		nC
Gate-to-Source Charge	Qgs	V _{DS} =10V, V _{GS} =10V, I _D =5A		1.8		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =10V, V _{GS} =10V, I _D =5A		1.3		nC
Diode Forward Voltage	V _{SD}	I _S =5A, V _G S=0		0.86	1.2	V

Switching Time Test Circuit





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