General purpose transistor (isolated transistor and diode)

UML6N

2SA2018 and RB521S-30 are housed independently in a UMT package.

Applications

DC / DC converter Motor driver

● Features

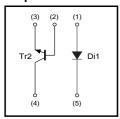
1) Tr : Low Vce(sat) Di : Low VF

2) Small package

●Structure

Silicon epitaxial planar transistor Schottky barrier diode

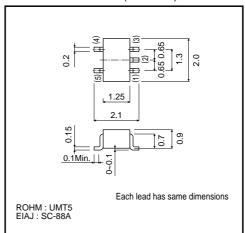
●Equivalent circuit



Packaging specifications

Туре	UML6N
Package	UMT5
Marking	L6
Code	TR
Basic ordering unit (pieces)	3000

●External dimensions (Unit : mm)



Rev.A

●Absolute maximum ratings (Ta=25°C)

Di1

Parameter	Symbol	Limits	Unit
Average revtified forward current	lo	200	mA
Forward current surge peak (60Hz, 1∞)	IFSM	1	Α
Reverse voltage (DC)	Vr	30	V
Junction temperature	Tj	125	°C
Range of storage temperature	Tstg	-55~+125	°C

Tr2

Parameter	Symbol	Limits	Unit
Collector-base voltage	Vсво	15	V
Collector-emitter voltage	Vceo	12	V
Emitter-base voltage	Vebo	6	V
Collector current	Ic	500	mA
Collector current	Іср	1	Α
Power dissipation	Pd	120	mW *1
Junction temperature	Tj	150	°C
Range of storage temperature	Tstg	-55~+125	°C

^{*1} Each terminal mounted on a recommended land.

●Electrical characteristics (Ta=25°C)

Di1

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	VR	-	0.40	0.50	V	I=200mA
Reverse current	lR	_	4.0	30	μΑ	V _R =10V

Tr2

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-emitter breakdown voltage	BVceo	12	_	-	V	Ic=1mA
Collector-base breakdown voltage	ВУсво	15	-	-	V	Ic=10μA
Emitter-base breakdown voltage	ВVево	6	-	-	V	Iε=10μA
Collector cut-off current	Ісво	-	-	100	nA	Vcb=15V
Emitter cut-off current	ІЕВО	_	-	100	nA	V _{EB} =6V
Collector-emitter saturation voltage	VCE(sat)	-	90	250	mV	Ic=200mA, Iв=10mA
DC current gain	hfe	270	-	680	-	Vce=2V, Ic=10mA
Transition frequency	f⊤	_	320	-	MHz	Vce=2V, Ie=-10mA, f=100MHz
Collector output capacitance	Cob	-	7.5	_	pF	Vcb=10V, IE=0mA, f=1MHz

•Electrical characteristic curves

Di1

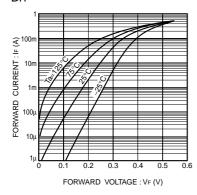
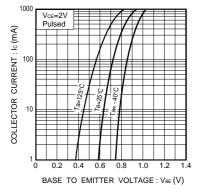


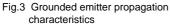
Fig.1 Forward characteristics

Fig.2 Reverse characteristics









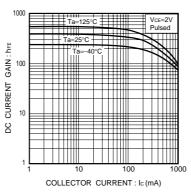


Fig.4 DC current gain vs. collector current

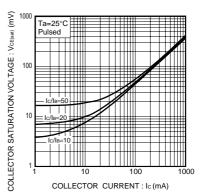


Fig.5 Collector-emitter saturation voltage vs. collector current (I)

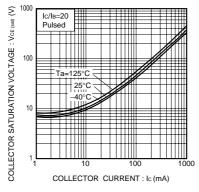


Fig.6 Collector-emitter saturation voltage vs. collector current (II)

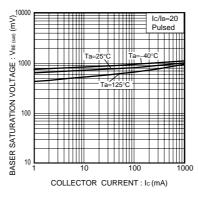


Fig.7 Base-emitter saturation voltage vs. collector current

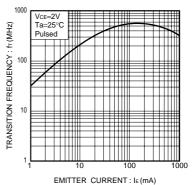


Fig.8 Gain bandwidth product vs. emitter current

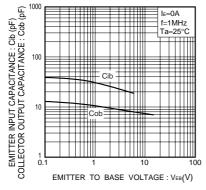


Fig.9 Collector output capacitance vs. collector-base voltage Emitter input capacitance vs. emitter-base voltage

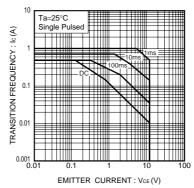


Fig.10 Safe operation area

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