

# **HAT2266H**

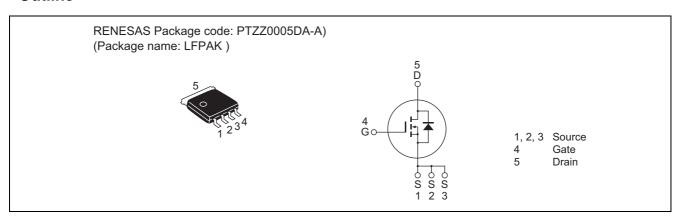
# Silicon N Channel Power MOS FET Power Switching

REJ03G1370-0500 Rev.5.00 Apr 05, 2006

#### **Features**

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance  $R_{DS(on)} = 9.2 \ m\Omega \ typ. \ (at \ V_{GS} = 10 \ V)$
- Lead Free

#### **Outline**



#### **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	60	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	I <sub>D</sub>	30	A
Drain peak current	I <sub>D(pulse)</sub> Note1	120	A
Body-drain diode reverse drain current	I <sub>DR</sub>	30	A
Avalanche current	I <sub>AP</sub> Note 2	20	A
Avalanche energy	E <sub>AR</sub> Note 2	34	mJ
Channel dissipation	Pch Note3	23	W
Channel to Case Thermal Resistance	θch-C	5.44	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

- 2. Value at Tch = 25°C, Rg  $\geq$  50  $\Omega$
- 3.  $Tc = 25^{\circ}C$

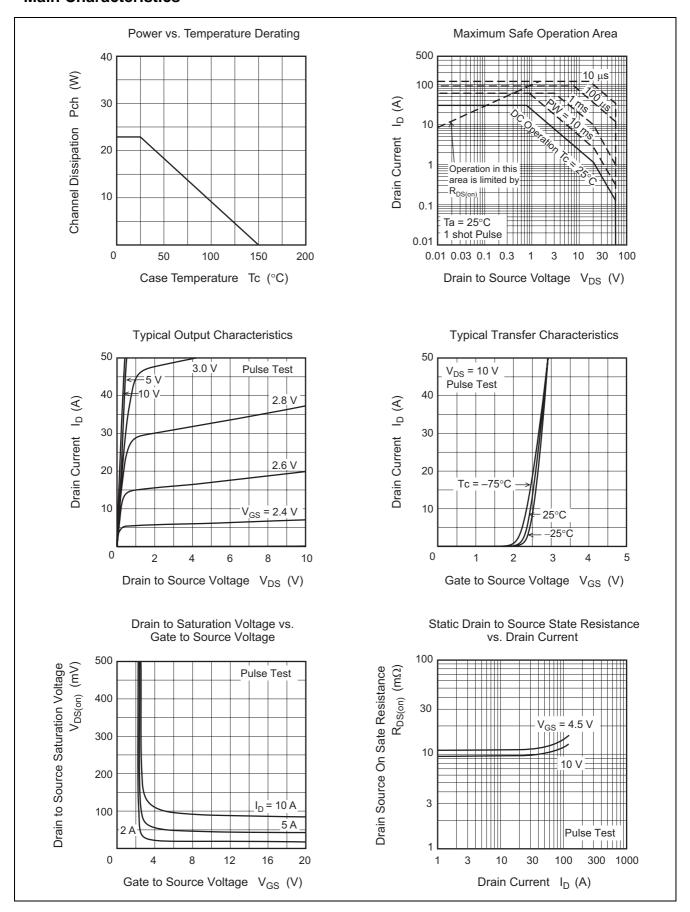
## **Electrical Characteristics**

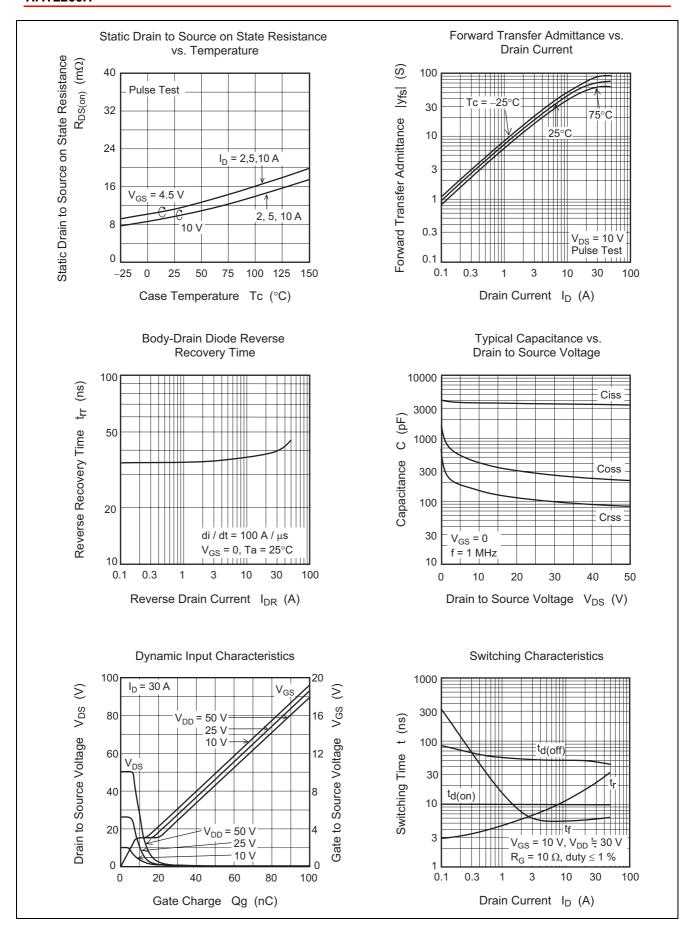
 $(Ta = 25^{\circ}C)$ 

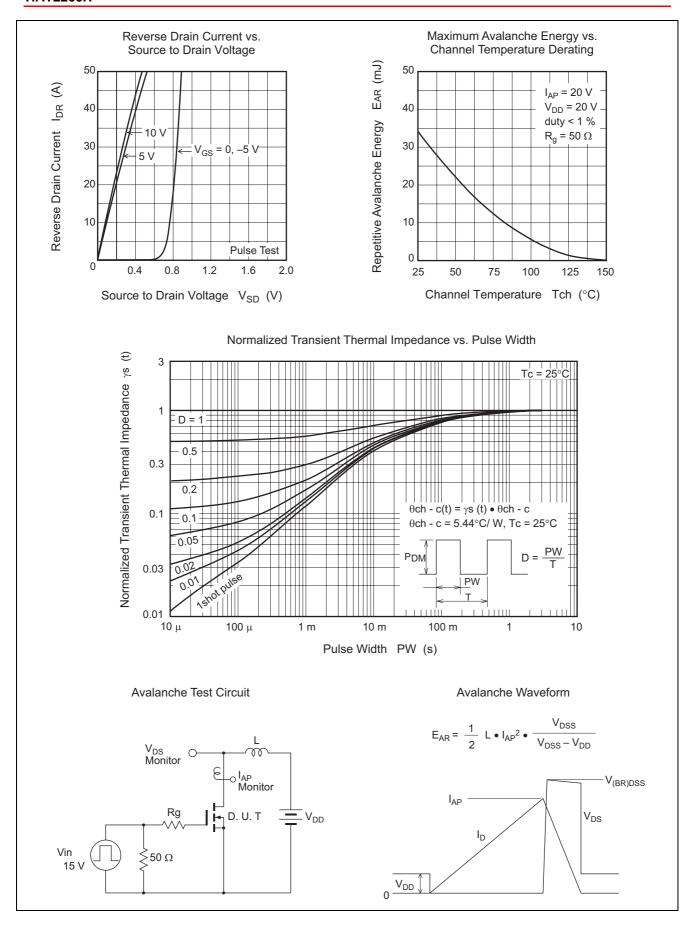
Item	Symbol	Min	Тур	Max	Unit	Test Conditions	
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	60	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$	
Gate to source leak current	I <sub>GSS</sub>	_	_	±0.1	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$	
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	1	μΑ	$V_{DS} = 60 \text{ V}, V_{GS} = 0$	
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	_	2.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$	
Static drain to source on state	R <sub>DS(on)</sub>	_	9.5	12	mΩ	$I_D = 15 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$	
resistance	R <sub>DS(on)</sub>	_	11	16	mΩ	$I_D = 15 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note4}}$	
Forward transfer admittance	y <sub>fs</sub>	35	70	_	S	$I_D = 30 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$	
Input capacitance	Ciss	_	3600	_	pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0,$	
Output capacitance	Coss	_	400	_	pF	f = 1 MHz	
Reverse transfer capacitance	Crss	_	145	_	pF		
Gate resistance	Rg	_	0.5	_	Ω		
Total gate charge	Qg	_	25	_	nC	$V_{DD} = 25 \text{ V}, V_{GS} = 4.5 \text{ V},$	
Gate to source charge	Qgs	_	8.2	_	nC	I <sub>D</sub> = 30 A	
Gate to drain charge	Qgd	_	9	_	nC	1	
Turn-on delay time	t <sub>d(on)</sub>	_	10	_	ns	$V_{GS} = 10 \text{ V}, I_D = 15 \text{ A},$	
Rise time	t <sub>r</sub>	_	15	_	ns	$V_{DD} \cong 30 \text{ V}, \text{ R}_{L} = 2 \Omega,$ $Rg = 4.7 \Omega$	
Turn-off delay time	t <sub>d(off)</sub>	_	50	_	ns		
Fall time	t <sub>f</sub>	_	5.5	_	ns		
Body-drain diode forward voltage	$V_{DF}$	_	0.84	1.10	V	$I_F = 30 \text{ A}, V_{GS} = 0^{\text{Note4}}$	
Body-drain diode reverse recovery time	t <sub>rr</sub>	_	40	_	ns	$I_F = 30 \text{ A}, V_{GS} = 0,$ $di_F/dt = 100 \text{ A}/\mu\text{s}$	

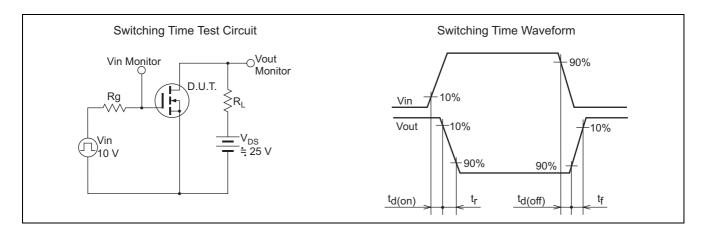
Notes: 4. Pulse test

#### **Main Characteristics**

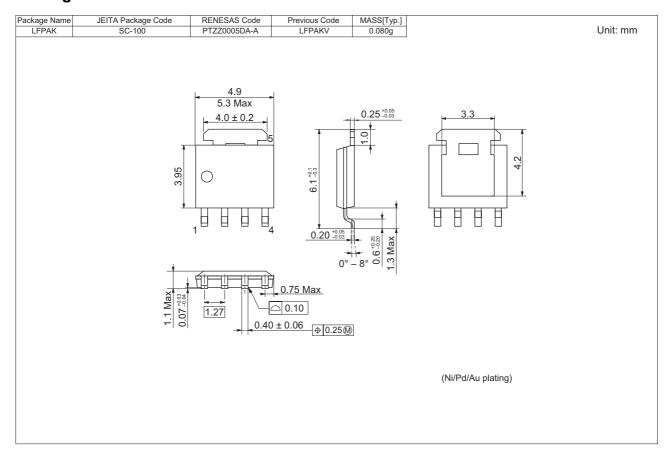








### **Package Dimensions**



### **Ordering Information**

Part Name	Quantity	Shipping Container
HAT2266H-EL-E	2500 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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