TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7PA17FU

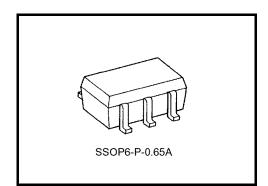
Dual Schmitt Buffer

Features

- Operating voltage range: V_{CC} = 1.8~3.6 V
- High-speed operation: t_{pd} = 4.0 ns (max) at V_{CC} = 3.0~3.6 V
 - t_{pd} = 4.3 ns (max) at V_{CC} = 2.3~2.7 V t_{pd} = 8.6 ns (max) at V_{CC} = 1.8 V
- High-level output current:

 $I_{OH}/I_{OL} = \pm 24 \text{ mA (min) at } V_{CC} = 3.0 \text{ V}$ $I_{OH}/I_{OL} = \pm 18 \text{ mA (min) at } V_{CC} = 2.3 \text{ V}$ $I_{OH}/I_{OL} = \pm 6 \text{ mA (min) at } V_{CC} = 1.8 \text{ V}$

- 3.6-V tolerant inputs
- 3.6-V power down protection outputs



Weight: 0.0068 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Value	Unit	
Power supply voltage	V _{CC}	-0.5~4.6	V	
DC input voltage	VIN	-0.5~4.6	V	
		-0.5~4.6 (Note 1)		
DC output voltage	Vout	-0.5~V _{CC} + 0.5 (Note 2)	V	
Input diode current	I _{IK}	-50	mA	
Output diode current	I _{OK}	-50 (Note 3)	mA	
DC output current	IOUT	±50	mA	
Power dissipation	PD	200	mW	
DC V _{CC} /ground current	ICC	±100	mA	
Storage temperature	T _{stg}	-65~150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

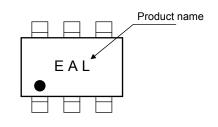
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{CC} = 0 V$

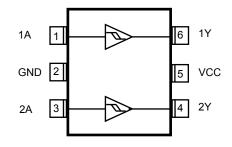
Note 2: High or Low state. I_{OUT} absolute maximum rating must be observed.

Note 3: V_{OUT} < GND

Marking



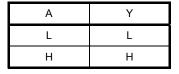
Pin Assignment (top view)



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Truth Table

IEC Logic Symbol





Operating Ranges

Characteristics	Symbol	Value	Unit
Power supply voltage	Vcc	1.8~3.6	V
Fower supply voltage	vcc	1.2~3.6 (Note 4)	v
Input voltage	V _{IN}	-0.3~3.6	V
Output voltage	Vour	0~3.6 (Note 5)	V
Output voltage	Vout	0~V _{CC} (Note 6)	v
		±24 (Note 7)	
Output Current	I _{OH} /I _{OL}	±18 (Note 8)	mA
		±6 (Note 9)	
Operating temperature	T _{opr}	-40~85	°C

Note 4: Data retention only

Note 5: $V_{CC} = 0 V$

Note 6: High or Low state

Note 7: $V_{CC} = 3.0 \sim 3.6 \text{ V}$

Note 8: V_{CC} = 2.3~2.7 V

Note 9: $V_{CC} = 1.8 V$

DC Electrical Characteristics (Ta = -40~85°C, 2.7 V < V_{CC} \leq 3.6 V)

Characteristics		Symbol	Tor	Test Condition		Min	Max	Unit
		Symbol			V _{CC} (V)	Min		Unit
	High level	VP				-	2.2	v
Threshold voltage	riigirievei	٧P		—	3.0	-	2.0	v
Theshold voltage	Low level	VN			3.6	0.8	-	v
		۷N		—	3.0	0.7	-	v
Hysteresys Voltage		V _H			3.6	0.3	1.2	v
Trysteresys voltage		۷H		_	3.0	0.3	1.2	v
				I _{OH} = −100 μA	2.7~3.6	V _{CC} - 0.2		
High-Level Output Voltage		V _{OH}	$V_{IN} = V_{IH}$	I _{OH} = -12 mA	2.7	2.2		V
				I _{OH} = -18 mA	3.0	2.4		
	I _{OH} = -24 mA			3.0	2.2			
				I _{OL} = 100 μA	2.7~3.6		0.2	
Low-Level Output Voltage		V _{OL}		I _{OL} = 12 mA	2.7	_	0.4	
	aye	VOL	$V_{IN} = V_{IL}$	I _{OL} = 18 mA	3.0		0.4	1
				I _{OL} = 24 mA	3.0	_	0.55	
Input Leakage Current		I _{IN}	V _{IN} = 0~3.6 V		2.7~3.6		±5.0	μA
Power-off Leakage Current		IOFF	$V_{IN}, V_{OUT} = 0^{-1}$	~3.6 V	0		10.0	μA
Quiescent Supply Current			V _{IN} = V _{CC} or GND		2.7~3.6	_	20.0	
	CIII	ICC	$V_{CC} \leq (V_{IN}, V_{OUT}) \leq 3.6 \text{ V}$		2.7~3.6	_	±20.0	μA
Increase in I _{CC} per Inp	out	Δlcc	$V_{IH} = V_{CC} - 0.$	6 V	2.7~3.6		750	

DC Electrical Characteristics (Ta = -40~85°C, 2.3 V \leq V_{CC} \leq 2.7 V)

Characteristics		Symbol	Top	Test Condition		Min	Max	Unit
		Symbol	165			IVIIII		Unit
Threshold voltage	High level	VP		_	2.3	-	1.8	V
Threshold voltage	Low level	V _N		_	2.3	0.5	-	v
Hysteresys Voltage		V _H		_	2.3	0.3	1.0	V
				I _{OH} = -100 μA	2.3~2.7	V _{CC} - 0.2		
High-Level Output Voltage		V _{OH}	$V_{IN} = V_{IH}$	I _{OH} =6 mA	2.3	2.0	_	V
				I _{OH} = -12 mA	2.3	1.8	_	
				I _{OH} = -18 mA	2.3	1.7	_	
Low-Level Output Voltage			$V_{IN} = V_{IL}$	I _{OL} = 100 μA	2.3~2.7		0.2	
		V _{OL}		$I_{OL} = 12 \text{ mA}$	2.3		0.4	
				I _{OL} = 18 mA	2.3	_	0.6	
Input Leakage Current		I _{IN}	V _{IN} = 0~3.6 V		2.3~2.7	_	±5.0	μA
Power-off Leakage Current		I _{OFF}	V _{IN} , V _{OUT} = 0~	V _{IN} , V _{OUT} = 0~3.6 V		_	10.0	μA
O discourt O marks O mark		laa	$V_{IN} = V_{CC}$ or G	$V_{IN} = V_{CC}$ or GND		_	20.0	
Quiescent Supply Curre	5110	ICC	$V_{CC} \leq (V_{IN}, V_{CC})$	UT) ≦ 3.6 V	2.3~2.7		±20.0	μA

DC Electrical Characteristics (Ta = $-40 \sim 85^{\circ}$ C, 1.8 V \leq V_{CC} < 2.3 V)

Characteristics		Symbol	Tool	Condition		Min	Мах	Unit
		Symbol	Test Condition		V _{CC} (V)	IVIIII	wax	Unit
Threshold voltage	High level	VP		—	1.8	-	1.4	v
Threshold voltage	Low level	V _N		_	1.8	0.25	-	v
Hysteresys Voltage		V _H		_	1.8	0.2	0.95	V
High-Level Output Voltage		Voh	V _{IN} = V _{IH}	I _{OH} = -100 μA	1.8	V _{CC} - 0.2	_	V
с . с				I _{OH} = –6 mA	1.8	1.4	_	
Low-Level Output Voltage		Mai		I _{OL} = 100 μA	1.8	_	0.2	
		V _{OL}	$V_{IN} = V_{IL}$ $I_{OL} = 6 \text{ mA}$		1.8	_	0.3	
Input Leakage Current		l _{IN}	V _{IN} = 0~3.6 V		1.8	_	±5.0	μA
Power-off Leakage Current		IOFF	V _{IN} , V _{OUT} = 0~3.6 V		0	_	10.0	μA
Quiescent Supply Current		loo	$V_{IN} = V_{CC}$ or G	V _{IN} = V _{CC} or GND		_	20.0	μA
Quiescent Supply Cul		Icc	$V_{CC} \leq (V_{IN}, V_O)$	UT)≦3.6 V	1.8		±20.0	μΑ

AC Electrical Characteristics (Ta = -40~85°C, input $t_r = t_f = 2.0 \text{ ns}$, $C_L = 30 \text{ pF}$, $R_L = 500 \Omega$)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
	t		1.8	1.0	8.6	
Propagation delay time	t _{pLH}	(Figure 1 and 2)	2.5 ± 0.2	0.8	4.3	ns
	tpHL		$\textbf{3.3}\pm\textbf{0.3}$	0.6	4.0	

For $C_L = 50 \text{ pF}$, add approximately 300 ps to the AC maximum specification.

Dynamic Switching Characteristics (Ta = 25° C, input: t_r = t_f = 2.0 ns, C_L = 30 pF)

Characteristics	Symbol	Test Condition			Тур.	Unit
				$V_{CC}(V)$		
		$V_{IN} = 1.8 V, V_{IL} = 0 V$	(Note 10)	1.8	0.25	
Quiet output maximum dynamic $~V_{OL}$	V _{OLP}	$V_{IN} = 2.5 \text{ V}, \text{ V}_{IL} = 0 \text{ V}$	(Note 10)	2.5	0.6	ns
		$V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	(Note 10)	3.3	0.8	
		$V_{IN} = 1.8 V, V_{IL} = 0 V$	(Note 10)	1.8	-0.25	
Quiet output minimum dynamic $~V_{OL}$	V _{OLV}	$V_{IN} = 2.5 \text{ V}, \text{ V}_{IL} = 0 \text{ V}$	(Note 10)	2.5	-0.6	ns
		$V_{IN} = 3.3 \text{ V}, V_{IL} = 0 \text{ V}$	(Note 10)	3.3	-0.8	
		$V_{IN} = 1.8 V, V_{IL} = 0 V$	(Note 10)	1.8	1.5	
Quiet output minimum dynamic $~V_{OH}$	V _{OHV}	$V_{IN} = 2.5 \text{ V}, \text{ V}_{IL} = 0 \text{ V}$	(Note 10)	2.5	1.9	ns
		$V_{IN} = 3.3 \text{ V}, \text{ V}_{IL} = 0 \text{ V}$	(Note 10)	3.3	2.2	

Note 10: Parameter guaranteed by design.

Capacitive Characteristics (Ta = 25°C)

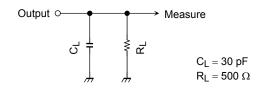
Characteristics	Symbol	Test Condition		TYP.	Unit	
Characteristics	Cymbol	Test Condition			V _{CC} (V)	onit
Input Capacitance	C _{IN}	—		1.8, 2.5, 3.3	4	pF
Power Dissipation Capacitance	C _{PD}	f _{IN} = 10 MHz	(Note 11)	1.8, 2.5, 3.3	27	pF

Note 11: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

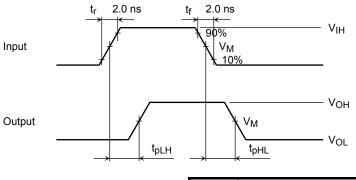
 $I_{CC \text{ (opr.)}} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/2$

TOSHIBA AC Test Circuit

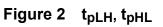




AC Waveforms



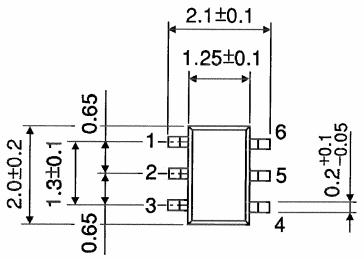
Symbol		V _{CC}	
Symbol	$3.3\pm0.3\;V$	$2.5\pm0.2\;V$	1.8 V
VIH	2.7 V	V _{CC}	V _{CC}
VM	1.5 V	V _{CC} /2	V _{CC} /2

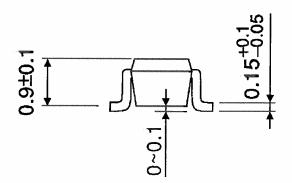


TOSHIBA

Package Dimensions

SSOP6-P-0.65A





Weight: 0.0068 g (typ.)

Unit: mm

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20070701-EN GENERAL

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