

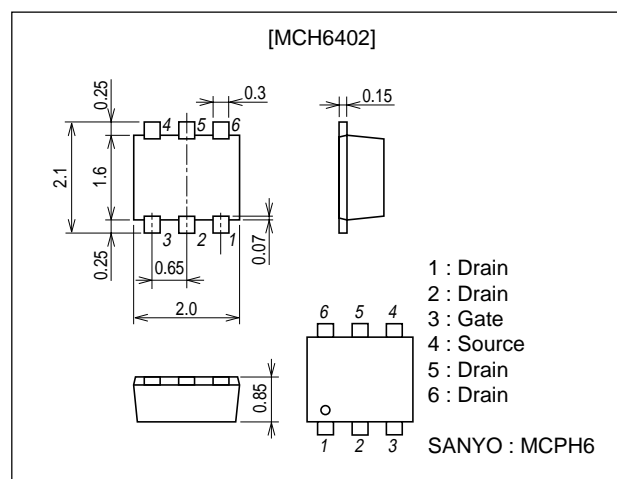
**SANYO****Ultrahigh-Speed Switching Applications****Features**

- Low ON-resistance.
- Ultrahigh-speed switching.
- 4V drive.

**Package Dimensions**

unit : mm

2193A

**Specifications****Absolute Maximum Ratings** at  $T_a=25^\circ\text{C}$ 

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	$V_{DS}$		30	V
Gate-to-Source Voltage	$V_{GS}$		$\pm 20$	V
Drain Current (DC)	$I_D$		4	A
Drain Current (Pulse)	$I_{DP}$	$PW \leq 10\mu\text{s}$ , duty cycle $\leq 1\%$	16	A
Allowable Power Dissipation	$P_D$	Mounted on a ceramic board (900mm <sup>2</sup> ×0.8mm)	1.5	W
Channel Temperature	$T_{ch}$		150	$^\circ\text{C}$
Storage Temperature	$T_{stg}$		-55 to +150	$^\circ\text{C}$

**Electrical Characteristics** at  $T_a=25^\circ\text{C}$ 

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D=1\text{mA}$ , $V_{GS}=0$	30			V
Zero-Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}=30\text{V}$ , $V_{GS}=0$			1	$\mu\text{A}$
Gate-to-Source Leakage Current	$I_{GSS}$	$V_{GS}=\pm 16\text{V}$ , $V_{DS}=0$			$\pm 10$	$\mu\text{A}$
Cutoff Voltage	$V_{GS(off)}$	$V_{DS}=10\text{V}$ , $I_D=1\text{mA}$	1.0		2.4	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS}=10\text{V}$ , $I_D=2\text{A}$	2.4	3.5		S
Static Drain-to-Source On-State Resistance	$R_{DS(on)1}$	$I_D=2\text{A}$ , $V_{GS}=10\text{V}$		50	65	$\text{m}\Omega$
	$R_{DS(on)2}$	$I_D=1\text{A}$ , $V_{GS}=4\text{V}$		100	140	$\text{m}\Omega$

Marking : KB

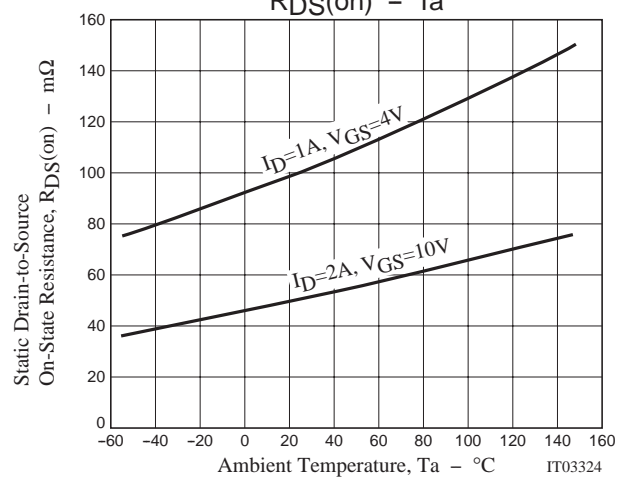
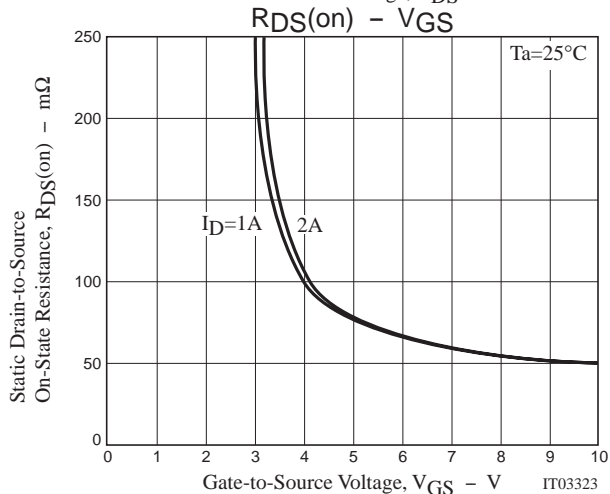
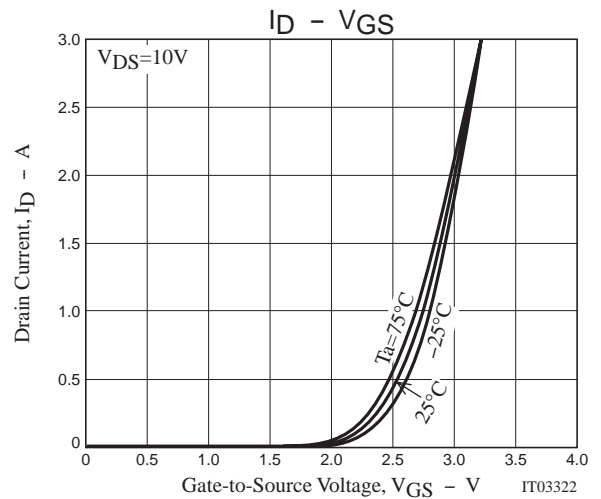
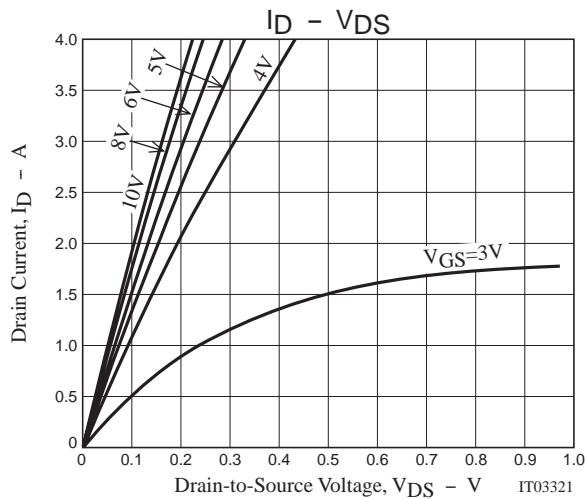
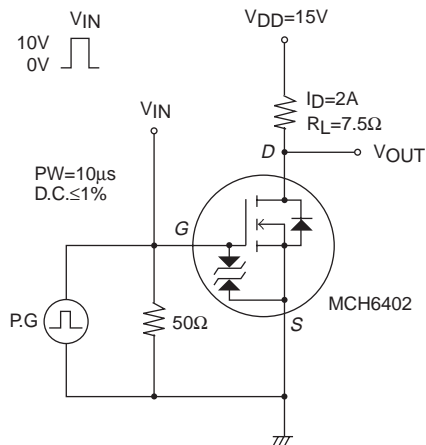
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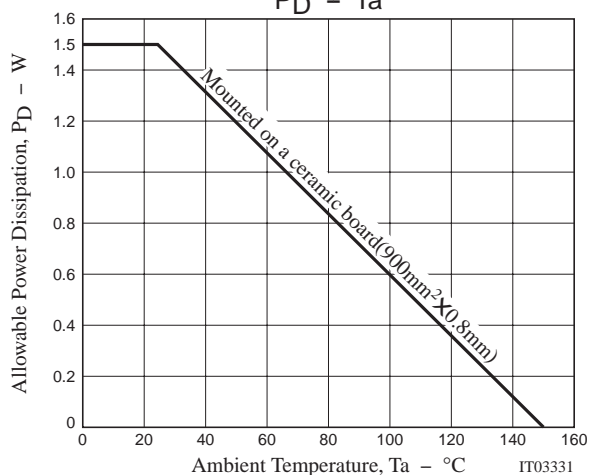
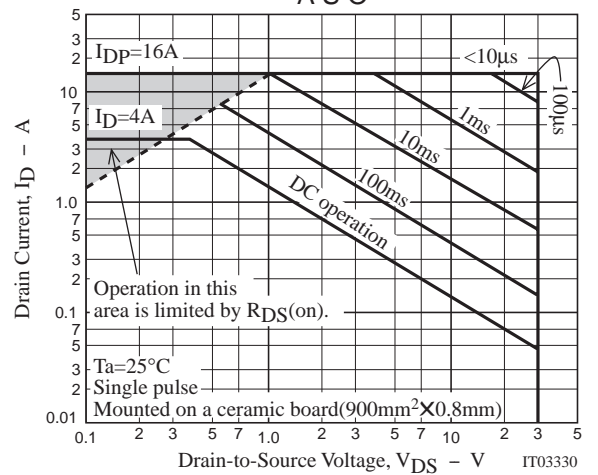
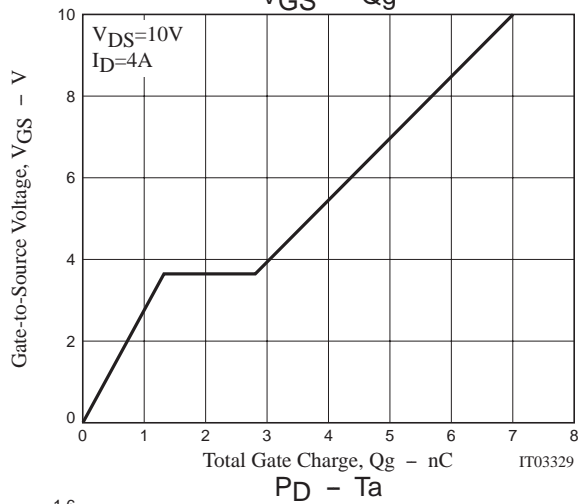
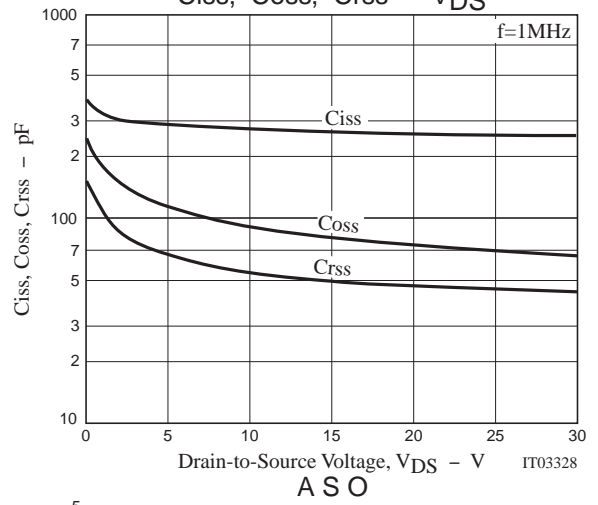
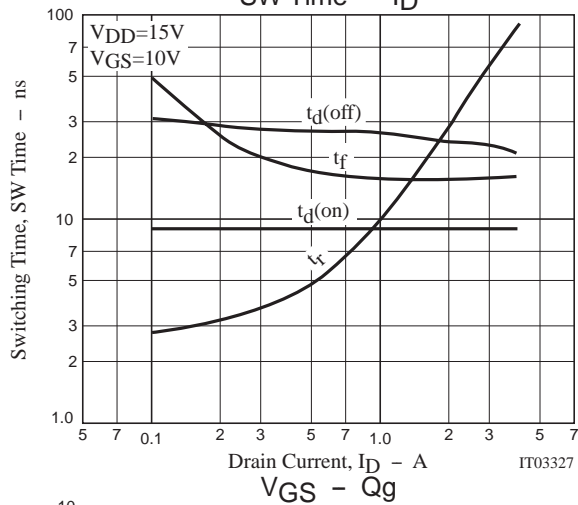
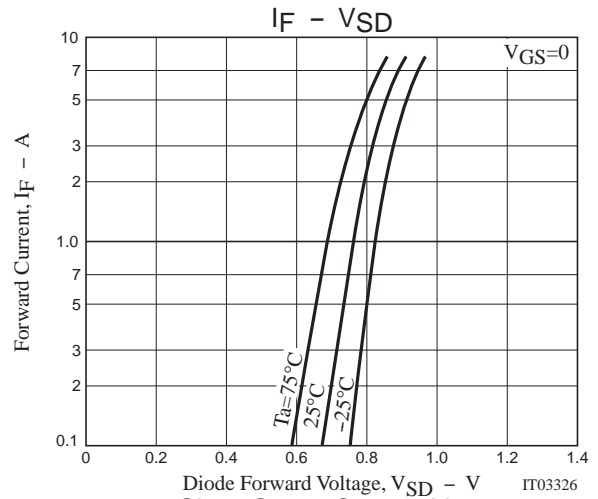
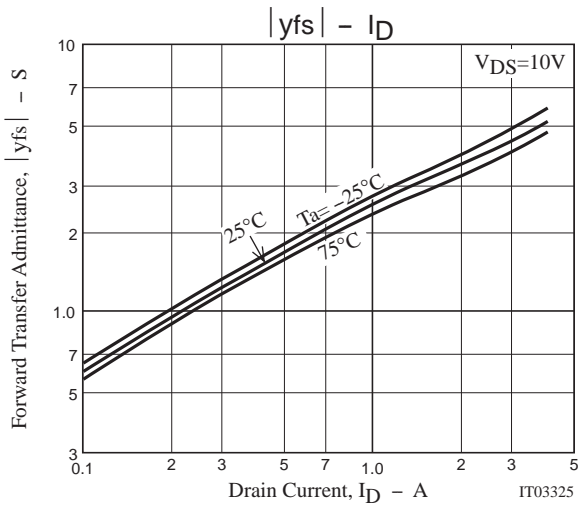
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Input Capacitance	$C_{iss}$	$V_{DS}=10V, f=1MHz$		270		pF
Output Capacitance	$C_{oss}$	$V_{DS}=10V, f=1MHz$		90		pF
Reverse Transfer Capacitance	$C_{rss}$	$V_{DS}=10V, f=1MHz$		55		pF
Turn-ON Delay Time	$t_d(on)$	See specified Test Circuit		9		ns
Rise Time	$t_r$	See specified Test Circuit		28		ns
Turn-OFF Delay Time	$t_d(off)$	See specified Test Circuit		24		ns
Fall Time	$t_f$	See specified Test Circuit		16		ns
Total Gate Charge	$Q_g$	$V_{DS}=10V, V_{GS}=10V, I_D=4A$		7		nC
Gate-to-Source Charge	$Q_{gs}$	$V_{DS}=10V, V_{GS}=10V, I_D=4A$		1.3		nC
Gate-to-Drain "Miller" Charge	$Q_{gd}$	$V_{DS}=10V, V_{GS}=10V, I_D=4A$		1.5		nC
Diode Forward Voltage	$V_{SD}$	$I_S=4A, V_{GS}=0$		0.84	1.2	V

### Switching Time Test Circuit





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